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Back-Contact Perovskite Solar Cell Modules Fabricated via Roll-to-Roll Slot-Die Coating: Scale-Up toward Manufacturing

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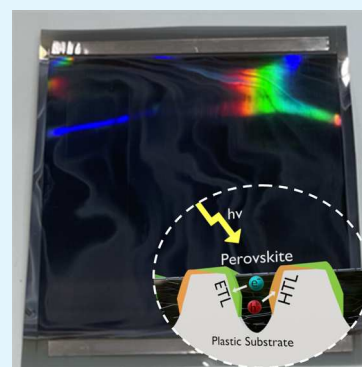
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ABSTRACT: We fabricate a type of back-contact perovskite solar cell based on 1.5 μm -width grooves that are embossed into a plastic film whose opposing “walls” are selectively coated with either n- or p-type contacts. A perovskite precursor solution is then deposited into the grooves, creating individual photovoltaic devices. Each groove device is series-connected to its neighbors, creating minimodules consisting of hundreds of connected grooves. Here, we report on the fabrication of groove-based devices using slot-die coating to deposit the perovskite precursor and explore the structure of the perovskite in the grooves using a range of microscopy and spectroscopy techniques. Significantly, our devices do not contain any expensive or scarce elements such as indium, indicating that this technology is both sustainable and low-cost. Furthermore, all coating processes explored here were performed using roll-to-roll processing techniques. Our technology is therefore completely scalable and is consistent with high-throughput, low-cost manufacturing.

KEYWORDS: *Perovskite, solar cell, back-contact, scaled, roll-to-roll, commercialization*



INTRODUCTION

Solar cells based on metal-halide perovskites have undergone rapid development in recent years, with their power conversion efficiencies (PCE) increasing from 3.8% to over 26%.¹ Such high efficiencies are the result of low exciton binding energies, long charge-carrier diffusion lengths and band gaps that are tunable to near IR wavelengths.^{2–4} As perovskites have intrinsically low materials processing costs, perovskite solar cells (PSCs) now appear to be a potential future competitor to devices based on silicon.^{4,5}

Almost all PSC devices reported are based on a planar layer-by-layer architecture that is deposited on a transparent conductive oxide (TCO) coated substrate. While such devices can have very high efficiencies, they also have limitations. For example, parasitic optical absorption within the charge transporting and contact layers can reduce the maximum attainable short-circuit current (J_{sc}). Each layer in a multilayer stack must also be deposited from a solvent that is orthogonal to the preceding layers to avoid their resolubilization; this can limit the range of solvents available to process devices and reduce the range of processing conditions that can be used. Planar architecture PSCs typically also include TCOs such as Indium Tin Oxide (ITO) as their light-facing electrode. Although ITO combines high conductivity and transparency, supplies of ITO are at increasing risk due to the growing

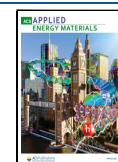
scarcity of indium and current world geopolitics.^{6,7} We note that indium is primarily extracted as a byproduct of zinc mining and is currently valued at around \$670/kg, compared to \$16/kg for nickel, another commonly used conductor. If the demand for indium exceeds that of zinc, it will be necessary to directly mine indium; a process that is less economically viable and is likely to further inflate materials costs.^{8,9} Indeed, if PSC production were to increase to a point where the global photovoltaic fleet provided terawatts of power per year, the annual production of indium would need to increase by 200%.¹⁰ Though alternatives to ITO exist and have been used in solar-cell devices (such as fluorine-doped tin oxide [FTO]), these materials often require high deposition temperatures, making them incompatible with deposition onto flexible plastic substrates. High process temperatures also increase both production time and cost.¹¹ For such reasons, there is

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significant interest in the development of solar cell technologies that do not rely on a steady supply of indium.

Back-contact solar cells represent an alternative to traditional planar architecture devices. In general, back-contact devices have all electrode and transport layers positioned at the rear of the device, with the active layer at the front of the device thus directly absorbing incident light. This type of structure has the advantage of potentially eliminating parasitic optical absorbance from device contacts and charge transport layers. Back-contact architectures were initially developed for silicon solar cells in the 1970s and were first used in perovskite solar cells in 2016, with a PCE of 6.5% reported.^{12,13} Here, devices were composed of “fingers” of a hole transport layer that were electrically isolated from a planar electron transport layer, with a perovskite coating the whole structure. In the following years, several back-contact PSC architectures have been demonstrated, including interdigitated, quasi-interdigitated, honeycomb and groove.^{13–16}

A key issue in the development of back-contact PSCs is the necessity to physically separate electron and hole transport layers by a distance that is commensurate with charge-carrier diffusion lengths, typically a few hundred nanometers in perovskites.^{17–20} If charge contacts are separated by a distance greater than this, device efficiency is reduced due to charge-carrier recombination losses.²¹ Thus, back-contact perovskite devices will require the use of submicron scale lithography techniques to define charge contacts, with this process being scalable for high-volume manufacture. Several lithography techniques have so far been explored; for example, the use of a self-assembly technique has been used to produce a honeycomb structured back-contact PSC in which the distance between each pore was approximately 7 μm . This design allowed devices to be created having champion PCEs of 11.2%.²² Further progress was made by the same group using cracked film lithography, where a lift-off technique was used to fill the cracks in a tin oxide film with nickel oxide and an insulator. When used in a back-contact configuration, PSCs were realized with reverse sweep efficiencies of over 6%.²³ In the same year, Deng et al. used lithography with polystyrene microspheres adsorbed onto a tin oxide film to template a honeycomb structure that could then be used as an evaporation mask for an insulating layer and a top electrode. This method produced back-contact devices with a PCE of 8.9%.²⁴ Of the various commonly used deposition techniques used to create back-contact devices, only one has been reported that involves the use of a truly scalable process (slot-die coating) with devices fabricated having PCEs of 0.18%.²⁵

In 2019, we reported a novel back-contact architecture based on a V-shaped microgroove structure.¹⁶ In brief, 1.6 μm wide grooves were embossed into a plastic film and had electrodes and transport layers coated onto their opposing walls, with a perovskite then coated over the film surface. Using this structure, single groove efficiencies were demonstrated with a PCE of 7.03%, and serially connected micromodules comprising 16 grooves achieved a PCE of 2.63%.

In this work, we make a significant advance on our previous work and demonstrate a significant scale-up of our technology, creating micromodules composed of up to 362 serially connected grooves. Such devices also have significantly enhanced performance, realizing stabilized PCEs of up to 12.8%. This enhancement in performance has resulted from a detailed device optimization program in which a large parameter space relating to both the preparation of the

patterned substrate and the techniques used to deposit the perovskite has been explored.

Here, we discuss the use of a range of techniques to explore the structure and operation of our devices to verify the structure of the electrodes and charge-transport layers deposited onto the opposing groove walls, including Scanning Electron Microscopy (SEM) and nanofocus X-ray fluorescence (XRF) mapping. This latter technique, together with Atomic Force Microscopy (AFM) allows us to characterize the crystallinity, topography, and size of perovskite grains within the grooves. We also explore photocurrent generation via photocurrent mapping and fluorescence decay lifetime measurements and use this to understand the functionality of the devices. Critically, we also show that the flexible, back-contact perovskite solar module devices we create can be fabricated using fully scalable, roll-to-roll deposition processes. Significantly, all techniques used in the processing of groove-based modules, including embossing, are fully compatible with upscaled manufacture, with the titanium and nickel metal contacts used here being significantly cheaper than Indium.

RESULTS AND DISCUSSION

Groove Structure and Fabrication. The structure of the devices explored here is schematically illustrated in Figure 1d. Briefly, fabrication begins with a PET: acrylic roll onto which a series of 1.5 μm -width grooves are embossed using a UV-curable acrylic. As we describe below, the back-contact architecture developed is based on coating opposing groove walls with electron- or hole-selective contacts. Here, the effective collection of charges without significant recombination effects requires the physical separation of the opposing contacts to be commensurate with typical carrier diffusion lengths. In the perovskite used in our devices (MAPbI_3), a range of carrier diffusion lengths have been reported, ranging from 0.1 to 2 μm , with this length being dependent on process conditions.^{26–28} In an ideal structure, the widths of the grooves would match carrier diffusion lengths, however, creating submicron features over large areas is challenging. We have therefore adopted a groove width of around 1.5 μm ; empirically we find this both results in efficient charge-carrier extraction and allows grooves to be reliably embossed over large areas having a consistent width and feature size.

The embossing process was undertaken at high volume under a commercial contract. In contrast to our previous work in which we used substrates embossed with a V-shaped groove, here we use grooves that have a U-shaped cross-section.¹⁶ This change in groove structure was based on the finding that U-shaped grooves are generally easier to emboss with high fidelity. This embossed roll was then loaded into a vacuum deposition chamber on a spool and was passed at a web speed of 5–15 mm/s in front of a series of deposition sources. Here, the coating selectivity of either wall was achieved using a directional deposition technique as shown in Figure 1b,c. To do this, an evaporation source was oriented at an oblique angle to the surface. This resulted in a “self-shading” effect that allowed each side of the groove walls to be coated by a contact material without coating the opposing wall, as previously reported.¹⁶ When selecting contact materials, it was necessary to identify materials having suitable electronic properties that could be deposited by either electron-beam (e-beam) or thermal evaporation. Attention was also paid to materials that were known to form stable contacts with perovskites, had low cost (to be compatible with a high-throughput manufacturing

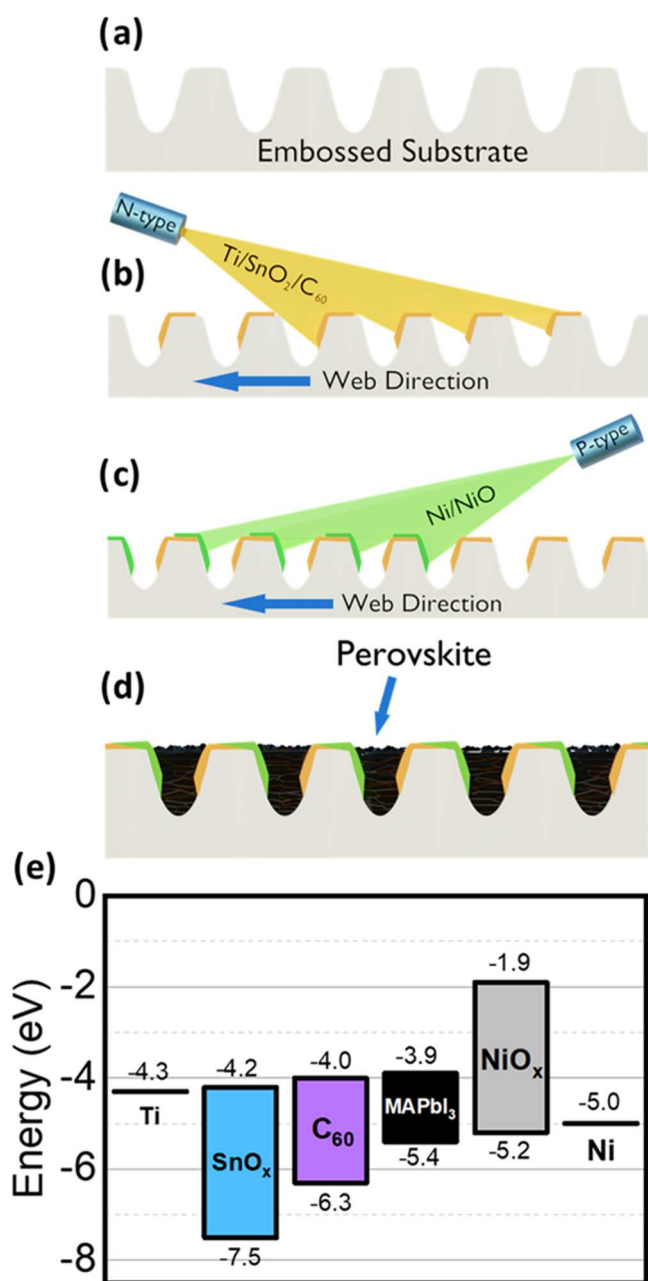


Figure 1. Schematic of the manufacture of a groove cascade showing (a) groove embossed into PET: acrylic substrate; (b, c) directional evaporation demonstrating the “self-shading” technique; (d) cross-section of complete groove after perovskite deposition; (e) Band diagram built from UPS, XPS and UV–visible spectrum data in S3, S4 and S5.

process), and did not require any high-temperature processes to be compatible with plastic substrates. For these reasons, we avoided commonly used contact materials such as gold, silver and ITO due to their cost, stability and processing requirements, respectively.

The n-type (electron extracting) contact deposited was composed of a Ti/SnO₂/C₆₀ multilayer, with the p-type (hole extracting) contact composed of Ni/NiO. All metals and metal oxides were deposited using e-beam evaporation with the SnO₂ and NiO deposited by a reactive deposition in which a low partial pressure of oxygen was maintained in the chamber to oxidize the deposited material.^{29,30} The n-type contact also

incorporated a C₆₀ layer deposited by thermal evaporation to enhance electron extraction. Here, an additional SnO₂ layer was included (which was not present in our previous work) to prevent undesirable reactions between titanium and C₆₀, with these reactions occurring at low temperature and in the solid state.³¹ Such reactions have been demonstrated to form amorphous titanium carbide which can result in the passivation of metals against corrosion, thus likely fouling metal electrodes in electronic devices causing increased series resistance.³³

As can be seen in Figure 1c, there is an overlap between the metal contacts of neighboring grooves at the apex of each groove. This creates a serial electrical connection between the grooves; a feature that—as we show below—allows us to create discrete minimodules that we term “cascades”. In the experiments described below, we have explored cascades composed of either 50 or 362 series-connected grooves; with the number of grooves in a cascade defined by the layout of the pattern initially embossed onto the plastic roll. Note that the substrate was also patterned with so-called “delineation features”. These consist of a set of deeper grooves that separate cascades without producing any photocurrent (as investigated below). We have chosen to emboss cascades containing relatively large numbers of grooves (up to 362), as this allows us to maximize the geometric fill factor - i.e., the relative area of the module that can produce a photocurrent.

We have performed extensive optimization experiments to enhance device efficiency; for the preparation of the substrate, this involved exploring the effect of deposition angle, partial pressure of oxygen in the deposition chamber during reactive depositions (affecting the degree of oxidation), individual layer thicknesses and the use of initial plasma-treatments on the embossed substrate. This optimization process and its effect on device efficiency is summarized in Supporting Information Figure S1. The approximate thicknesses of the various charge-transport and extraction layers in optimized devices are detailed in Table 1 and have been estimated via cross-sectional

Table 1. Approximate Layer Thicknesses in Champion Groove Devices, Estimated from SEM Cross-Sectional Imaging

material	approximate thickness (nm)
Ni	35
NiO _x	25
Ti	55
SnO ₂	25
C ₆₀	20

microscopy as presented later. As a result of the geometry of the deposition process, the deposition angle and the shape of the groove, the thickness of each individual evaporated layer is often found to vary as a function of depth into the groove.

The final step in the fabrication process involved the deposition of the perovskite MAPbI₃ (where MA refers to methylammonium, CH₃NH₃⁺) from an acetonitrile (ACN) solvent.³² This system was chosen as it could be converted to a perovskite with only short and low-temperature annealing. This was required to minimize possible warping or melting of the plastic substrate.

The perovskite precursor was deposited by slot-die coating. This is a scalable technique that has recently been used to fabricate fully roll-to-roll coated, conventional architecture flexible PSC modules, having a PCE of 11.0%.³⁴ In our

experiments, the substrate was translated relative to the slot-die head at a web speed of 1 m/min. A typical coating run involved the deposition of perovskite over a 3 m length of substrate, creating around 4000 individual device cascades. The coated substrate was then heated to 85 °C using an in-line oven and finally respoled without any further processing being applied. Here, device efficiency was improved by extensive optimization studies that explored effects such as web speed, ink flow rate, solution concentration, the effect of temperature, and the use of gas quench as summarized in Figure S1.

The complete fabrication process is shown schematically in Figure 2a. Figure 2b shows an image of part of the coated web; here the grooves that are embossed into the surface are evident via the colorful optical interference of the reflected light. For comparison, structures on smaller sections of the substrate were also fabricated from the same precursor by spin-coating.

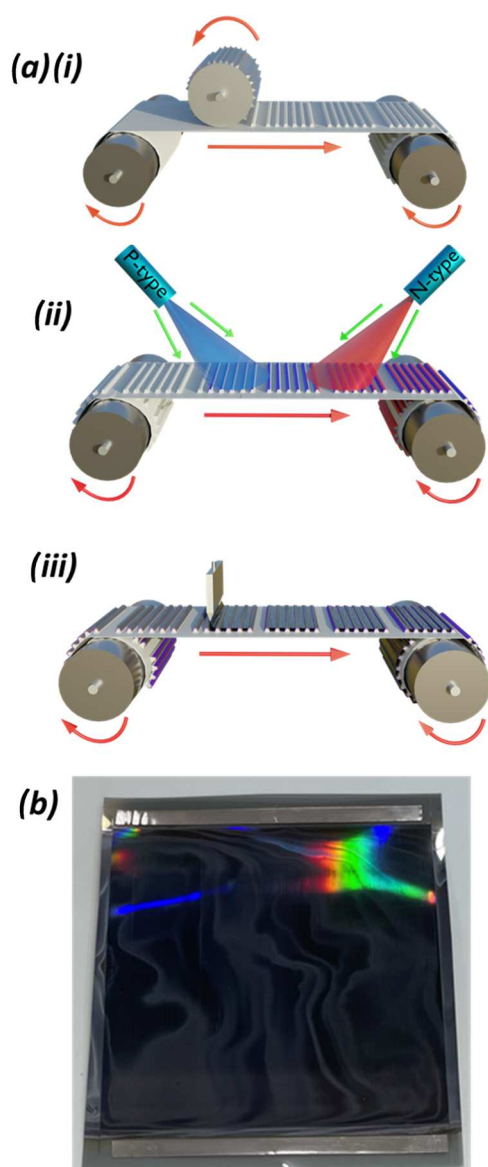


Figure 2. (a) Schematic of roll-to-roll processing of flexible groove-based perovskite solar cells showing roll-to-roll (i) embossing, (ii) evaporation of transport layers, and (iii) slot-die coating of perovskite (groove numbers and dimensions not to scale); (b) photograph of a roughly 10×10 cm unit of grooves from which test samples are cut.

Owing to the relatively sensitive nature of the embossed polymeric substrate, our perovskite precursor formulation has been optimized around a short, low-temperature anneal. The absorption of a typical MAPbI₃ film spin-cast on a quartz substrate is shown in Figure S2. In all cases, the resultant devices had the following architecture: Ti/SnO₂/C₆₀/MAPbI₃/NiO/Ni (see Figure 1d). Grazing incidence wide-angle X-ray Scattering (GIWAXS) measurements (see Figure S3) confirmed the presence of a perovskite structure within the grooves with no large-scale directionality observed. X-ray diffraction (XRD) measurements also demonstrate that the crystalline structure of spin-cast and slot-die coated MAPbI₃ are very similar (See Figure S4).

Figure 1e shows a band diagram across a single groove device, with data plotted for NiO, SnO₂, C₆₀ and MAPbI₃. Here, Ultraviolet Photoelectron Spectroscopy (UPS) measurements (recorded using UV HeI photons), allowed us to calculate the valence-band edge from the work-function of the different materials via the measured electron binding energies. By constructing a Tauc plot from optical absorption measurements, we were also able to calculate the optical bandgaps of SnO₂ and NiO, with a value for MAPbI₃ taken from Noel et al.³² This process is described in more detail in methods with data summarized in Figures S5 to S7. It can be seen in Figure 1e that the conduction band of MAPbI₃ at -4.0 eV is well matched with the work function of Ti and the conduction bands of SnO₂ and C₆₀, being -4.3 , -4.2 , and -4.0 eV respectively, suggesting facile electron extraction. We also observe a 0.4 eV difference between the valence band of MAPbI₃ and the work function of Ni. However, the conduction band offset between MAPbI₃ and NiO is significantly larger than the valence band offset of MAPbI₃ and C₆₀, implying that NiO is more effective at blocking electrons than C₆₀ is at blocking holes.

Characterizing the Device Structure. In order to optimize the efficiency of the groove-based perovskite solar cells, we have explored the crystallinity, topography and chemical identity of the constituent materials. Gaining a full understanding of device morphology over length scales commensurate with charge-diffusion lengths is critical in order to identify structures that may result in performance losses. Figure 3a shows an SEM image recorded in cross-section through a typical series of grooves. For completeness, SEM images of a series of unfilled grooves are shown in Figure S8. As discussed above, grooves were embossed with a “U-shaped” cross-section, which is clearly visible in the cross-sectional SEM. Here, we can individually identify and quantify the thicknesses of the Ti, SnO₂, C₆₀, Ni and NiO layers, as listed in Table 1. It is clear that each wall of the groove is coated with a different series of materials that overlap at the apex of the grooves. However, identifying the chemical composition and distribution along the grooves is impossible from the SEM images alone.

As can be seen in Figure 3a the perovskite (visible as a light-gray region) largely fills the grooves, although a small void is often observed at the bottom of the grooves. We believe that such voids likely result from trapped solvent remaining after precursor deposition or partial dewetting of the ink from the bare substrate at the bottom of the groove. A small amount of perovskite material can be seen at the apex of some grooves in the top-down SEM and AFM images shown in Figure 3b,c respectively. These can be a source of performance loss in the devices as this overburden can create shunting pathways

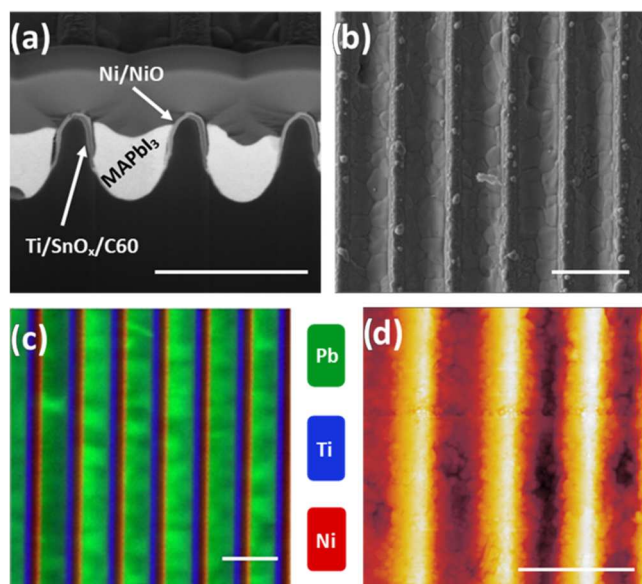


Figure 3. (a) FIB-SEM cross-section of groove device showing multiple grooves in series; (b) top-down SEM of five grooves in series; (c) X-ray fluorescence map of a fully roll-to-roll coated groove cascade with lead, titanium and nickel shown in green, blue and red respectively; (d) AFM of four grooves in series (not matching areas). All scale bars 2 μm .

between adjacent grooves, resulting in reduced module performance. We have therefore optimized our coating process to minimize the volume of material deposited at the groove apex; such optimization required control over solution concentration, coating speed and slot-die coating (slot-die head geometry, solution flow rate, environmental conditions, etc.).

Even without perovskite being present, the distribution of the transport layers cannot be elucidated by SEM alone. To explore the distribution of the various metals that form the contacts, we have used nanofocus X-ray Fluorescence (XRF) mapping to characterize devices at various stages in their fabrication. The nanofocus technique involves using an X-ray beam generated by a synchrotron (Diamond Light Source station I14) which is focused to a 50 nm diameter spot. The beam spot was then scanned across the surface with the emitted X-rays dispersed using a spectrometer to determine which elements are present from their characteristic X-ray emission energies. This technique produces high-resolution maps of the relative chemical composition of the film at 50 nm resolution, bypassing the optical diffraction limit.

A typical scanning XRF image of a series of perovskite-filled grooves is shown in Figure 3c, with the distribution of Ni, Ti and Pb plotted using red, blue, and green respectively. Images of each material individually deposited onto the grooves are shown in Figures S9 and S10. We find, as anticipated, the n- and p-type layers are clearly defined, being coated onto opposing walls with no evidence of any contact between these layers at the bottom of the groove (which might otherwise provide shorting pathways). From the image, we determine an approximate separation between n- and p-type contacts at the surface of around 950 nm. Note that the tin signal is generally weaker than that of the other elements and has significant spectral overlap with Ti emission so is not clearly distinguishable (Figure S9), nevertheless, the presence of SnO₂ can be confirmed from the cross-sectional SEM images. The

elemental distribution across the substrate has also been confirmed using EDX SEM as shown in Figures S13 and S14.

The scanning XRF measurements allow us to gain some insight into the structure of the perovskite within the grooves. It is apparent that there is some fluctuation in the magnitude of the lead signal over length scales of around a micron. Interestingly, we also find that the average lead signal varies *between* neighboring grooves; a result that suggests that some grooves have a thicker perovskite layer (i.e., deeper fill into the groove) than others. From the above-discussed SEM images, we find that the surface-level of the perovskite does not vary appreciably across the grooves, suggesting that some of the local variations in the lead and iodine signals may instead result from variations in the size and shape of subsurface voids located at the bottom of the grooves. The origin of the variation of the size and distribution of such voids is currently not understood; they may result from slight differences in the depth or dimensions of the grooves or local variations in the distribution of the charge-extraction contacts which result in enhanced local dewetting. We also find that the lead signal can vary by a factor of 2 across the width of one groove (Figure S11); a result suggesting that the perovskite is thicker toward the nickel-coated walls. We speculate that this may result from improved wetting of the precursor on the NiO surface compared to the opposing C₆₀-coated contact. We also find fluctuations in the relative ratio of lead and iodine in the perovskite across the surface (Figure S12) suggesting that certain areas are lead-rich—possibly resulting from local regions of PbI₂. Interestingly, in samples in which the perovskite precursor was spin-cast, these regions seem to be more localized to specific grooves; an effect that may result from variations in solvent drying dynamics between the two methods. Note however, that we do not find evidence for systematic changes in the stoichiometry of the perovskite across the width of the grooves when deposited roll-to-roll via slot-die coating (Figure S11).

As was evident from the top-down SEM images, we again clearly observe perovskite grain structure in our AFM measurements, as shown in Figure 3d. We can postprocess such images to more clearly identify grain boundaries as shown in Figure S15. From this, we determine that typical grains have a mean size of 114 nm. This compares with the mean grain size of control perovskite films that were spin-cast onto a quartz-glass surface of 105 nm. We note that the average separation between the walls of the grooves (and thus the charge-extraction contacts) is around 950 nm. This distance is greater than the thickness of the perovskite active layer in a conventional-architecture solar cell (which is typically 300–600 nm), and thus we expect that photogenerated charges within each groove will likely have to traverse a number of grain boundaries before being extracted at a device contact. Recombination at grain boundaries may therefore act as a loss mechanism within our devices.

Charge Separation and Generation of Photocurrent.

Having validated the distribution of the charge transport and contact materials and the polycrystalline nature of the perovskite, we now explore the ability of such devices to generate and extract a photocurrent. Here, we first demonstrate the function of transport materials to act as charge extraction layers via characterizing photoluminescence decay lifetime of the MAPbI₃ perovskite when spin-coated on a series of different representative substrates using Time-Correlated Single-Photon Counting (TCSPC). This is shown

in Figure 4a where we present example decay curves of perovskite deposited into grooves where only one of the walls

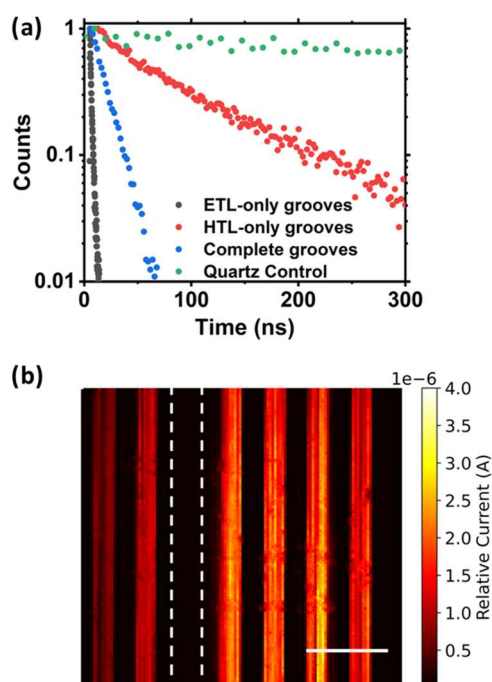


Figure 4. (a) TCSPC decay curves of MAPbI₃ on a quartz substrate and grooves featuring one or both walls coated, recorded using Time-Correlated Single-Photon Counting. (b) Photocurrent map showing current generated from a series of device cascades. Position of shunted cascade indicated by white dashed lines. Scale bar 500 μm.

was coated with a contact material. These substrates are designed to explore the efficiency by which each of the different charge-transport layers are able to extract charge-carriers from the device. For completeness, the photoluminescence decay of the perovskite spin-coated on a quartz substrate is also shown. The data presented in Figure 4a was taken from part of a larger data set with measurements made at a series of different points across each substrate. We show histograms of these decay lifetimes across 10 μm × 10 μm areas in Figure S16, with data also recorded on quartz substrates (Figures S17 and S18). Our analysis of this data set indicates median decay lifetimes of 1.0, 72, and 14 ns for the perovskite deposited on ETL-only (C₆₀/SnO₂), HTL-only (NiO/Ni), and grooves with both walls coated respectively. This compares to the median lifetime of 1252 ns of an identical MAPbI₃ film spin-coated onto a quartz substrate without any transport layers.

Clearly, therefore, the different transport layers modify charge recombination mechanics in different ways. As the photoluminescence intensity is proportional to the local electron and hole density, we associate a reduction in decay lifetime with an increase in the nonradiative recombination rates. Indeed, we suspect that the SnO₂/C₆₀ ETL interface is characterized by a series of defects which enhance nonradiative recombination. The observed reduction in the emission lifetime of the perovskite in single-carrier devices can therefore be attributed to increased nonradiative recombination around the perovskite/transport layer interface. Interestingly, the observed decay lifetime of the structure containing both ETL and HTL contacts is intermediate between those with either

ETL or HTL alone. We propose therefore that the built-in field in the full device helps to reduce nonradiative recombination by moving holes away from the ETL/perovskite interface.

As discussed above, the series-connected grooves embossed onto the substrate form modules which we term cascades. Between each adjacent cascade, we also emboss a series of deeper, wider grooves, known collectively as “delineation features”. The added depth of these features ensures that transport layers are not in contact with perovskite and can thus electrically isolate each cascade from those adjacent. Extending these delineation features to cap the ends of a cascade of grooves aids to further direct extraction of electrons and holes in a direction perpendicular to the cascades. This also allows cascades to be connected in parallel simply through the use of conductive tracks at opposite sides of the substrate. This allowed simultaneous connection to be made to either the n- or p-type side of the cascades. A schematic of a typical layout of the cascades allowing their parallel connection is shown in Figure S19.

To evidence photovoltaic activity in parallel connected cascades, we have recorded maps of photocurrent generation following optical excitation at 637 nm using compressive sensing current mapping. In this technique, a series of different pixel patterns having varying spatial frequency are projected at the cascades via an optical microscope.^{35,36} Here, the photocurrent generated by the cascades is recorded for each projected pattern, and by postprocessing the data, it is possible to reconstruct the spatial distribution of photocurrent generated across the surface. A typical image of the photocurrent generated across a series of 50-groove cascades is shown in Figure 4b. Due to the limited resolution of the optical microscope, we do not clearly resolve individual grooves within each cascade. However, we find a relatively uniform photocurrent generated across individual cascades, indicating a comparable level of performance between the individual cascades. Interestingly, we find that one cascade generated no measurable current (white dashed line). This implies the cascade was shunted, which could be caused by poor electrical connection to the cascade during measurement, or by significant shorting pathways from overfilled grooves. Nevertheless, it is clear that the failure of this one cascade does not affect the performance of neighboring cascades, with such parallel connected cascades forming separate PV minimodules.

Electronic Characterization of Devices. Having explored the physical structure of the groove devices and their ability to generate and extract a photocurrent, we now discuss the performance of our devices and focus our attention on devices fabricated by slot die coating, although devices fabricated by spin coating have very similar performance. For testing purposes, the coated roll shown in Figure 2b was first cut into small sections of around 1.5 cm × 1.5 cm. Each cut section typically contained around 20 cascades, with each cascade comprising 362 serial connected grooves. The act of cutting sections from the substrate surface meant that the cascades were no longer connected in parallel at the edges of the substrate, but instead were electrically isolated from each other by the delineation features. This allowed the JV characteristics of individual cascades to be recorded using a probe station with alignment of the probes at opposite sides of the cascade performed using a microscope. No aperture mask was required in this measurement, as our previous work has shown this is unnecessary when illuminating these devices. Figure S20 shows an image of a section of coated substrate

together with a schematic indicating how different areas are selected for study. In our calculation of PCE, we first calculate the active area of each cascade from the known width of the individual grooves ($1.5 \mu\text{m}$), the number of grooves in each cascade (e.g., 362) and their length as measured using callipers (typically 1.5 cm). This gives a typical active area of around 0.0815 cm^2 . Note, we also assume a geometric fill factor of 82% to account for the area of the cascade in which current is generated, discounting the area at the apex of each groove. When we include this geometric fill factor, we determine a typical active area for each cascade of 0.0689 cm^2 .

Devices were tested under 1 sun using a calibrated AAA solar simulator, with light directed to the grooves through the transparent plastic substrate (i.e., the solar simulator was placed underneath the probe station with light projected upward). Interestingly, we find that a photocurrent could be generated whether the device is illuminated from above or through the substrate, with illumination from beneath resulting in higher recorded efficiencies. This indicates that groove devices can be regarded as a bifacial technology.

Figure 5a is a box plot of device metrics recorded from 23 individual cascades. We find that cascades had mean values of PCE, J_{sc} , V_{oc} and fill factor of 6.7%, 0.033 mA/cm^2 , 359 V and 55.7% respectively. Here the champion cascade had a PCE, J_{sc} , V_{oc} and fill factor of 11.3%, 0.056 mA/cm^2 , 373 V and 54.2% respectively. A JV curve of a typical cascade is shown in Figure 5b. As these modules consisted of 362 grooves, short-circuit currents and open circuit voltages can be scaled by this value to represent a comparable value to flat devices. In doing this, we find typical cascades have equivalent J_{sc} and V_{oc} values of 11.9 mA/cm^2 and 0.99 V , with champion values of 20.3 mA/cm^2 and 1.03 V determined. We can in fact compare the performance of groove cascades with that of regular planar architecture perovskite PV devices having a similar composition in terms of layer structure (Figure S21). Here, we find that p-i-n and n-i-p devices with the same transport layers and perovskite with no annealing have PCEs of 11.3 and 16.3% respectively. Note that the efficiency of these controls are similar to the anticipated performance of MAPbI_3 prepared from an ACN precursor which has not been subject to thermal annealing (around 17%).²⁹

Figure 5d plots the stabilized efficiency of a champion cascade (362 grooves) which reaches an efficiency of 12.8% after 7 min of testing. This performance should be compared with our previous work in which we demonstrated a PCE of 7.03% for a single groove, and 2.63% for a mini-module consisting of 16 grooves. The significant increase in performance for these up-scaled grooves results from the extensive optimization experiments performed over a period of 1 year in which around 10,000 individual cascades were fabricated and tested.

In Figure 5c we plot the external quantum efficiency of a series of cascades. It can be seen that as expected the device response falls to zero at the perovskite band-edge around 820 nm . Note the relatively low EQE of the cascades results from the fact that the short-circuit current output from a module is relatively small as discussed above. We note that if we multiply the peak EQE (0.118%) by the number of grooves in the cascade (362), we obtain a peak EQE value of 42.7% ; a value in reasonable agreement with what can be expected from a conventional architecture perovskite solar cell. We can also use our EQE measurement to calculate integrated photocurrent across the visible spectrum and determine an integrated short-

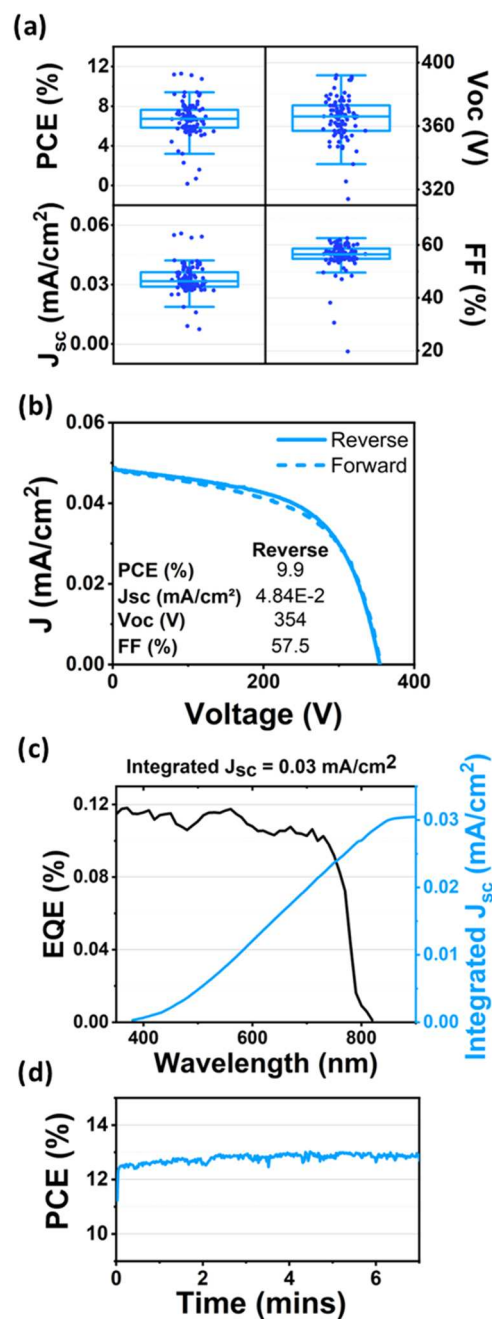


Figure 5. (a) Box plots of performance metrics for fully roll-to-roll evaporated and coated groove devices; (b) example $J-V$ curve of fully roll-to-roll coated cascade with metrics inset; (c) external quantum efficiency of representative groove device; (d) stabilized efficiency of champion cascade.

circuit current to 0.03 mA/cm^2 ; a value in good agreement with the average short-circuit current measured from the JV studies reported in Figure 5a.

We have also briefly investigated the shelf life stability of spin-coated cascades (see Figure S22). Here, substrates were stored without encapsulation under nitrogen for a period of 37 days and tested periodically. We observed that in 50-groove cascades, PCE increased considerably in the first few days after fabrication, with the champion cascade reaching its peak efficiency of 10.9% (average 9.9%) at day 5. After 14 and 37 days, the efficiency of cascades had stabilized, demonstrating an average PCE of 7.3%. It was found that V_{oc} remained

relatively constant throughout the testing period, with J_{sc} and FF undergoing a variation that accounted for the observed changes in efficiency.

DISCUSSION

We have shown that back-contact groove-based modules represent a new approach to the high-volume manufacture of perovskite solar cells. Devices are made using a directional vapor-based coating technique applied to an embossed substrate that creates micron-width, electrically separated n- and p-type contacts with high fidelity. Each of the coating and patterning processes used have been individually performed using a roll-to-roll based process, with the MAPbI₃ perovskite deposited grooves by slot-die coating. We have characterized the structure of our devices using a range of microscopy and spectroscopy techniques. It is apparent that some compositional and ink fill inhomogeneity is present within individual grooves and from groove-to-groove which we believe may result from nonuniform drying dynamics of the precursor ink, or differences in the wettability of the n- and p-type contact materials. Significantly, we often identify the presence of submicron-sized voids at the bottom of the grooves. The effect of such voids is not currently understood, though as charge extraction occurs in a direction parallel to the substrate, such voids may not have any noticeable effect on device performance. This is clearly an open question and is ripe for further studies. We show that the average size of the perovskite grains is smaller than the width of the grooves, indicating that extracted charges likely have to traverse one or more grain boundaries. It is possible that recombination at such grain boundaries is one factor that limits device performance, however it may be possible to reduce such effects via appropriate passivation strategies.

We note that device efficiency is likely to be dependent on the size and shape of the grooves, with narrower grooves expected to reduce the length of charge-carrier extraction pathways and therefore help to suppress charge-carrier recombination. Deeper grooves that are fully filled with perovskite are also expected to enhance light absorption and therefore increase short-circuit current. However, the fabrication of very narrow and deep grooves using embossing techniques is not without its challenges, as is completely filling high aspect-ratio grooves with a perovskite. Future work will address these issues.

It is clear that the primary loss mechanism in the devices studied results from a relatively low fill factor. We suspect that the primary contribution to this effect results from a series resistance created at the electronic contact between adjacent grooves. It is possible that this electronic loss can be mitigated by modifying the structure and layout of the grooves to enhance the contact area between adjacent grooves and thereby improve the fill factor.

Measurements of radiative emission lifetime indicate that decay lifetimes are reduced significantly when the perovskite is placed in contact with the SnO₂/C₆₀ electron extracting contact, compared to the NiO/Ni contact alone. This suggests that there may be a population of defects or trap-states at the interface between the SnO₂/C₆₀ contact and the perovskite that enhance nonradiative recombination. Future work will explore the development of material systems that reduce such nonradiative recombination, with this likely to result in improvements in device module efficiency.

We have used photocurrent mapping techniques to explore the uniformity of photocurrent generated by parallel connected minimodules, each composed of 362 groove cascades. Here we occasionally find that cascades appear to generate no measurable current, indicating that they are at short circuit. The origin of such effects is not understood, though this does not affect adjacent cascades and indicates that significant improvements in overall module efficiency can be expected via improving the homogeneity of the perovskite deposition process.

Finally, further work will investigate the effect of extended exposure to the combined effects of moisture, light and heat. Critically, the demonstration of stable devices will require the development of an encapsulation system appropriate for our device architecture. This is likely to be rather different from the encapsulation processes that are used to protect regular architecture perovskite devices in which the active layer is located beneath charge-transporting layers and metal contacts, which both provide some degree of environmental protection. As the perovskite in groove devices is exposed, care will be required in choosing materials appropriate for encapsulation to avoid unwanted chemical reactions.

CONCLUSIONS

In summary, we have explored the upscaling via slot-die coating of a novel MAPbI₃ perovskite back-contact solar-cell technology and have shown that cascades of serially connected grooves can be created having a stabilized efficiency of up to 12.8%. We believe this demonstration of flexible back-contact perovskite solar cell modules manufactured by fully scalable processes that only contains low-cost Earth-abundant materials represents a very promising step toward the commercialization of this technology.

ASSOCIATED CONTENT

Supporting Information

The Supporting Information is available free of charge at <https://pubs.acs.org/doi/10.1021/acsaem.4c02734>.

Methods and materials, supporting data (GIWAXS, UV-vis, XRD, Tauc plots, SEM, XRF, AFM, TCSPC, and device layout schematics) (PDF)

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Notes

The authors declare the following competing financial interest(s): D.G.L. is co-director of the company Ossila Ltd that retails materials used in photovoltaic research. PowerRoll Ltd hold the following patents relating to the substrates used in this work: Canada 2840327, China, 201180071841.6, 201480006419.6, Europe 2724380, EP 2951866, USA 14/264670, 14/764599, South Africa 2014/00531, Brazil BR1120150175007, Hong Kong 16103709.8, India 2812/KOLNP/2015, Japan 2015-555797, UK GB2487419.

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