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# Resource Allocation in Backscatter-Assisted Wireless Powered MEC Networks with Limited MEC Computation Capacity

Yinghui Ye, Liqin Shi, Xiaoli Chu, *Senior Member, IEEE*, Rose Qingyang Hu, *Fellow, IEEE*, and Guangyue Lu

**Abstract**—In this paper, we consider a backscatter-assisted wireless powered mobile edge computing (MEC) network, where multiple Internet-of-Things (IoT) nodes harvest energy from the energy signals transmitted by a power beacon (PB) and utilize the harvested energy for local computing and task offloading via hybrid backscatter communication (BackCom) and active transmission (AT). Considering the limited computation capacity of the MEC server and the quality-of-service (QoS) and energy-causality constraints per IoT node, we propose two resource allocation schemes to maximize the total computation bits of all the IoT nodes and the system computation energy efficiency (EE), respectively, by jointly optimizing the computation frequency and time of the MEC server and each IoT node, the transmit power of the PB and each IoT node, and the BackCom power reflection coefficient and the time for energy harvesting (EH), BackCom, and AT of each IoT node. The non-convex computation bits maximization problem is transformed to a convex one by introducing a series of auxiliary variables and proof by contradiction, and then solved by the existing convex tools. The system computation EE maximization is a non-convex nonlinear programming problem. We propose a two-layer iterative algorithm to solve it optimally and devise a reduced-complexity iterative algorithm to solve it sub-optimally by leveraging the block coordinate decent technique. Computer simulations validate the convergence of the proposed iterative algorithms and their superior performance over the benchmark schemes in terms of the computation bits or EE.

**Index Terms**—Backscatter communications, computation bits, computation energy efficiency, energy harvesting, mobile edge computing.

## I. INTRODUCTION

THE Internet of Things (IoT) will play an important role in future intelligent services. However, owing to the cost limitation, IoT devices are usually energy- and computation-constrained, making it very challenging for them to timely han-

dle computation-intensive tasks [1]. To address this challenge, wireless powered mobile edge computing (WPMEC), which integrates wireless power transfer (WPT) [2] with mobile edge computing (MEC) [3]–[5], has been proposed as a promising solution. The key idea of WPMEC is to let IoT devices harvest energy from radio frequency (RF) signals transmitted by a dedicated energy source, e.g., a power beacon (PB), and then utilize the harvested energy to support the local processing and/or task offloading of tasks [1], [6].

Resource allocation schemes based on binary or partial computation offloading have been proposed for WPMEC. In binary computation offloading, an IoT node's task is either executed locally or completely offloaded to an MEC server [3], while in partial computation offloading, a portion of the IoT node's task is offloaded to the MEC server and the rest is computed locally [5]. In [6], the authors maximized the computation success probability by jointly optimizing the binary computation offloading decision, the user's computation frequency and the time allocation for energy harvesting (EH) and task offloading under the energy-causality and delay constraints. In [7], the total computation rate of all the users in a multi-user WPMEC network was maximized by jointly optimizing user's binary computation offloading decision, computation frequency and time allocation for EH and task offloading. In [8], an online algorithm, which jointly selects the binary computation offloading mode and adjusts the time resource for WPT and task offloading according to the time-varying channel, was proposed to maximize the weighted sum-computation rate of all the IoT nodes in a multi-user WPMEC network. Under partial computation offloading, the weighted sum-computation bits of all the IoT nodes were maximized and the PB's energy consumption was minimized respectively in [9] and [10], by jointly optimizing the PB's beamforming and the IoT nodes' computation frequency, task bits for offloading, and offloading time. In [11], the authors maximized the computation energy efficiency (EE) of all the IoT nodes in a WPMEC network by jointly optimizing the transmit power of the PB and each IoT node, and each IoT node's computation frequency and time for task offloading and EH. In [12], a resource allocation scheme was proposed for cooperation-assisted WPMEC, where the IoT node close to the AP servers as relay, to minimize the PB's energy consumption by jointly optimizing the time for EH and cooperative data offloading, and the transmit power of each IoT node.

In the above works [6]–[12], the IoT nodes offload task bits to the MEC server following the harvest-then-transmit

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protocol via active transmission (AT), which enjoys a high transmission rate for data offloading but at the cost of a high power consumption [13]. Under the energy-causality constraint, each IoT node has to be allocated with a long period for harvesting sufficient energy to support AT, leaving only a short period for AT offloading within each time block. This will limit their task offloading performance. Different from AT, backscatter communication (BackCom) allows an IoT node to modulate and reflect the incident signals by adjusting the antenna load impedance instead of generating RF signals by itself [14]. Such an approach avoids the use of active components, resulting in a much lower power consumption as well as a lower offloading rate than AT. Accordingly, AT and BackCom have different tradeoffs<sup>1</sup> between data transmission and power consumption [15], [16], which can be exploited to achieve efficient task offloading in WPMEC. Driven by this, hybrid BackCom-AT has been employed for computation offloading in WPMEC [17].

Hybrid BackCom-AT offloading brings new challenges in resource allocation as AT offloading and BackCom offloading share the same pool of resources within each time block and the optimization of resource allocation needs to consider the tradeoff between them. In [18], under a complete offloading strategy that offloads all task bits of an IoT node to the MEC server, the authors minimized the energy consumption of the PB by jointly optimizing the IoT nodes' transmit power for AT, and their time for BackCom offloading and AT offloading. Under partial computation offloading, the total computation bits of all the IoT nodes were maximized by jointly optimizing the BackCom power reflection coefficient, transmit power for AT, BackCom and AT offloading time, local computation frequency and computation time of each IoT node [19]. Under the same setting as in [19], a max-min computation EE problem and a computation EE maximization problem were studied in [20] and [21], respectively. In [22], to minimize the total delay for tasks offloading and computing of all the IoT nodes, the authors jointly optimized the transmission time of the PB, and each IoT node's transmit power for AT, and their portions of task bits for BackCom offloading, AT offloading and local computing.

We note that the MEC server's energy consumption and computation time and practically limited computation capacity have not been considered in the existing works on hybrid BackCom-AT WPMEC networks [18]–[22]. However, the computation time of the MEC server may not be negligible for ultra-delay-sensitive applications [5]. Besides, assuming a sufficiently high computation frequency of the MEC server, thus ignoring its computation time, makes it impossible to determine the computation energy consumption of the MEC server, which however should be considered when designing energy efficient resource allocation schemes for MEC networks [5], [23]. Please note that considering the limited computation capacity for the MEC server introduces additional variables, e.g., the MEC server's computation frequency and time, which are potentially coupled in the optimization of

resource, thus, bringing new challenges in resource allocation.

In this paper, we design two resource allocation schemes for a hybrid BackCom-AT WPMEC network, where a PB and an MEC server are deployed close to multiple IoT nodes to provide them with energy and computational services on demand, in order to maximize the total computation bits of all the IoT nodes and the system computation EE, respectively, while considering the MEC server's energy consumption and computation time and capacity. The main contributions are summarized as follows.

- Considering the computation resource allocation at the MEC server and partial computation offloading at each IoT node, we maximize the sum computation bits of all the IoT nodes by jointly optimizing the transmit power of the PB, the MEC server's computation frequency and time, and each IoT node's local computation frequency and time, EH time, BackCom time, AT time, BackCom power reflection coefficient and AT transmit power, subject to the quality-of-service (QoS) and other necessary constraints. This joint optimization is formulated into a non-convex problem. By using the proof by contradiction and introducing a series of auxiliary variables, the original problem is transformed into a convex one and is then solved by the convex tools.
- The system computation EE is defined as the ratio of the total computation bits of all the IoT nodes to the total energy consumption of the PB, MEC server and all the IoT nodes in the system. The system computation EE maximization problem is a non-convex fractional programming problem of the same optimization variables as in the sum computation bits maximization problem. To solve it, based on the Dinkelbach's method and the bisection method, we first propose a two-layer iterative algorithm to obtain the optimal resource allocation scheme. By leveraging the block coordinate decent (BCD) technique, we decompose the original problem into two subproblems, i.e., 1) given the transmit power of the PB, jointly optimizing all the other optimization variables, and 2) optimizing the transmit power of the PB while all the other variables are given. We further develop a reduced-complexity iterative algorithm to a suboptimal resource allocation.
- Our analytical and numerical results show that under the limited computation capacity of the MEC server, each IoT node should always perform local computing throughout each time block in order to achieve the maximum total computation bits or system computation EE; the total computation bits are maximized when the MEC server always uses the maximum allowed computing frequency to complete their computing tasks; the system computation EE maximization is maximized when the available time in each time block is used up for BackCom/AT offloading, tasks computing at the IoT node and the MEC server, while the computing frequency of the MEC server is jointly optimized with its computing time and the offloaded task bits; the proposed total computation bits/EE maximization schemes outperform the bench-

<sup>1</sup>The offloading rate of AT is higher than that of BackCom, but the energy consumption of BackCom is much lower than that of AT [15].

mark schemes in terms of computation bits/EE and the proposed reduced-complexity iterative algorithm achieves the suboptimal performance.

The rest of the paper is organized as follows. In Section II, we introduce the system model and analyze the working flow. Section III studies the computation bits maximization problem. In Section IV, the system computation EE maximization problem is studied and two iterative algorithms are developed to obtain the optimal and suboptimal solutions, respectively. Simulation results and the conclusion are given in Sections V and VI, respectively.

In the existing works on backscatter-assisted wireless powered networks, the sum throughput [16], [24] and the EE [25], [26] have been maximized by optimizing the allocation of communication resources between the BackCom and AT under the energy-causality constraint of each IoT node. Our work is different in that we consider not only the tradeoff between the BackCom and AT for task offloading, but also that between the different computation capacities of the IoT nodes and the MEC server.

## II. SYSTEM MODEL AND WORKING FLOW

As shown in Fig. 1, we consider a hybrid BackCom-AT WPMEC network that consists of one PB,  $K$  IoT nodes and one MEC server. Each node is equipped with a single antenna and works in the half-duplex mode. The  $K$  IoT nodes harvest energy from the RF signals emitted by the PB and use the harvested energy to offload their tasks to the MEC server as well as compute tasks locally. In order to avoid consuming the battery power and thereby to prolong the operation time of each IoT node, the energy consumed at each IoT node for computing and offloading their tasks is assumed to be less than or equal to its harvested energy in each time block  $T$ . Following [19]–[22], we assume that the bits of each task are bit-wise independent so that the partial offloading scheme can be applied at each IoT node. Each IoT node is equipped with separate circuits for backscattering, AT, EH, and computing, respectively, and thus can perform local computation when offloading tasks or harvesting energy. A quasi-static fading channel model is assumed, that is, all the channels remain static within one time block but may vary across adjacent time blocks. We also assume that perfect channel state information (CSI) is available at the MEC server for obtaining the performance bound. The MEC server can obtain the CSI of all links at the beginning of each transmission block by using channel estimation methods such as the least-square estimation [27], and exploiting the channel reciprocity, as detailed in Section II-E of [28].

Each time block  $T$  is divided into five phases, which are the EH phase, the BackCom phase, the AT phase, the task execution phase, and the downloading phase. In the EH phase, the PB broadcasts energy signals and each IoT node harvests energy from the received signals. In the BackCom phase and the AT phase, the  $K$  IoT nodes offload parts of their tasks to the MEC server following the time division multiple access (TDMA) protocol. Specifically, in the BackCom phase, the PB keeps broadcasting energy signals and the  $K$  IoT nodes take

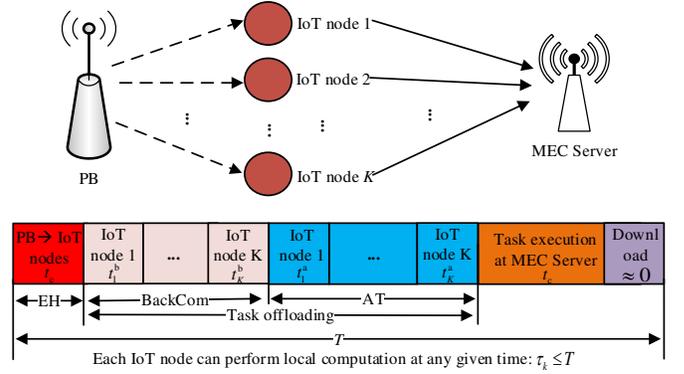


Fig. 1. Hybrid BackCom-AT WPMEC and its frame structure.

turns to offload task bits by modulating and backscattering the energy signals to the MEC server. In the AT phase, the PB keeps silent<sup>2</sup> [18]–[22] and the  $K$  IoT nodes take turns to transmit task bits to the MEC server using the harvested energy. In the task execution phase, the PB and all the IoT nodes keep silent, while the MEC server executes all the received computation tasks. The MEC server sends the computation results to the IoT nodes in the downloading phase. In this work, we consider the scenarios where the computation result bits are much less than the task bits, e.g., automatic manufacturing systems. Thus, the downloading phase is significantly shorter than the other four phases, and is ignored hereafter for simplicity.

### A. EH Phase

Let  $t_e$  denote the duration of the EH phase and  $P_t$  denote the transmit power of the PB, where  $0 < P_t \leq P_{\max}$  and  $P_{\max}$  is the maximum allowed transmission power at the PB. Then, the harvested energy of the  $k$ -th ( $k \in \mathcal{K} = \{1, 2, \dots, K\}$ ) IoT node during the EH phase can be computed as  $E_k^h = t_e \eta P_t g_k$ , where  $g_k$  is the channel power gain between the  $k$ -th IoT node and the PB,  $\eta$  ( $0 < \eta < 1$ ) denotes the energy conversion efficiency<sup>3</sup>.

### B. BackCom Phase

The BackCom phase is divided into  $K$  sub-phases. In the  $k$ -th sub-phase of duration  $t_k^b$ , the  $k$ -th IoT node offloads its task bits to the MEC server via BackCom while the others perform EH. The task bits offloaded by the  $k$ -th IoT node via BackCom are given by

$$R_k^b = t_k^b B \log_2 \left( 1 + \frac{\xi \rho_k P_t g_k h_k}{B \sigma^2} \right), \quad (1)$$

<sup>2</sup>Please note that our work can be extended to the scenario where the PB continues to broadcast energy signal in the AT phase, in which case, the IoT nodes can harvest more energy in each time block but the PB consumes more energy and the MEC server needs to remove the PB energy signal as interference.

<sup>3</sup>For analytical tractability, this paper considers the linear EH model. Please note that this work can be extended to the scenarios with a non-linear EH model by using the approach adopted in [15] or [29].

where  $B$  is the channel bandwidth,  $\rho_k$  ( $0 \leq \rho_k \leq 1$ ) denotes the power reflection coefficient of the  $k$ -th IoT node,  $h_k$  represents the channel power gain between the  $k$ -th IoT node and the MEC server,  $\sigma^2$  is the noise power spectral density, and  $\xi$  denotes the performance gap<sup>4</sup> between the BackCom capacity and the Shannon capacity.

From (1), we can see that the portion  $\rho_k P_t g_k$  of the received power is used for backscattering the  $k$ -th IoT node's task bits to the MEC server; while the rest of the received power,  $(1 - \rho_k) P_t g_k$ , is flowed into the EH circuit. During the sub-phase  $t_k^b$ , the harvested energy of the  $k$ -th IoT node and the  $i$ -th ( $i \in \mathcal{K} \setminus k$ ) IoT node can be, respectively, computed as

$$E_k^b = t_k^b \eta (1 - \rho_k) P_t g_k, \quad (2)$$

$$E_{k,i}^b = t_k^b \eta P_t g_i. \quad (3)$$

Accordingly, at the end of the BackCom phase, the total harvested energy of the  $k$ -th IoT node is determined by

$$\begin{aligned} E_k^{\text{total}} &= E_k^h + E_k^b + \sum_{i \in \mathcal{K}, i \neq k} E_{i,k}^b \\ &= \eta P_t g_k \left( t_e + \sum_{i \in \mathcal{K}, i \neq k} t_i^b \right) + \eta P_t g_k t_k^b (1 - \rho_k) \\ &= \eta P_t g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - \eta P_t g_k \rho_k t_k^b. \end{aligned} \quad (4)$$

The energy consumption of the  $k$ -th IoT node for BackCom is given by  $P_c t_k^b$ , where  $P_c$  denotes the circuit power consumption for BackCom [19]–[21].

### C. AT Phase

The AT phase is also divided into  $K$  subphases. In the  $k$ -th subphase of duration  $t_k^a$ , the  $k$ -th IoT node transmits its task bits to the MEC server. Letting  $p_k$  denote the transmit power of the  $k$ -th IoT node, the task bits offloaded by the  $k$ -th IoT node during  $t_k^a$  can be calculated as

$$R_k^a = t_k^a B \log_2 \left( 1 + \frac{p_k h_k}{B \sigma^2} \right). \quad (5)$$

Note that the value of  $p_k$  is limited by the total energy harvested by the  $k$ -th IoT node during the EH and BackCom phases minus the energy consumed for offloading and local computing so far.

The energy consumption of the  $k$ -th IoT node for offloading via AT can be calculated as  $(p_k + p_c) t_k^a$ , where  $p_c$  denotes the constant circuit power consumption for AT.

<sup>4</sup>During  $t_k^b$ , the received signal at the MEC server is given by  $\sqrt{\rho_k P_t g_k h_k} x_e x_k^b + \sqrt{P_0 g_T R} x_e + n$ , where  $x_e$ ,  $x_k^b$  and  $n$  are the energy signal transmitted by the PB, the offloaded data via BackCom and the additive Gaussian noise at the MEC server, respectively. Assuming that the signal received from the PB,  $\sqrt{P_0 g_T R} x_e$ , is removed by performing successive interference cancellation, as the PB's energy signal can be predefined and known by the MEC server [13]. The remaining signal for decoding  $x_k^b$  can be written as  $\sqrt{\rho_k P_t g_k h_k} x_e x_k^b + n$ . The Shannon capacity is for Gaussian inputs, but for BackCom, the multiplicative signal  $x_e x_k^b$  does not necessarily follow the Gaussian distribution and thus a performance gap exists between the Shannon and BackCom capacities. In this work, we use  $\xi$  ( $0 < \xi < 0$ ) to represent this performance gap following [15], [16], [19].

At the end of the AT phase, the total task bits offloaded by all the IoT nodes are given as

$$\begin{aligned} R_{\text{sum}}^o &= \sum_{k=1}^K (R_k^b + R_k^a) \\ &= \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi \rho_k P_t g_k h_k}{B \sigma^2} \right) + t_k^a B \log_2 \left( 1 + \frac{p_k h_k}{B \sigma^2} \right) \right). \end{aligned} \quad (6)$$

### D. Task Execution Phase

After successfully receiving the task bits offloaded by the IoT nodes, the MEC server starts to execute the received tasks.

Let  $f_m$  (in Hz unit) and  $t_c$  (in s unit) denote the computation frequency and time of the MEC server, respectively. The MEC server's computation capacity can be computed as

$$R_m = \frac{t_c f_m}{C_{\text{cpu}}}, \quad (7)$$

where  $C_{\text{cpu}}$  represents the number of CPU cycles required for computing one bit at the MEC server.

The number of task bits computed by the MEC server is determined by the minimum between the received task bits and the computation capacity of the MEC server, i.e.,

$$\begin{aligned} R_m^e &= \min \{ R_{\text{sum}}^o, R_m \} \\ &= \min \left\{ \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi \rho_k P_t g_k h_k}{B \sigma^2} \right) + t_k^a B \log_2 \left( 1 + \frac{p_k h_k}{B \sigma^2} \right) \right), \frac{t_c f_m}{C_{\text{cpu}}} \right\}. \end{aligned} \quad (8)$$

Letting  $\varepsilon_m$  denote the effective capacitance coefficient of the processor chip at the MEC server, the energy consumption of the MEC server for task executing is given by [30]

$$E_m^e = \varepsilon_m f_m^3 t_c. \quad (9)$$

### E. Local Computation

At any time during a time block, each IoT node can compute its own tasks locally. Letting  $f_k$  (in Hz unit) and  $\tau_k$  (in s unit) denote the computation frequency and time of the  $k$ -th IoT node, respectively, the task bits computed locally by the  $k$ -th IoT node are given by

$$R_k^e = \frac{\tau_k f_k}{C_{\text{cpu},k}}, \quad (10)$$

where  $C_{\text{cpu},k}$  denotes the number of CPU cycles required for computing one bit at the  $k$ -th IoT node.

The consumed energy for local computation at the  $k$ -th IoT node is given by

$$E_k^e = \varepsilon_k f_k^3 \tau_k, \quad (11)$$

where  $\varepsilon_k$  is the effective capacitance coefficient of the processor chip at the  $k$ -th IoT node. Note that the values of  $E_k^e$  and  $\tau_k$  are limited by the harvested energy minus the energy consumed for offloading by the  $k$ -th IoT node.

### III. COMPUTATION BITS MAXIMIZATION

In this section, we design an optimal resource allocation scheme to maximize the total computation bits for the hybrid BackCom-AT WPMEC network by jointly optimizing the transmit power of the PB  $P_t$ , the EH time  $t_e$ , the MEC server's computation frequency  $f_m$  and time  $t_c$ , and the IoT nodes' BackCom time vector  $\mathbf{t}^b = [t_1^b, \dots, t_K^b]$ , AT time vector  $\mathbf{t}^a = [t_1^a, \dots, t_K^a]$ , power reflection coefficient vector  $\boldsymbol{\rho} = [\rho_1, \dots, \rho_K]$ , transmit power vector  $\mathbf{p} = [p_1, \dots, p_K]$ , local computation frequency vector  $\mathbf{f} = [f_1, \dots, f_K]$  and time vector  $\boldsymbol{\tau} = [\tau_1, \dots, \tau_K]$ . Specifically, we formulate a computation bits maximization problem subject to the QoS, energy causality, latency, computational budget and transmit power constraints. Then, we transform the formulated problem into a convex one and obtain the optimal solution using the existing convex optimization tools.

#### A. Problem Formulation

The total computation bits include the task bits computed by both the MEC server and the IoT nodes, and are given by

$$R_{\text{total}}(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, \boldsymbol{\tau}, t_c, f_m) = R_m^e + \sum_{k=1}^K R_k^e, \quad (12)$$

where  $\mathbf{t}^b = [t_1^b, \dots, t_K^b]$ ,  $\mathbf{t}^a = [t_1^a, \dots, t_K^a]$ ,  $\boldsymbol{\rho} = [\rho_1, \dots, \rho_K]$ ,  $\mathbf{p} = [p_1, \dots, p_K]$ ,  $\mathbf{f} = [f_1, \dots, f_K]$  and  $\boldsymbol{\tau} = [\tau_1, \dots, \tau_K]$ . The maximization of the total computation bits should consider the following constraints.

1) *QoS Constraint*: Letting  $L_{\min,k}$  and  $\beta_k$  ( $0 \leq \beta_k \leq 1$ ),  $\forall k \in \mathcal{K}$ , denote the minimum required task bits and the portion of task bits offloaded to the MEC server for the  $k$ -th IoT node, respectively, the following three inequalities should be satisfied for each IoT node to guarantee that the minimum required task bits of each IoT node are computed successfully,

$$R_k^b + R_k^a \geq \beta_k L_{\min,k}, \quad \forall k, \quad (13)$$

$$\sum_{k=1}^K \beta_k L_{\min,k} \leq R_m, \quad (14)$$

$$R_k^e \geq (1 - \beta_k) L_{\min,k}, \quad \forall k, \quad (15)$$

where (13) and (14) jointly ensure that at least  $\beta_k L_{\min,k}$  task bits are offloaded to and computed successfully by the MEC server, while (15) ensures that at least  $(1 - \beta_k) L_{\min,k}$  task bits are computed successfully at the  $k$ -th IoT node.

2) *Energy-Causality Constraint*: To ensure that the consumed energy of each IoT node for offloading and computing is less than its harvested energy within each time block, the energy-causality constraint is expressed as

$$P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 \tau_k \leq E_k^{\text{total}}, \quad \forall k. \quad (16)$$

Based on (12)-(16), the computation bits maximization problem can be formulated as

$$\begin{aligned} \mathbf{P}_0 : \quad & \max_{P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, \boldsymbol{\tau}, t_c, f_m, \boldsymbol{\beta}} R_{\text{total}}(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, \boldsymbol{\tau}, t_c, f_m) \\ \text{s.t.} \quad & \text{C1 : (13) - (15),} \\ & \text{C2 : (16),} \\ & \text{C3 : } 0 \leq f_m \leq f_{\max}, 0 \leq f_k \leq f_k^{\max}, \quad \forall k, \\ & \text{C4 : } t_e + \sum_{k=1}^K (t_k^b + t_k^a) + t_c \leq T, t_e, t_k^b, t_k^a, t_c \geq 0, \quad \forall k, \\ & \text{C5 : } 0 \leq \tau_k \leq T, \quad \forall k, \\ & \text{C6 : } 0 < P_t \leq P_{\max}, p_k \geq 0, \quad \forall k, \\ & \text{C7 : } 0 \leq \rho_k \leq 1, \quad \forall k, \\ & \text{C8 : } 0 \leq \beta_k \leq 1, \quad \forall k, \end{aligned}$$

where  $\boldsymbol{\beta} = [\beta_1, \dots, \beta_K]$ , and  $f_k^{\max}$  and  $f_{\max}$  are the maximum computing frequencies for the  $k$ -th IoT node and the MEC server, respectively.

In  $\mathbf{P}_0$ , C1 is the QoS constraint, C2 is the energy-causality constraint. C3 constrains the maximum computing frequencies at the MEC server and IoT nodes, C4 and C5 are the latency constraints, which ensure that all the task bits should be executed within  $T$ , C6 and C7 are the constraints on the transmit power of the PB and the IoT nodes and the power reflection coefficient of each IoT node, respectively, and C8 constrains the portion of task bits offloaded to the MEC server for each IoT node. The transmit power and computation capacity constraints together with the minimum QoS requirement  $L_{\min,k}$  may cause an infeasibility issue, i.e., it may be impossible to find a solution to the formulated total computation bits maximization problem (as well as the system computation EE maximization problem in Section IV) under certain values of those constraints and  $L_{\min,k}$ . It is reasonable to assume that the probability of an infeasibility event is very low for practical settings of the maximum PB transmit power  $P_{\max}$  or the maximum computing frequencies of the IoT nodes and the MEC server.

It is observed that  $\mathbf{P}_0$  is a typical non-convex optimization problem since there are coupling relationships among several optimization variables (i.e.,  $P_t$ ,  $t_e$ ,  $\rho_k$  and  $t_k^b$ ,  $f_k$  and  $\tau_k$ ,  $f_m$  and  $t_c$ , etc.) in the objective function and constraints. We note that directly introducing auxiliary variables into  $\mathbf{P}_0$  may not decouple all the coupling optimization variables. Because there will always be at least one non-convex constraint remaining. In order to solve  $\mathbf{P}_0$ , we first derive the closed-form expressions for parts of the optimal solutions and then transform  $\mathbf{P}_0$  into a convex one by introducing auxiliary variables.

#### B. Solution

In order to remove the min function in the objective, we introduce a slack variable  $\lambda$  and let  $\lambda = \min\{R_{\text{sum}}^o, R_m\}$ . Accordingly, the optimization problem  $\mathbf{P}_0$  can be reformulated as

$$\begin{aligned} \mathbf{P}_1 : \quad & \max_{P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, \boldsymbol{\tau}, t_c, f_m, \beta, \lambda} \lambda + \sum_{k=1}^K R_k^e \\ \text{s.t.} \quad & \text{C1 - C8,} \\ & \text{C9 : } R_{\text{sum}}^o \geq \lambda, \\ & \text{C10 : } R_m \geq \lambda. \end{aligned}$$

As for  $\mathbf{P}_1$ , we provide the following proposition to determine the optimal transmit power of the PB  $P_t^*$ , the optimal

computation frequency of the MEC server  $f_m^*$  and the optimal computing time of the  $k$ -th IoT node  $\tau_k^*$ .

**Proposition 1:** For the considered network, the maximum total computation bits are achieved when the PB transmits energy signals with the maximum transmit power, the MEC server executes all the received tasks with its maximum computation frequency and each IoT node performs local computation throughout a time block, namely  $P_t^* = P_{\max}$ ,  $f_m^* = f_{\max}$  and  $\tau_k^* = T$ .

*Proof.* Please see Appendix A. ■

Substituting  $P_t = P_{\max}$ ,  $f_m = f_{\max}$  and  $\tau_k = T$  into  $\mathbf{P}_1$ , we have

$$\begin{aligned} \mathbf{P}_2 : \quad & \max_{t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, t_c, \beta, \lambda} \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu},k}} \\ \text{s.t. C1'} : & t_k^b B \log_2 \left( 1 + \frac{\xi \rho_k P_{\max} g_k h_k}{B \sigma^2} \right) + R_k^a \geq \beta_k L_{\min,k}, \forall k, \\ & \sum_{k=1}^K \beta_k L_{\min,k} \leq \frac{t_c f_{\max}}{C_{\text{cpu}}}, \\ & \frac{f_k T}{C_{\text{cpu},k}} \geq (1 - \beta_k) L_{\min,k}, \forall k, \\ \text{C2'} : & P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 T \\ & \leq \eta P_{\max} g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - t_k^b \eta \rho_k P_{\max} g_k, \forall k, \\ \text{C3'} : & 0 \leq f_k \leq f_k^{\max}, \forall k, \\ \text{C4, C7, C8,} \\ \text{C9'} : & \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi \rho_k P_{\max} g_k h_k}{B \sigma^2} \right) + R_k^a \right) \geq \lambda, \\ \text{C10'} : & \frac{t_c f_{\max}}{C_{\text{cpu}}} \geq \lambda. \end{aligned}$$

It can be observed that  $\mathbf{P}_2$  is still non-convex due to the non-convex constraints, C1', C2' and C9', which contain coupled variables, e.g.,  $t_k^b$  and  $\rho_k$ . To overcome the non-convexity, we introduce the auxiliary variables,  $x_k = \rho_k t_k^b$  and  $y_k = t_k^a p_k$ , and use  $\frac{x_k}{t_k^b}$  and  $\frac{y_k}{t_k^a}$  to replace  $\rho_k$  and  $p_k$ , respectively. Thus,  $\mathbf{P}_2$  is equivalently transformed into  $\mathbf{P}_3$ , as follows,

$$\begin{aligned} \mathbf{P}_3 : \quad & \max_{t_e, t^b, t^a, \mathbf{x}, \mathbf{y}, \mathbf{f}, t_c, \beta, \lambda} \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu},k}} \\ \text{s.t. C1''} : & t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_{\max} g_k h_k}{t_k^b B \sigma^2} \right) \\ & + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) \geq \beta_k L_{\min,k}, \forall k, \\ & \sum_{k=1}^K \beta_k L_{\min,k} \leq \frac{t_c f_{\max}}{C_{\text{cpu}}}, \\ & \frac{f_k T}{C_{\text{cpu},k}} \geq (1 - \beta_k) L_{\min,k}, \forall k, \\ \text{C2''} : & P_c t_k^b + y_k + p_c t_k^a + \varepsilon_k f_k^3 T \\ & \leq \eta P_{\max} g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - \eta x_k P_{\max} g_k, \forall k, \\ \text{C3', C4, C8, C10',} \\ \text{C7'} : & 0 \leq x_k \leq t_k^b, y_k \geq 0, \forall k, \\ \text{C9''} : & \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_{\max} g_k h_k}{t_k^b B \sigma^2} \right) \right. \\ & \left. + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) \right) \geq \lambda, \end{aligned}$$

where  $\mathbf{x} = [x_1, \dots, x_K]$  and  $\mathbf{y} = [y_1, \dots, y_K]$ .

**Proposition 2:**  $\mathbf{P}_3$  is convex and can be efficiently solved by using the convex optimization tool, e.g., CVX.

*Proof.* Please see Appendix B. ■

#### IV. SYSTEM COMPUTATION EE MAXIMIZATION

In this section, we maximize the system computation EE maximization for the hybrid BackCom-AT WPMEC system

under the QoS, energy causality, latency, computational budget and transmit power constraints, by jointly optimizing the same variables as of the computation bits maximization problem  $\mathbf{P}_0$ . The formulated joint optimization problem is highly non-convex and difficult to solve. We devise two iterative algorithms to solve it optimally and sub-optimally (with a reduced complexity), respectively.

##### A. Problem Formulation

Following [31], [32], the system computation EE for the considered network is defined as the ratio of the total computation bits of all the IoT nodes to the total energy consumption of the PB, MEC server and all the IoT nodes in the system. The total consumed energy consists of three parts: the consumed energy of the PB for wireless energy transfer, the energy consumption of all the IoT nodes for task offloading and computing, and the energy consumed at the MEC server. Accordingly, the total energy consumption can be computed as

$$\begin{aligned} E_{\text{total}} = & (P_t + P_{\text{sc}}) \underbrace{\left( t_e + \sum_{k=1}^K t_k^b \right)}_{\text{Part 1}} - \sum_{k=1}^K E_k^{\text{total}} + \underbrace{E_m^e}_{\text{Part 3}} \\ & + \underbrace{\sum_{k=1}^K (P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 \tau_k)}_{\text{Part 2}}, \end{aligned} \quad (17)$$

where  $P_{\text{sc}}$  denotes the constant circuit power consumption at the PB.

The computation EE of the system is given by

$$q_s(P_t, t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, \tau, t_c, f_m) = \frac{R_{\text{total}}}{E_{\text{total}}}. \quad (18)$$

Based on (18) and the QoS and energy-causality constraints in (13)-(16), the system computation EE maximization problem can be formulated as

$$\begin{aligned} \mathbf{P}_4 : \quad & \max_{P_t, t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, \tau, t_c, f_m, \beta} q_s(P_t, t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, \tau, t_c, f_m) \\ \text{s.t. C1 - C8.} \end{aligned}$$

Similar to  $\mathbf{P}_1$ ,  $\mathbf{P}_4$  is also highly non-convex and difficult to solve due to multiple coupled optimization variables in both the objective function and constraints, which can not be tackled by means of variable substitution. Besides,  $\mathbf{P}_4$  is more complicated than  $\mathbf{P}_1$  since the objective function of  $\mathbf{P}_4$  is a ratio of two functions. The following section is devoted to solving  $\mathbf{P}_4$ .

##### B. Solution and Iterative Algorithms

We provide the following proposition to determine the optimal computation time of each IoT node, as part of the optimal solution to  $\mathbf{P}_4$ .

**Proposition 3:** In order to achieve the maximum computation EE for the considered system, each IoT node performs local computation throughout a time block, i.e.,  $\tau_k^* = T$ .

*Proof.* Proposition 3 can be proven by using contradiction and the detailed process is similar to Appendix A. Thus, the proof is omitted here for brevity. ■

By substituting  $\tau_k = T$  into  $\mathbf{P}_4$ , we have

$$\begin{aligned} \mathbf{P}_5 : \quad & \max_{P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, T, t_c, f_m} q_s(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, T, t_c, f_m) \\ \text{s.t. } & \text{C1}^\dagger : (13), (14), \frac{f_k T}{C_{\text{cpu}, k}} \geq (1 - \beta_k) L_{\min, k}, \forall k, \\ & \text{C2}^\dagger : P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 T \\ & \leq \eta P_t g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - t_k^b \eta \rho_k P_t g_k, \forall k, \\ & \text{C3, C4, C6 - C8.} \end{aligned}$$

We can see that  $\mathbf{P}_5$  is still non-convex mainly because  $P_t$  is coupled with  $\rho_k$ ,  $t_k^b$  and  $t_e$ . In the following, we first solve  $\mathbf{P}_5$  for given  $P_t$ .

1) *Solving  $\mathbf{P}_5$  for given  $P_t$* : To remove the *min* function in (8) from the objective function of  $\mathbf{P}_5$ , we introduce the slack variable  $\lambda = \min \{R_{\text{sum}}^o, R_m\}$  into  $\mathbf{P}_5$  and have

$$\begin{aligned} \mathbf{P}_6 : \quad & \max_{t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, t_c, f_m, \beta, \lambda} \frac{\lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu}, k}}}{E_{\text{total}}(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, T, t_c, f_m)} \\ \text{s.t. } & \text{C1}^\dagger, \text{C2}^\dagger, \text{C3, C4, C7 - C10.} \end{aligned}$$

Next, we use the following lemma from the Dinkelbach's method to transform the objective function of  $\mathbf{P}_6$  from a fractional form into a subtractive form.

**Lemma 1.** The optimal solution to  $\mathbf{P}_6$  is achieved if and only if the following equation holds.

$$\begin{aligned} & \max_{t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, t_c, f_m, \beta, \lambda} \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu}, k}} \\ & - q^+ E_{\text{total}}(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, T, t_c, f_m) = \lambda^+ + \sum_{k=1}^K \frac{f_k^+ T}{C_{\text{cpu}, k}} \\ & - q^+ E_{\text{total}}(P_t, t_e^+, \mathbf{t}^{b+}, \mathbf{t}^{a+}, \boldsymbol{\rho}^+, \mathbf{p}^+, \mathbf{f}^+, T, t_c^+, f_m^+) = 0, \end{aligned} \quad (19)$$

where  $q^+$  denotes the maximum objective function value of  $\mathbf{P}_6$  and the superscript  $+$  indicates the optimal solution to  $\mathbf{P}_6$ . The proof of Lemma 1 can be readily obtained based on the generalized fractional programming theory [33] and the detailed proof is omitted here for brevity.

Based on Lemma 1, we propose a Dinkelbach-based iterative algorithm to solve  $\mathbf{P}_6$  as summarized in Algorithm 1. Denoting the given value of the system computation EE by  $q$ , in each iteration of Algorithm 1, the following problem  $\mathbf{P}_7$  is solved for the given  $q$  and the obtained optimal solution is used to update the value of  $q$ , which is used as the given  $q$  for the next iteration until the value of  $q$  converges.

$$\begin{aligned} \mathbf{P}_7 : \quad & \max_{t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, t_c, f_m, \beta, \lambda} \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu}, k}} \\ & - q E_{\text{total}}(P_t, t_e, \mathbf{t}^b, \mathbf{t}^a, \boldsymbol{\rho}, \mathbf{p}, \mathbf{f}, T, t_c, f_m) \\ \text{s.t. } & \text{C1}^\dagger, \text{C2}^\dagger, \text{C3, C4, C7 - C10.} \end{aligned}$$

Note that  $\mathbf{P}_7$  is still a non-convex optimization problem due to the existence of coupling relationships between multiple variables, i.e.,  $\rho_k$  and  $t_k^b$ , etc. In order to tackle  $\mathbf{P}_7$ , we

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**Algorithm 1** Dinkelbach-based Iterative Algorithm for  $\mathbf{P}_6$ 


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- 1: Set the maximum error tolerance  $\epsilon$ ;
  - 2: Set the iteration index  $l = 1$  and  $q = 0$ ;
  - 3: **repeat**
  - 4: Solve  $\mathbf{P}_8$  with a given  $q$ , and obtain the optimal solution, denoted by  $\{t_e^\diamond, \mathbf{t}^{b\diamond}, \mathbf{t}^{a\diamond}, \boldsymbol{\rho}^\diamond, \mathbf{p}^\diamond, \mathbf{f}^\diamond, t_c^\diamond, f_m^\diamond, \lambda^\diamond\}$ ;
  - 5: Compute the computation EE of the system as  $q^\diamond = \frac{\lambda^\diamond + \sum_{k=1}^K \frac{f_k^\diamond T}{C_{\text{cpu}, k}}}{E_{\text{total}}(P_t, t_e^\diamond, \mathbf{t}^{b\diamond}, \mathbf{t}^{a\diamond}, \boldsymbol{\rho}^\diamond, \mathbf{p}^\diamond, \mathbf{f}^\diamond, T, t_c^\diamond, f_m^\diamond)}$ ;
  - 6: **if**  $|q^\diamond - q| \leq \epsilon$  **then**
  - 7:   Set  $q^+ = q$ , Flag = 1 and the obtained solution is the optimal solution to  $\mathbf{P}_6$ ;
  - 8: **else**
  - 9:   Set  $q = q^\diamond$ , Flag = 0 and  $l = l + 1$ ;
  - 10: **end if**
  - 11: **until** Flag = 1.
- 

introduce the auxiliary variables  $x_k$ ,  $y_k$ ,  $\phi = f_m t_c$  and  $\varphi = f_m^3 t_c$  into  $\mathbf{P}_7$  and transform it into

$$\begin{aligned} \mathbf{P}_8 : \quad & \max_{t_e, \mathbf{t}^b, \mathbf{t}^a, \mathbf{x}, \mathbf{y}, \mathbf{f}, t_c, f_m, \beta, \lambda} \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu}, k}} - q \left( \left( t_e + \sum_{k=1}^K t_k^b \right) \right. \\ & \times \left( P_t + P_{\text{sc}} - \eta P_t \sum_{k=1}^K g_k \right) + \eta P_t \sum_{k=1}^K x_k g_k + \varepsilon_m \varphi \\ & \left. + \sum_{k=1}^K \left( P_c t_k^b + y_k + p_c t_k^a + \varepsilon_k f_k^3 T \right) \right) \\ \text{s.t. } & \text{C1}^{\dagger\dagger} : t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_t g_k h_k}{t_k^b B \sigma^2} \right) \\ & + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) \geq \beta_k L_{\min, k}, \forall k, \\ & \sum_{k=1}^K \beta_k L_{\min, k} \leq \frac{\phi}{C_{\text{cpu}}}, \\ & \frac{f_k T}{C_{\text{cpu}, k}} \geq (1 - \beta_k) L_{\min, k}, \forall k, \\ & \text{C2}^{\dagger\dagger} : P_c t_k^b + y_k + p_c t_k^a + \varepsilon_k f_k^3 T \\ & \leq \eta P_t g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - \eta x_k P_t g_k, \forall k, \\ & \text{C3}^{\dagger\dagger} : 0 \leq \varphi \leq \phi f_{\max}^2, 0 \leq f_k \leq f_k^{\max}, \forall k, \\ & \text{C4}^{\dagger\dagger} : t_e + \sum_{k=1}^K (t_k^b + t_k^a) + \sqrt{\frac{\phi^3}{\varphi}} \leq T, t_e, t_k^b, t_k^a \geq 0, \forall k, \\ & \text{C7}^{\dagger\dagger} : 0 \leq x_k \leq t_k^b, y_k \geq 0, \forall k, \text{C8,} \\ & \text{C9}^{\dagger\dagger} : \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_t g_k h_k}{t_k^b B \sigma^2} \right) \right. \\ & \left. + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) \right) \geq \lambda, \\ & \text{C10}^{\dagger\dagger} : \frac{\phi}{C_{\text{cpu}}} \geq \lambda, \end{aligned}$$

where  $f_m = \sqrt{\frac{\phi}{\varphi}}$  and  $t_c = \sqrt{\frac{\phi^3}{\varphi}}$ .

It is easy to prove that  $\mathbf{P}_8$  is convex and can be solved by using existing convex methods (e.g., interior point method, Lagrange duality, etc.) efficiently. The proof is similar to Appendix B and is omitted here for brevity.

In order to gain more insights into the optimal solutions, we use the Lagrange duality method to solve  $\mathbf{P}_8$  and obtain the following proposition.

**Proposition 4:** The system computation EE is maximized when the MEC server uses the maximum allowable time for task computing, i.e.,  $t_c^+ = T - \left( t_e^+ + \sum_{k=1}^K (t_k^{b+} + t_k^{a+}) \right)$ .

*Proof.* Please see Appendix C.  $\blacksquare$

**Algorithm 2** Bisection-based Iterative Algorithm for  $P_{\min}$ 

- 1: Initialize the maximum tolerance  $\epsilon$ ;
- 2: Set  $P_{\text{low}} = 0$ ,  $P_{\text{up}} = P_{\text{max}}$ ;
- 3: **repeat**
- 4:   Set  $P_t = (P_{\text{low}} + P_{\text{up}})/2$ ;
- 5:   Solve (20) with a given  $P_t$ , and obtain  $\mathcal{A}$ ;
- 6:   **if**  $\mathcal{A}$  is an nonempty set **then**
- 7:     Set  $P_{\text{up}} = P_t$ ;
- 8:   **else**
- 9:     Set  $P_{\text{low}} = P_t$ ;
- 10:   **end if**
- 11: **until**  $P_{\text{up}} - P_{\text{low}} < \epsilon$ ;
- 12:  $P_{\min}$  is given by  $\frac{P_{\text{up}} + P_{\text{low}}}{2}$ .

2) *Optimal solution to  $\mathbf{P}_5$* : After  $\mathbf{P}_6$  is solved in its feasible range  $[P_{\min}, P_{\max}]$  via Algorithm 1, the optimal solution to  $\mathbf{P}_5$  can be obtained by identifying the  $\mathbf{P}_6$  solution that returns the largest objective function value of  $\mathbf{P}_5$  among all the solutions obtained by Algorithm 1 together with the corresponding value of  $P_t$ . This indicates that limiting the search scope of  $P_t$  can reduce the complexity of solving  $\mathbf{P}_5$ . Due to the energy-causality and QoS constraints in (13)-(16), there exists a lower bound of  $P_t$ , denoted by  $P_{\min}$ , which can be determined by minimizing  $P_t$  while satisfying all the constraints of  $\mathbf{P}_5$ . The obtained  $P_{\min}$  can be used to limit the search scope of  $P_t$ . Let  $[P_{\min}, P_{\max}]$  denote the range of  $P_t$  that makes  $\mathbf{P}_5$  feasible, and  $P_{\min}$  can be obtained by solving

$$\mathbf{P}_9 : \min_{P_t, t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, t_c, f_m, \beta} P_t$$

$$\text{s.t. } C1^{\dagger\dagger}, C2^{\dagger\dagger}, C3^{\dagger}, C4^{\dagger}, C6, C7^{\dagger}, C8.$$

$\mathbf{P}_9$  is a non-convex problem due to the non-convex constraints  $C1^{\dagger\dagger}$  and  $C2^{\dagger\dagger}$ , where each IoT node has unique  $\rho_k$  and  $t_k^b$  but they share the same  $P_t$ . Fortunately, we find that the objective function of  $\mathbf{P}_9$  is a monotonous function of  $P_t$  and when  $P_t$  is fixed,  $\mathbf{P}_9$  reduces to

$$\text{find } \mathcal{A} = \{t_e, t^b, t^a, \rho, \mathbf{p}, \mathbf{f}, t_c, f_m, \beta\}$$

$$\text{s.t. } C1^{\dagger\dagger}, C2^{\dagger\dagger}, C3^{\dagger}, C4^{\dagger}, C7^{\dagger}, C8, \quad (20)$$

which is convex and can be solved using the existing convex tools. Based on (20), the minimum  $P_t$  that satisfies all the constraints of  $\mathbf{P}_9$  can be obtained by the bisection method. Hence, we propose a bisection-based iterative algorithm in Algorithm 2 to search for  $P_{\min}$ . For a certain error tolerance  $\epsilon$ , the bisection method in Algorithm 2 is guaranteed to converge.

Based on Algorithm 1 and Algorithm 2, a two-layer iterative algorithm is devised in Algorithm 3 to obtain the optimal solution to  $\mathbf{P}_5$ . In the outer loop (lines 7, 9 and 10), the value of  $P_t$  increases by a sufficiently small step size  $\epsilon_0$  from  $P_{\min}$  to  $P_{\max}$  and  $\mathbf{P}_5$ 's objective function values obtained from the current and previous runs of the inner loop are compared to identify the largest objective function value (and the corresponding solution to  $\mathbf{P}_6$  and  $P_t$  value); while in the inner loop (line 8), the optimal solution of  $\mathbf{P}_6$  for a given  $P_t$  is obtained.

*Remark 1*: It is worth noting that our proposed two-layer iterative algorithm in Algorithm 3 can serve the following

**Algorithm 3** Two-layer Iterative Algorithm for  $\mathbf{P}_5$ 

- 1: Initialize a sufficiently small step size  $\epsilon_0$ ;
- 2: Obtain  $P_{\min}$  by using Algorithm 2;
- 3: Set  $P_t = P_{\min}$ ;
- 4: Solve  $\mathbf{P}_6$  with a given  $P_t$  via Algorithm 1;
- 5: Compute the computation EE of the system  $q^*$  and obtain the corresponding solution, denoted by  $\{P_t, t_e^*, t^{b*}, t^{a*}, \rho^*, \mathbf{p}^*, \mathbf{f}^*, t_c^*, f_m^*, \lambda^*\}$ ;
- 6: **repeat**
- 7:   Set  $P_t = P_t + \epsilon_0$ ;
- 8:   Solve  $\mathbf{P}_6$  with a given  $P_t$  to obtain the system computation EE  $q^+$  and the corresponding solution using Algorithm 1;
- 9:   **If**  $q^+ > q^*$
- 10:     Set  $q^* = q^+$  and update the optimal solution;
- 11:   **End**
- 12: **until**  $P_t > P_{\max}$ .

two purposes. Firstly, compared with the exhaustive search method, Algorithm 3 enjoys a much lower complexity while achieving the optimal resource allocation because the optimal local computation time of each IoT node has been analytically derived and Algorithm 2 narrows the searching range for  $P_t$  by identifying its lower bound  $P_{\min}$ . Secondly, in addition to the considered hybrid BackCom-AT and partial computation offloading, Algorithm 3 can also be used to achieve the optimal resource allocation under the complete offloading mode (i.e., all the IoT nodes offload all their task bits to the MEC server via hybrid BackCom-AT) or the pure BackCom mode (i.e., all the IoT nodes only use BackCom to offload their task bits) by letting  $f_k = 0, \forall k$  or  $p_k = 0, t_k^a = 0, \forall k$ , respectively.

Note that in the fully local computing mode or the pure AT mode where all the IoT nodes perform local computing only or only use AT to offload their task bits, respectively, the proposed Dinkelbach-based iterative algorithm in Algorithm 1 can be applicable by letting making a few variable substitutions because in these two modes,  $P_t$  is only coupled with  $t_e$ .

3) *A reduced-complexity iterative algorithm for solving  $\mathbf{P}_5$* : The computational complexity of Algorithm 3 may be high for some special cases, e.g., the case with a large value of  $P_{\max} - P_{\min}$ , because the number of feasible values of  $P_t$  and thus the times of calling Algorithm 1 can be vary large. Instead, we devise a suboptimal, reduced-complexity iterative algorithm to solve  $\mathbf{P}_5$  via BCD.

When  $P_t$  is fixed,  $\mathbf{P}_5$  reduces to  $\mathbf{P}_6$  and Algorithm 1 can be used to achieve the optimal solution to  $\mathbf{P}_6$ , which can be solved optimally by Algorithm 1.

When all the other optimization variables except for  $P_t$  are given,  $\mathbf{P}_5$  reduces to

$$\mathbf{P}_{10} : \max_{P_t} \frac{\min\{\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a), R_m\} + D_0}{P_t A_0 + B_0}$$

$$\text{s.t. } C1^{\dagger}, C2^{\dagger}, C6,$$

$$\text{where } A_0 = t_e + \sum_{k=1}^K t_k^b - \eta \sum_{k=1}^K \left( g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - t_k^b \rho_k g_k \right), \quad B_0 =$$

$$\sum_{k=1}^K (P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 \tau_k) + E_m^e + P_{sc} \left( t_e + \sum_{k=1}^K t_k^b \right), C_k = \frac{\xi \rho_k g_k h_k}{B \sigma^2} \text{ and } D_0 = \sum_{k=1}^K R_m^e.$$

Combining C1<sup>†</sup>, C2<sup>†</sup> and C6, we can obtain the range of  $P_t$  as  $P_L \leq P_t \leq P_{\max}$ , where

$$P_L = \max \left\{ 0, \frac{2 \frac{\beta_k L_{\min, k} - R_k^a}{t_k^b B} - 1}{C_k}, \frac{P_c t_k^b + (p_k + p_c) t_k^a + \varepsilon_k f_k^3 T}{\eta g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - t_k^b \eta \rho_k g_k}, \forall k \right\}. \quad (21)$$

In order to handle the *min* function in the objective function of  $\mathbf{P}_{10}$ , we consider the following two cases, which are **Case I**:  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) \geq R_m$ , and **Case II**:  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) \leq R_m$ .

In Case I,  $\mathbf{P}_{10}$  can be rewritten as

$$\mathbf{P}_{11} : \max_{P_t} \frac{R_m + D_0}{P_t A_0 + B_0} \\ \text{s.t. } \max(0, P_L, P_{L2}) \leq P_t \leq P_{\max},$$

where  $P_{L2}$  is the unique solution<sup>5</sup> to  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) = R_m$  with respect to  $P_t$ . Since the objective function is a monotonically decreasing function of  $P_t$ , the optimal solution to  $\mathbf{P}_{11}$  is given by  $\max(0, P_L, P_{L2})$ .

In Case II,  $\mathbf{P}_{10}$  can be rewritten as

$$\mathbf{P}_{12} : \max_{P_t} \frac{\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) + D_0}{P_t A_0 + B_0} \\ \text{s.t. } \max(0, P_L) \leq P_t \leq \min(P_{L2}, P_{\max}).$$

**Proposition 5:** Let  $P_t^*$  denote the optimal solution to  $\mathbf{P}_{12}$ .  $P_t^*$  can be computed as

$$P_t^* = \begin{cases} \min(P_{L2}, P_{\max}), & \text{if } \sum_{k=1}^K t_k^b B f_k(\min(P_{L2}, P_{\max})) \\ & \geq A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right), \\ \max(0, P_L), & \text{if } \sum_{k=1}^K t_k^b B f_k(\max(0, P_L)) \\ & \leq A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right), \\ P_0, & \text{otherwise,} \end{cases} \quad (22)$$

where  $f_k(P_t) = \frac{C_k(P_t A_0 + B_0)}{(1 + C_k P_t) \ln 2} - A_0 \sum_{k=1}^K \log_2(1 + C_k P_t)$  and  $P_0$  is the unique solution to  $\sum_{k=1}^K t_k^b B f_k(P_t) - A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right) = 0$ .

*Proof.* Please see Appendix D. ■

Combining the above two cases, the optimal solution to  $\mathbf{P}_{10}$  is given by  $\max(0, P_L, P_{L2})$  or  $P_t^*$  depending on which of them achieves a higher system computation EE.

By exploiting the BCD technique based on Algorithm 1 and the above obtained optimal solution to  $\mathbf{P}_{10}$ , we propose a reduced-complexity iterative algorithm for solving  $\mathbf{P}_5$  is shown in Algorithm 4.

Note that in Algorithm 4, the value of  $P_{L2}$  in the  $ii$ -th iteration is determined by the PB's transmit power obtained in the  $ii$ -th iteration, namely  $P_t^{ii}$ . The reasons are as follows. On the one hand, the following Lemma indicates

<sup>5</sup>In line 7 of Algorithm 4, we provide a way to determine  $P_{L2}$  and the detailed reasons are provided in the last three paragraphs of this subsection.

#### Algorithm 4 Reduced-complexity Iterative Algorithm for $\mathbf{P}_5$

- 1: Initialize the maximum iterations  $I_{\max}$  and the maximum tolerance  $\epsilon$ ;
- 2: Obtain  $P_{\min}$  by using Algorithm 2;
- 3: Set  $P_t^1 = \frac{P_{\min} + P_{\max}}{2}$  and iteration index  $ii = 1$ ;
- 4: **repeat**
- 5: Solve  $\mathbf{P}_6$  for a given  $P_t^{ii}$  via Algorithm 1 and obtain  $\left\{ t_e^{ii+1}, \left\{ t_k^{b(ii+1)} \right\}_{k=1}^K, \left\{ t_k^{a(ii+1)} \right\}_{k=1}^K, \left\{ \rho_k^{ii+1} \right\}_{k=1}^K, t_c^{ii+1}, f_m^{ii+1}, \left\{ p_k^{ii+1} \right\}_{k=1}^K, \left\{ f_k^{ii+1} \right\}_{k=1}^K \right\}$ ;
- 6: Compute the corresponding computation EE, denoted by  $q_1^{ii+1}$ ;
- 7: Compute  $P_L$  based on (21) and set  $P_{L2} = P_t^{ii}$ ;
- 8: Obtain  $P_0$  by means of the bisection method and determine  $P_t^*$  based on Proposition 5;
- 9: **if**  $\frac{R_m + D_0}{\max(0, P_L, P_{L2}) A_0 + B_0} \geq \frac{\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t^*) + R_k^a) + D_0}{P_t^* A_0 + B_0}$  **then**
- 10: Set  $P_t^{ii+1} = \max(0, P_L, P_{L2})$  and  $q_2^{ii+1} = \frac{R_m + D_0}{\max(0, P_L, P_{L2}) A_0 + B_0}$ ;
- 11: **else**
- 12: Set  $P_t^{ii+1} = \frac{P_t^*}{\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t^*) + R_k^a) + D_0}$  and  $q_2^{ii+1} = \frac{P_t^*}{P_t^* A_0 + B_0}$ ;
- 13: **end if**
- 14: **if**  $|q_2^{ii+1} - q_1^{ii+1}| < \epsilon$  **then**
- 15: Set  $P_t^* = P_t^{ii+1}$ ,  $t_e^* = t_e^{ii+1}$ ,  $t_c^* = t_c^{ii+1}$ ,  $f_m^* = f_m^{ii+1}$ ,  $p_k^* = p_k^{ii+1}$ ,  $f_k^* = f_k^{ii+1}$ ,  $\rho_k^* = \rho_k^{ii+1}$ ,  $t_k^{b*} = t_k^{b(ii+1)}$ ,  $t_k^{a*} = t_k^{a(ii+1)}$ ,  $\forall k$ , Flag = 1 and return;
- 16: **else**
- 17: Set  $ii = ii + 1$  and Flag = 0;
- 18: **end if**
- 19: **until** Flag = 1 or  $ii = I_{\max}$

that by using the obtained solution of  $\mathbf{P}_6$  in each iteration, the given PB's transmit power can make the equation  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) = R_m$  hold.

**Lemma 2.** When  $\mathbf{P}_6$  is optimally solved to a given  $P_t$ , the total task bits offloaded by all the IoT nodes should equal the MEC server's computation capacity during the task execution phase, i.e.,  $\sum_{k=1}^K \left( t_k^{b+} B \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} B \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) = \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$ , and  $P_{L2}$  is equal the given  $P_t$ .

*Proof.* Please see Appendix E. ■

On the other hand, according to the definition of  $P_{L2}$  below  $\mathbf{P}_{11}$ ,  $P_{L2}$  is also a solution for the equation  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) = R_m$  with respect to  $P_t$ . Substituting the obtained solution of  $\mathbf{P}_6$  into the equation  $\sum_{k=1}^K (t_k^b B \log_2(1 + C_k P_t) + R_k^a) = R_m$ , we know that the left side of this equation is an increasing function with respect to  $P_t$  and  $R_m$  is a constant. As only one solution exists for the above equation,  $P_{L2}$  equals the given  $P_t$ .

## V. NUMERICAL RESULTS

In this section, we verify the effectiveness and the superiority of the proposed schemes via computer simulations.

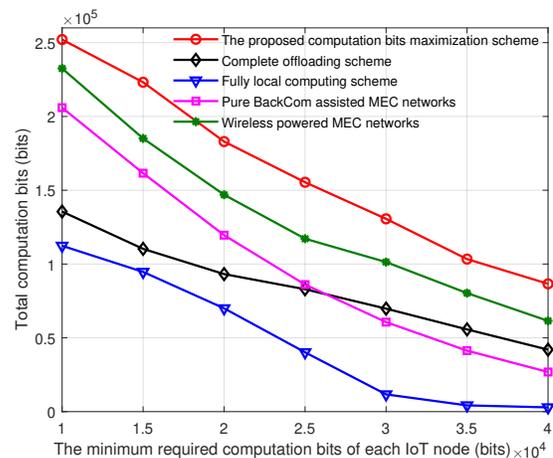
Table 1: Key Simulation Settings

Parameters	Notation	Value
The entire time block	$T$	1 Second
The communication bandwidth	$B$	100 kHz
The number of cycles for one bit at the $k$ -th IoT node	$C_{\text{cpu},k}$	1000 Cycles/bit
The number of cycles for one bit at the MEC server	$C_{\text{cpu}}$	1000 Cycles/bit
The constant circuit power consumption for BackCom at the $k$ -th IoT node	$P_c$	10 $\mu$ W
The constant circuit power consumption for AT at the $k$ -th IoT node	$p_c$	1 mW
The constant circuit power consumption at the PB	$P_{\text{sc}}$	10 mW
The maximum transmit power at the PB	$P_{\text{max}}$	1 W
The fixed energy conversion efficiency at each IoT node	$\eta$	0.7
The performance gap reflecting the real modulation for BackCom	$\xi$	-15dB
The noise power spectral density	$\sigma^2$	-120dBm/Hz
The number of IoT nodes	$K$	4
The effective capacitance coefficient of the $k$ -th IoT node	$\varepsilon_k$	$10^{-26}$
The effective capacitance coefficient of the MEC server	$\varepsilon_m$	$10^{-28}$
The maximum CPU frequency at the $k$ -th IoT node	$f_k^{\text{max}}$	$5 \times 10^8$ Hz
The maximum CPU frequency at the MEC server	$f_{\text{max}}$	$10^{10}$ Hz
The minimum computation bits of the $k$ -th IoT node	$L_{\text{min},k}$	10 kbits

Unless otherwise specified, the basic simulation parameters are provided as shown in Table I [5], [11], as shown at the top of the next page. We consider a standard channel fading model, where the channel gain between the PB and the  $k$ -th IoT node is modeled by  $g_k = g'_k d_{0k}^{-\alpha}$  with the small-scale fading  $g'_k$ , distance  $d_{0k}$ , and path loss exponent  $\alpha$ . The channel gain from the MEC server to the  $k$ -th IoT node is given by  $h_k = h'_k d_{1k}^{-\alpha}$  with the small-scale fading  $h'_k$  and distance  $d_{1k}$ . Here we let  $\alpha = 3$ ,  $d_{01} = 12\text{m}$ ,  $d_{02} = 10\text{m}$ ,  $d_{03} = 15\text{m}$ ,  $d_{04} = 13\text{m}$ ,  $d_{11} = 60\text{m}$ ,  $d_{12} = 65\text{m}$ ,  $d_{13} = 50\text{m}$  and  $d_{14} = 55\text{m}$ .

In order to illustrate the advantages of the proposed computation bits/EE maximization scheme, we compare the performance under the proposed schemes with the following four benchmark schemes:

- **Complete offloading scheme:** All the tasks at all the IoT nodes can only be offloaded to the MEC server for computation and each IoT node can choose BackCom, AT, or hybrid BackCom and AT to transmit its task bits. For maximizing the total computation bits of all the IoT nodes, this scheme is obtained by solving  $\mathbf{P}_3$  with  $f_k = 0$ , while for maximizing the system computation EE, this scheme is achieved based on Remark 1.
- **Fully local computing scheme:** All the IoT nodes can only compute their tasks locally. For maximizing the total computation bits of all the IoT nodes, this scheme is optimized under the same constraints as  $\mathbf{P}_3$  with  $t_k^b = 0$ ,  $t_k^a = 0$ ,  $x_k = 0$ ,  $y_k = 0$ , and  $t_c = 0$ . This scheme with the aim of maximizing the system computation EE is obtained by means of the proposed Dinkelbach-based iterative algorithm through a few changes, i.e., letting  $P_t = \frac{x}{t_e}$ .
- **Pure BackCom assisted MEC networks:** This scheme is optimized for pure BackCom assisted MEC networks, where each IoT node can offload its partial task bits to the MEC server for computation and the offloaded task bits can only be transmitted via BackCom. Likewise, for maximizing the total computation bits of all the IoT nodes, this scheme is achieved by solving  $\mathbf{P}_3$  with  $t_k^a = 0$  and  $y_k = 0$ . Remark 1 is used to obtain the scheme for maximizing the system computation EE in pure BackCom assisted MEC networks.

Fig. 2.  $R_{\text{total}}$  versus the minimum required computation bits.

- **Wireless powered MEC networks:** This scheme is optimized for the wireless powered MEC networks, where each IoT node harvests energy from RF signals first and then uses the harvested energy to offload its partial task bits to the MEC server for computation. Accordingly, this scheme for maximizing the total computation bits of all the IoT nodes is optimized by solving  $\mathbf{P}_3$  with  $t_k^b = 0$  and  $x_k = 0$ , while this scheme for maximizing the system computation EE is achieved by using the proposed Dinkelbach-based iterative algorithm with  $P_t = \frac{x}{t_e}$ .

#### A. Performance analysis for the proposed computation bits maximization scheme

Fig. 2 demonstrates the total computation bits of all the IoT nodes  $R_{\text{total}}$  versus the minimum required computation bits at each IoT node. For convenience, we let  $L_{\text{min},1} = L_{\text{min},2} = L_{\text{min},3} = L_{\text{min},4} = L_{\text{min}}$  and  $L_{\text{min}}$  is ranged from 10 kbits to 40 kbits. In order to illustrate the superiority of the proposed computation bits maximization scheme in terms of  $R_{\text{total}}$ , we compare  $R_{\text{total}}$  achieved by the proposed scheme with those obtained by the complete offloading scheme, the fully local computing scheme, pure BackCom assisted MEC networks and wireless powered MEC networks. From this figure, we

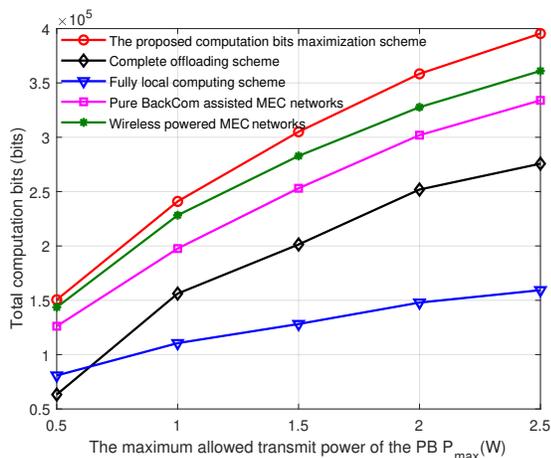


Fig. 3. Total computation bits of all the IoT nodes  $R_{\text{total}}$  versus  $P_{\max}$ .

can observe that  $R_{\text{total}}$  under all the schemes decreases with the increase of  $L_{\min}$ . This is because the increase of  $L_{\min}$  denotes a higher computation bits requirement for each IoT node and more resources need to be allocated to the IoT nodes with worse channels, leading to a reduction to the total computation bits. Besides, we can find that the proposed computation bits maximization scheme always outperforms pure BackCom assisted MEC networks and wireless powered MEC networks in terms of the total computation bits, which verifies the advantages of the combination of BackCom and the wireless powered MEC networks. By comparisons, we can also see that the proposed computation bits maximization scheme can achieve the highest  $R_{\text{total}}$  since the other schemes can be regarded as special cases for the proposed scheme and have less flexibility to utilize resources for maximizing the total computation bits.

Fig. 3 shows the total computation bits of all the IoT nodes versus the maximum allowed transmit power at the PB,  $P_{\max}$ , under different schemes. Here  $P_{\max}$  varies from 0.5 W to 2.5 W. It can be observed that the total computation bits of all the IoT nodes under all the schemes increases with the increase of  $P_{\max}$ . The reasons are as follows. Based on Proposition 1, the optimal transmit power of the PB is determined by  $P_{\max}$  and a higher transmit power allows IoT nodes to harvest more energy for task offloading and computing. Besides, a higher transmit power also improves the power of the backscattered signal, increasing the task bits offloaded at each IoT node via BackCom. Likewise, the advantages of the combination of BackCom and the wireless powered MEC networks are also illustrated by comparing the proposed scheme with pure BackCom assisted MEC networks and wireless powered MEC networks. Besides, we also observe that the total computation bits under the proposed scheme are the highest among these five schemes, which also demonstrates the superiority of the proposed scheme in terms of total computation bits. The IoT nodes choose to perform task offloading only when the harvested energy (or the transmit power of the PB) is large enough; otherwise, IoT nodes prefer to compute locally for achieving more computation bits.

Fig. 4 depicts the impact of the computation capacity of the

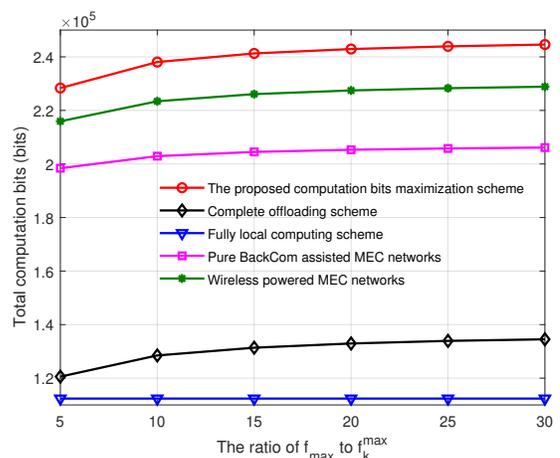


Fig. 4. Total computation bits v.s. the ratio of  $f_{\max}$  to  $f_k^{\max}$ .

MEC server on the total computation bits of all the IoT nodes, where the ratio of the maximum computation frequency of the MEC server  $f_{\max}$  to that of the  $k$ -th IoT node  $f_k^{\max}$  varies from 5 to 30 and  $f_k^{\max}$  is fixed as 500 MHz. It can be observed that the total computation bits under all the schemes except for the fully local computing scheme increase when the MEC server's computation capacity improves, while the total computation bits under the fully local computing scheme basically keeps unchanged. This is because a higher computation capacity of the MEC server allows more task bits to be offloaded and computed, increasing the total computation bits. Whereas the MEC server's computation capacity does not influence the total computation bits under the fully local computing scheme. It can also be seen that the proposed computation bits maximization scheme is superior to the other schemes in terms of total computation bits for all the considered values of  $\frac{f_{\max}}{f_k^{\max}}$ .

#### B. Convergence analysis for the proposed iterative algorithms

Fig. 5 demonstrates the convergence analysis of the proposed iterative algorithms, such as, Algorithm 1, Algorithm 2, Algorithm 3 and Algorithm 4. Specifically, Fig. 5(a) shows the convergence of Algorithm 1 under different settings of  $P_t$  and  $L_{\min}$ , the convergence of Algorithm 2 with different values of  $L_{\min}$  is illustrated in Fig. 5(b) and in Fig. 5(c), the convergence analysis and performance comparisons between Algorithm 3 and Algorithm 4 are plotted. From Fig. 5(a), it can be observed that less than 4 iterations are required for the proposed Dinkelbach-based iterative algorithm in Algorithm 1 to converge to the maximum system computation EE, which illustrates that the proposed Dinkelbach-based iterative algorithm is computationally efficient. The proposed bisection-based iterative algorithm in Algorithm 2 is used to obtain the maximum value of  $P_t$ , namely  $P_{\min}$ , and it can be observed from Fig. 5(b) that it takes about 9 iterations to achieve the value of  $P_{\min}$ , which verifies the convergence of the proposed bisection-based iterative algorithm. Besides, we can also observe that  $P_{\min}$  increases when  $L_{\min}$  increases since with a larger  $L_{\min}$ , the energy consumption at each IoT node

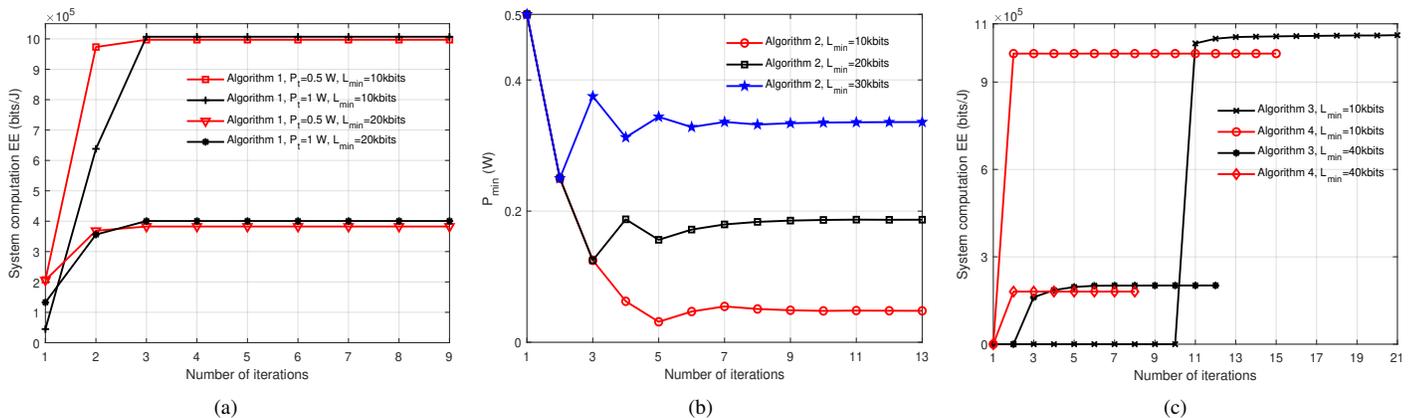


Fig. 5. The convergence of the proposed iterative algorithms: (a) the convergence of Algorithm 1; (b) the convergence of Algorithm 2; (c) the convergence analysis and comparisons between Algorithm 3 and Algorithm 4.

for task offloading and computing may increase, leading to an improvement for  $P_t$ .

From Fig. 5(c), we can observe that both the proposed two-layer iterative algorithm in Algorithm 3 and the proposed reduced-complexity iterative algorithm in Algorithm 4 are convergent. Specifically, the number of iterations required for the convergence of the proposed reduced-complexity iterative algorithm is much less than that of the proposed two-layer iterative algorithm, illustrating the low complexity of the proposed reduced-complexity iterative algorithm. By comparisons, we can also see that the system computation EE achieved by the proposed reduced-complexity iterative algorithm is always close to that of the proposed two-layer iterative algorithm, which further verifies the suboptimal performance of the proposed reduced-complexity iterative algorithm in terms of system computation EE. That is, the low complexity of the proposed reduced-complexity iterative algorithm is achieved at the cost of a slight reduction to the system computation EE. Moreover, we also find that the required iterations for the proposed two-layer iterative algorithm under the set of  $L_{\min} = 40$  kbits are less than those under the set of  $L_{\min} = 10$  kbits. This is because the improvement of  $L_{\min}$  increases the value of  $P_{\min}$ , resulting in a narrow searching range of  $P_t$ .

### C. Performance analysis for the proposed system computation EE maximization schemes

Fig. 6 shows the system computation EE under different schemes versus  $L_{\min}$ . Here we consider two schemes, which are the proposed two-layer iterative algorithm in Algorithm 3 and the proposed reduced-complexity iterative algorithm shown in Algorithm 4, to maximize the system computation EE. Besides, for better comparisons, we also include the performance under the proposed computation bits maximization scheme. As shown in this figure, it can be observed that the system computation EE under all the schemes decreases when  $L_{\min}$  increases. The reasons are as follows. With the increase of  $L_{\min}$ , the energy consumption at each IoT node for task offloading and computing may increase. Besides, the total computation bits offloaded by all the IoT nodes may also increase, leading to an improvement for the energy consumption of the MEC server. That is, the total system energy

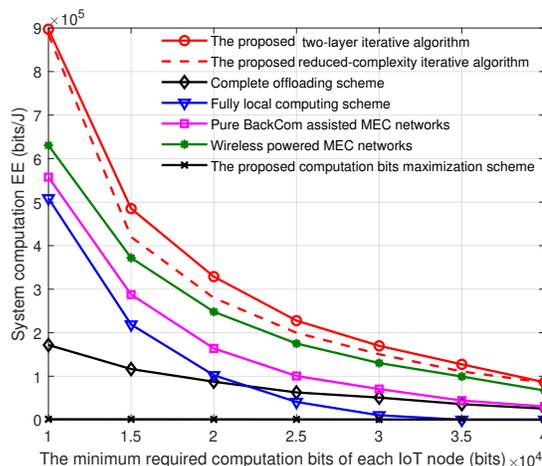


Fig. 6. System computation EE versus the minimum required computation bits at each IoT node.

consumption increases with  $L_{\min}$ . Since the energy consumption grows faster than the growth of the computation bits, the system computation EE decreases with the increase of  $L_{\min}$ . By comparisons, we also find that the system computation EE under the proposed reduced-complexity iterative algorithm is close to that under the proposed two-layer iterative algorithm and always outperforms other schemes, namely, the complete offloading scheme, the fully local computing scheme, pure BackCom assisted MEC networks, and wireless powered MEC networks, verifying the suboptimal performance of Algorithm 4 and the superiority of the combination of BackCom and the wireless powered MEC networks. Moreover, it can be observed that the proposed computation bits maximization scheme achieves the worst performance among these schemes in terms of system computation EE, showing that the proposed computation bits maximization scheme has compromise on energy-efficiency performance.

Fig. 7 shows the effect of the system computation EE on  $P_{\max}$  under different schemes. It can be observed that the system computation EE under all the schemes increases with the increase of  $P_{\max}$  when  $P_{\max}$  is small and when  $P_{\max}$  is large enough, the system computation EE converges. This is

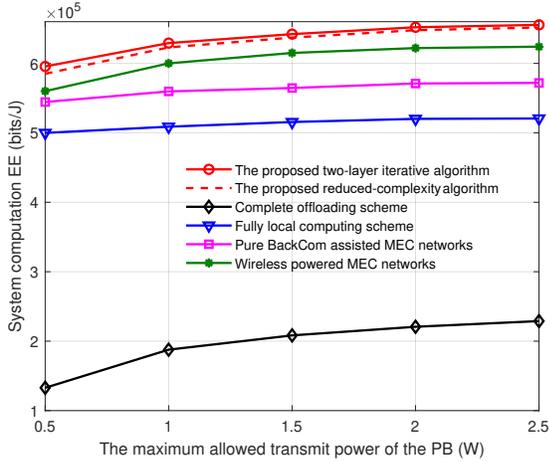


Fig. 7. System computation EE versus the maximum allowed transmit power at the PB.

because when  $P_{\max}$  is small, the optimal transmit power of the PB is determined by  $P_{\max}$ , while when  $P_{\max}$  is large enough, the optimal transmit power of the PB is fixed and its value is independent of  $P_{\max}$ . By comparisons, we can also see that the proposed reduced-complexity iterative algorithm can achieve the suboptimal performance in terms of system computation EE and the proposed system computation EE maximization scheme always outperforms the other schemes.

## VI. CONCLUSIONS

In this paper, we studied the total computation bits and system computation EE maximizations for a hybrid BackCom-AT WPMEC network, subject to the MEC server's computation capacity constraint, the energy causality and QoS constraints of each IoT node. The total computation bits maximization problem was transformed into a convex one and solved by the CVX, while the system computation EE maximization problem was optimally solved by our proposed two-layer iterative algorithm. Subsequently, we devised a reduced-complexity iterative algorithm to obtain a suboptimal solution to the system computation EE maximization problem. Simulations results validated the superiority of the proposed total-computation-bits maximization scheme and system-computation-EE maximization algorithms over several baseline schemes. Besides, we obtained the following two insights. First, when the MEC server's computation capacity is finite, the complete offloading scheme is not the optimal choice for maximizing the total computation bits or system computation EE. Second, the total computation bits are always maximized when the MEC server adopts the maximum allowed computing frequency, which, however, does not maximize the system computation EE.

### APPENDIX A

#### PROOF OF PROPOSITION 1

Here we prove Proposition 1 by means of contradiction.

#### A. Proofs for $P_t^* = P_{\max}$ and $f_m^* = f_{\max}$

Assume that  $\{P_t^*, t_e^*, \{t_k^{b*}\}_{k=1}^K, \{t_k^{a*}\}_{k=1}^K, \{\rho_k^*\}_{k=1}^K, t_c^*, f_m^*, \{p_k^*\}_{k=1}^K, \{f_k^*\}_{k=1}^K, \{\tau_k^*\}_{k=1}^K, \lambda^*\}$  is the optimal

solution to  $\mathbf{P}_1$ , where  $P_t^* < P_{\max}$  and  $\lambda^* = \min\{\sum_{k=1}^K (t_k^{b*} B \log_2(1 + \frac{\xi \rho_k^* P_t^* g_k h_k}{B \sigma^2}) + t_k^{a*} B \log_2(1 + \frac{p_k^* h_k}{B \sigma^2})) + \frac{t_c^* f_m^*}{C_{\text{cpu}}}\}$ . Accordingly, the maximum number of the computation bits of the considered system  $R_{\text{total}}^*$  is given by  $\lambda^* + \sum_{k=1}^K \frac{\tau_k^* f_k^*}{C_{\text{cpu},k}}$ . Then we can construct another solution satisfying  $P_t^+ = P_{\max} > P_t^*$ ,  $t_e^+ = t_e^*$ ,  $t_c^+ = t_c^*$ ,  $f_m^+ = f_m^*$ ,  $t_k^{b+} = t_k^{b*}$ ,  $t_k^{a+} = t_k^{a*}$ ,  $\rho_k^+ = \rho_k^*$ ,  $p_k^+ = p_k^*$ ,  $f_k^+ = f_k^*$ ,  $\tau_k^+ = \tau_k^*$  and  $\lambda^+ = \min\{\sum_{k=1}^K (t_k^{b+} B \log_2(1 + \frac{\xi \rho_k^+ P_t^+ g_k h_k}{B \sigma^2}) + t_k^{a+} B \log_2(1 + \frac{p_k^+ h_k}{B \sigma^2})) + \frac{t_c^+ f_m^+}{C_{\text{cpu}}}\}$ . It is quite evident that the constructed solution is a feasible solution which satisfies all the constraints of  $\mathbf{P}_1$ . Let  $R_{\text{total}}^+$  be the achievable computation bits under the constructed solution. Then we have  $R_{\text{total}}^+ = \lambda^+ + \sum_{k=1}^K \frac{\tau_k^+ f_k^+}{C_{\text{cpu},k}} \geq R_{\text{total}}^*$  since  $\lambda^+ \geq \lambda^*$ , which contradicts the above assumption. Thus,  $P_t^* = P_{\max}$  holds when the maximum computation bits of the considered system are achieved. The same method can also be applied to prove that  $f_m^* = f_{\max}$  is satisfied for maximizing the total computation bits by making only a few changes and the detailed process is omitted here for brevity.

#### B. Proof for $\tau_k^* = T$

When  $P_t, t_e, t_c, t_k^b, t_k^a, \rho, p, t_c, f_m, \lambda$  and  $\{f_i, \tau_i\}_{i=\{1,2,\dots,K\} \setminus k}$  are fixed, we jointly optimize  $f_k$  and  $\tau_k$  to maximize the total achievable computation bits of the system. Assume that the optimal computation frequency  $f_k^*$  and time  $\tau_k^* < T$  satisfy all the constraints of  $\mathbf{P}_1$  with other parameters fixed. Then the maximum computation bits of the considered system  $R_{\text{total}}^*$  is given by  $\lambda + \sum_{i=1, i \neq k}^K \frac{\tau_i f_i}{C_{\text{cpu},i}} + \frac{\tau_k^* f_k^*}{C_{\text{cpu},k}}$ . Another feasible solution can be constructed as  $\{f_k^+, \tau_k^+\}$  with  $\tau_k^+ = T$  and  $\tau_k^+ f_k^+ (f_k^+)^2 = \tau_k^* f_k^* (f_k^*)^2$ . Accordingly, the achievable computation bits under the constructed solution are given by  $R_{\text{total}}^+ = \lambda + \sum_{i=1, i \neq k}^K \frac{\tau_i f_i}{C_{\text{cpu},i}} + \frac{\tau_k^+ f_k^+}{C_{\text{cpu},k}}$ . Based on  $\tau_k^+ f_k^+ (f_k^+)^2 = \tau_k^* f_k^* (f_k^*)^2$  and  $\tau_k^+ = T > \tau_k^*$ , we have  $f_k^+ < f_k^*$  and  $\tau_k^+ f_k^+ > \tau_k^* f_k^*$ , leading to  $R_{\text{total}}^+ > R_{\text{total}}^*$ , which contradicts the above assumption that  $\tau_k^* < T$ . Thus,  $\tau_k^* = T$  holds when the maximum computation bits of the considered system are achieved.

Based on the above analysis, Proposition 1 is obtained.

## APPENDIX B

### PROOF OF PROPOSITION 2

After carefully analyzing  $\mathbf{P}_3$ , it is not hard to conclude that the objective function and all the constraints except C1'' and C9'' are linear. Thus,  $\mathbf{P}_3$  is convex if and only if constraints C1'' and C9'' are convex. For constraint C1'', we only need to prove that for given  $k$ ,  $t_k^b B \log_2(1 + \frac{\xi x_k P_{\max} g_k h_k}{t_k^b B \sigma^2}) + t_k^a B \log_2(1 + \frac{y_k h_k}{t_k^a B \sigma^2}) \geq \beta_k L_{\min,k}$  is convex. Using the fact that the perspective function can preserve convexity, we know that the convexity of the above constraint is the same as that of  $B \log_2(1 + \frac{\xi x_k P_{\max} g_k h_k}{B \sigma^2}) + B \log_2(1 + \frac{y_k h_k}{B \sigma^2}) \geq \beta_k L_{\min,k}$ , which is convex. Similarly, we can prove that C9'' is also convex. The proof is complete.

APPENDIX C  
PROOF OF PROPOSITION 4

Let  $\alpha = (\alpha_0, \alpha_1, \dots, \alpha_6)$ ,  $\theta = (\theta_0, \theta_1, \theta_2, \dots, \theta_K)$ ,  $\varpi = (\varpi_0, \varpi_1, \varpi_2, \dots, \varpi_K)$ ,  $\omega = (\omega_0, \omega_1, \omega_2, \dots, \omega_K)$ , and  $\mu = (\mu_0, \mu_1, \mu_2, \dots, \mu_K)$  be the non-negative Lagrange multipliers with respect to all the constraints for  $\mathbf{P}_8$ . Then the Lagrangian function of  $\mathbf{P}_8$  is given by (C.1), as shown at the top of the next page.

By taking the partial derivative of  $\mathcal{L}$  with respect to  $\varphi$  and  $\phi$ , we have

$$\frac{\partial \mathcal{L}}{\partial \varphi} = \frac{\mu_0 \sqrt{\phi^3}}{2\sqrt{\varphi^3}} - q\varepsilon_m - \omega_0, \quad (\text{C.2})$$

$$\frac{\partial \mathcal{L}}{\partial \phi} = \frac{\alpha_0}{C_{\text{cpu}}} + \omega_0 f_{\text{max}}^2 - \frac{3\mu_0}{2} \sqrt{\frac{\phi}{\varphi}}. \quad (\text{C.3})$$

By letting  $\frac{\partial \mathcal{L}}{\partial \varphi} = \frac{\partial \mathcal{L}}{\partial \phi} = 0$ , we can obtain

$$f_m^+ = \left[ \frac{3\mu_0 C_{\text{cpu}}}{2(\alpha_0 + \omega_0 f_{\text{max}}^2 C_{\text{cpu}})} \right]^+ = \left[ \sqrt[3]{\frac{\mu_0}{2(q\varepsilon_m + \omega_0)}} \right]^+, \quad (\text{C.4})$$

where  $[x]^+ = \max\{x, 0\}$ .

Based on (C.4), it can be observed that when there are tasks to be computed at the MEC server, namely  $f_m^+ > 0$ ,  $\mu_0 > 0$  must be satisfied. Then according to the Karush-Kuhn-Tucker (KKT) conditions, the equation  $\mu_0 \left( T - t_e^+ - \sum_{k=1}^K (t_k^{b+} + t_k^{a+}) - \sqrt{\frac{(\phi^+)^3}{\varphi^+}} \right) = 0$  should always hold. Substituting  $\mu_0 > 0$  into the above equation, we have  $T - t_e^+ - \sum_{k=1}^K (t_k^{b+} + t_k^{a+}) - \sqrt{\frac{(\phi^+)^3}{\varphi^+}} = T - t_e^+ - \sum_{k=1}^K (t_k^{b+} + t_k^{a+}) - t_c^+ = 0$ . That is, if  $f_m^+ > 0$ , the MEC server always uses the maximum allowed time to compute tasks. Note that in the case of  $f_m^+ = 0$ , the MEC server can not provide computation service for the IoT nodes. Then the value of  $t_c^+$  does not influence the system computation EE of the investigated network and we can also let  $t_c^+ = T - t_e^+ - \sum_{k=1}^K (t_k^{b+} + t_k^{a+})$  for convenience.

APPENDIX D  
PROOF OF PROPOSITION 5

Let  $f(P_t)$  denote the objective function of  $\mathbf{P}_{12}$ , given by  $f(P_t) = \frac{\sum_{k=1}^K t_k^b \text{B} \log_2(1 + C_k P_t) + \sum_{k=1}^K R_k^a + D_0}{P_t A_0 + B_0}$ . Taking the first-order derivative of  $f(P_t)$  with respect to  $P_t$ , we have

$$\frac{\partial f(P_t)}{\partial P_t} = \frac{\sum_{k=1}^K t_k^b B f_k(P_t) - A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right)}{(P_t A_0 + B_0)^2}, \quad (\text{D.1})$$

where  $f_k(P_t) = \frac{C_k(P_t A_0 + B_0)}{(1 + C_k P_t) \ln 2} - A_0 \log_2(1 + C_k P_t)$ . In order to tell the monotonicity of  $\frac{\partial f(P_t)}{\partial P_t}$ , we first calculate the first-order derivative of  $f_k(P_t)$  as

$$\frac{\partial f_k(P_t)}{\partial P_t} = \frac{-C_k^2 (B_0 + A_0 P_t)}{(1 + C_k P_t)^2 \ln 2} < 0. \quad (\text{D.2})$$

Then the function  $\sum_{k=1}^K t_k^b B f_k(P_t)$  decreases with the increase of  $P_t$ . That is, there is only one solution making  $\frac{\partial f(P_t)}{\partial P_t} = 0$  hold. Let  $P_0$  be the solution to

$\sum_{k=1}^K t_k^b B f_k(P_t) - A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right) = 0$  and the value of  $P_0$  can be obtained by means of the bisection method since the function  $\sum_{k=1}^K t_k^b B f_k(P_t)$  is a monotonic decrease function. Then we find that when  $0 < P_t < P_0$ ,  $\sum_{k=1}^K t_k^b B f_k(P_t) - A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right) > 0$  and  $f(P_t)$  increases with  $P_t$ . Likewise, when  $P_t > P_0$ ,  $\sum_{k=1}^K t_k^b B f_k(P_t) - A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right) < 0$  and  $f(P_t)$  shows a downward trend.

Combining the range of  $P_t$  with  $\max(0, P_L) \leq P_t \leq \min(P_{L2}, P_{\text{max}})$ , the optimal solution to  $\mathbf{P}_{12}$ , denoted by  $P_t^*$ , is determined by  $\max(0, P_L)$ ,  $P_0$ , or  $\min(P_{L2}, P_{\text{max}})$ . Specifically, if  $\sum_{k=1}^K t_k^b B f_k(\max(0, P_L)) \leq A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right)$  is satisfied, then  $f(P_t)$  monotonically decreases with  $P_t \in [\max(0, P_L), \min(P_{L2}, P_{\text{max}})]$  and  $P_t^*$  is given by  $\max(0, P_L)$ . If  $\sum_{k=1}^K t_k^b B f_k(\min(P_{L2}, P_{\text{max}})) \geq A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right)$  holds,  $f(P_t)$  monotonically increases at the condition of  $P_t \in [\max(0, P_L), \min(P_{L2}, P_{\text{max}})]$  and  $P_t^*$  is determined by  $\min(P_{L2}, P_{\text{max}})$ . If  $\sum_{k=1}^K t_k^b B f_k(\min(P_{L2}, P_{\text{max}})) < A_0 \left( \sum_{k=1}^K R_k^a + D_0 \right) < \sum_{k=1}^K t_k^b B f_k(\max(0, P_L))$  is obtained, then we have  $\max(0, P_L) \leq P_0 \leq \min(P_{L2}, P_{\text{max}})$  and  $P_t^* = P_0$ .

Therefore, the optimal solution to  $\mathbf{P}_{12}$  is summarized as Proposition 5.

APPENDIX E  
PROOF OF LEMMA 2

Here we prove Lemma 2 by means of contradiction. In particular, when  $\mathbf{P}_6$  is optimally solved,  $\sum_{k=1}^K \left( t_k^{b+} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} \text{B} \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) \neq \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$  is assumed to be satisfied. That means that either  $\sum_{k=1}^K \left( t_k^{b+} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} \text{B} \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) > \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$  or  $\sum_{k=1}^K \left( t_k^{b+} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} \text{B} \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) < \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$  holds. If  $\sum_{k=1}^K \left( t_k^{b+} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} \text{B} \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) < \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$  holds, then we can construct another solution which satisfies  $p_k^o = p_k^+$ ,  $f_k^o = f_k^+$ ,  $\rho_k^o = \rho_k^+$ ,  $t_k^{bo} = t_k^{b+}$ ,  $t_k^{ao} = t_k^{a+}$ ,  $t_e^o = t_e^+$ ,  $t_c^o = t_c^+$  and  $\sum_{k=1}^K \left( t_k^{bo} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^o P_t g_k h_k}{B \sigma^2} \right) + t_k^{ao} \text{B} \log_2 \left( 1 + \frac{p_k^o h_k}{B \sigma^2} \right) \right) = \frac{t_c^o f_m^o}{C_{\text{cpu}}} < \frac{t_c^+ f_m^+}{C_{\text{cpu}}}$ . It can be observed that the constructed solution satisfies all the constraints of  $\mathbf{P}_6$  and the constructed solution can achieve the same computation bits as the optimal one while consuming less energy due to  $f_m^o < f_m^+$ . This means that the system computation EE under the constructed solution is higher than that under the optimal solution, which contradicts the fact that  $\{t_e^+, \{t_k^{b+}\}_{k=1}^K, \{t_k^{a+}\}_{k=1}^K, \{\rho_k^+\}_{k=1}^K, \{p_k^+\}_{k=1}^K, \{f_k^+\}_{k=1}^K, t_c^+, f_m^+\}$  is the optimal solution to  $\mathbf{P}_6$ . The same way can also be applied to the case with the case  $\sum_{k=1}^K \left( t_k^{b+} \text{B} \log_2 \left( 1 + \frac{\xi \rho_k^+ P_t g_k h_k}{B \sigma^2} \right) + t_k^{a+} \text{B} \log_2 \left( 1 + \frac{p_k^+ h_k}{B \sigma^2} \right) \right) >$

$$\begin{aligned}
\mathcal{L} = & \lambda + \sum_{k=1}^K \frac{f_k T}{C_{\text{cpu},k}} - q \left( \left( t_e + \sum_{k=1}^K t_k^b \right) \left( P_t + P_{\text{sc}} - \eta P_t \sum_{k=1}^K g_k \right) + \eta P_t \sum_{k=1}^K x_k g_k + \varepsilon_m \varphi + \sum_{k=1}^K (P_c t_k^b + y_k + p_c t_k^a + \varepsilon_k f_k^3 T) \right) \\
& - \sum_{k=1}^K \alpha_k \left( t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_t g_k h_k}{t_k^b B \sigma^2} \right) + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) - \beta_k L_{\min,k} \right) + \alpha_0 \left( \frac{\phi}{C_{\text{cpu}}} - \sum_{k=1}^K \beta_k L_{\min,k} \right) + \omega_0 (\phi f_{\max}^2 - \varphi) \\
& + \sum_{k=1}^K \theta_k \left( \frac{f_k T}{C_{\text{cpu},k}} - (1 - \beta_k) L_{\min,k} \right) + \sum_{k=1}^K \varpi_k \left( \eta P_t g_k \left( t_e + \sum_{i=1}^K t_i^b \right) - \eta x_k P_t g_k - P_c t_k^b - y_k - p_c t_k^a - \varepsilon_k f_k^3 T \right) \\
& + \sum_{k=1}^K \omega_k (f_k^{\max} - f_k) + \mu_0 \left( T - t_e - \sum_{k=1}^K (t_k^b + t_k^a) - \sqrt{\frac{\phi^3}{\varphi}} \right) + \sum_{k=1}^K \mu_k (t_k^b - x_k) + \varpi_0 \left( \frac{\phi}{C_{\text{cpu}}} - \lambda \right) \\
& + \theta_0 \left( \sum_{k=1}^K \left( t_k^b B \log_2 \left( 1 + \frac{\xi x_k P_t g_k h_k}{t_k^b B \sigma^2} \right) + t_k^a B \log_2 \left( 1 + \frac{y_k h_k}{t_k^a B \sigma^2} \right) \right) - \lambda \right). \tag{C.1}
\end{aligned}$$

$\frac{t_e^+ f_m^+}{C_{\text{cpu}}}$  and the detailed process is omitted here for brevity. On this basis, Lemma 2 is obtained.

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