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# Development of InGaAs/AlGaAsSb Geiger mode Avalanche Photodiodes

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**Abstract**— Near-infrared linear mode  $\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.56}\text{Sb}_{0.44}$  avalanche photodiodes (APDs) exhibit excellent temperature stability, potentially simplifying Geiger mode operation. We have carried out the first experimental evaluation of  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}/\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.56}\text{Sb}_{0.44}$  APDs in Geiger mode. Characterization on multiple devices included temperature-dependent dark current, avalanche multiplication, dark count rate, afterpulsing, and single photon detection efficiency (SPDE). The temperature coefficient of breakdown voltage extracted from avalanche multiplication data was  $13.5 \text{ mV}\cdot\text{K}^{-1}$ , much lower than InGaAs/InP Geiger mode APDs, reducing changes in operation voltage and offering possible protection from high optical power thermal attack in communication systems. At 200 K, SPDE were 5-16 % with dark count rate of  $1\text{-}20 \text{ Mc}\cdot\text{s}^{-1}$ , comparable to InAlAs and early InP-based Single Photon APDs. The afterpulsing at 200 K was negligible for hold-off time  $> 50 \mu\text{s}$  (reducing to  $5 \mu\text{s}$  at 250 K). These are similar to the performance of InGaAs/InAlAs and some InGaAs/InP Geiger mode APDs. The data reported in this article is available from the ORDA digital repository (<https://doi.org/10.15131/shef.data.24125721>)

**Index Terms**— Single Photon Detection efficiency, AlGaAsSb, Single Photon Avalanche Photodiode, Geiger Mode Avalanche Photodiode, SPAD

## I. Introduction

Single Photon Detection is increasingly important due to rising demands for applications such as laser ranging [1], Quantum Key Distribution [2], and remote gas sensing [3]. The operation wavelength of these applications is often  $\sim 1550 \text{ nm}$ , either to utilize the low-loss window in optical fiber-based systems or to better penetrate atmospheric obscurants in free-space systems. The dominant single-photon detector technologies for  $1550 \text{ nm}$  wavelength photons are Superconducting Nanowires Single Photon Detectors (SNSPDs) [4] and Single Photon Avalanche Diodes (SPADs) [5]. SPADs are Avalanche Photodiodes (APDs) operated in Geiger mode to achieve single photon detection. For both detector technologies, low Dark Count Rate (DCR) and high SPDE are two of the most important performance metrics. SNSPDs provide Single Photon Detection Efficiency (SPDE) as high as 90 % [6] but require cryogenic operation temperature ( $< 4 \text{ K}$ ), limiting widespread adoption. SPDE values of SPADs are much lower, at  $\sim 20\text{--}60 \%$  [7], but they can be cooled using inexpensive and compact thermoelectric coolers. Hence, SPADs remain the single photon detector of choice for many applications.

The most dominant SPADs for  $\sim 1550 \text{ nm}$  wavelength are InGaAs/InP SPADs, which are available commercially from several vendors (e.g. [8] and [9]). They use  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$  (lattice-matched to InP substrates), hereafter referred to as InGaAs, to absorb infrared photons and InP for avalanche breakdown [10-19]. SPADs for  $1550 \text{ nm}$  wavelength are operated beyond their breakdown voltage ( $V_{bd}$ ) and often in gated mode, so DCR, i.e., the number of false counts expected for a free-running device, should be used when comparing SPADs. InGaAs/InP SPADs operated at  $200\text{--}300 \text{ K}$  and  $1550 \text{ nm}$  wavelength have so far resulted in combinations of DCR and SPDE of  $10^2\text{-}10^5 \text{ cps}$  and  $20\text{-}40 \%$ , respectively [7,20]. Incorporating micro-lens and Distributed Bragg Reflector could improve the SPAD's performance (e.g.,  $500 \text{ cps}$  and  $40 \%$  at  $223 \text{ K}$ ) [16]. Reducing temperature can reduce the DCR (e.g.,  $15 \text{ cps}$  at  $163 \text{ K}$ ) [18]. Implementing sinusoidal gating has allowed SPADs to achieve SPDE as high as  $\sim 60 \%$  at room temperature, albeit with high DCRs [13,14].

The InGaAs/InP combination for SPADs originated from the APDs for O- and C-band optical communication systems.

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Although the avalanche material InP benefits from a mature manufacturing process, its temperature stability of avalanche characteristics, characterized by thermal coefficient of breakdown voltage,  $C_{bd}$ , is worse than alternative avalanche materials, such as  $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$ , hereafter referred to as InAlAs [21]. Coupled with potentially higher avalanche breakdown probability, SPADs using InAlAs avalanche layers have been reported [22-27]. However, their performance is still below those of InGaAs/InP SPADs (InAlAs SPADs typically exhibit higher DCR than InP SPADs for a given SPDE). For example, the best  $\text{In}_{0.52}\text{Al}_{0.48}\text{As}$  SPAD exhibits a DCR and SPDE (at 1550 nm wavelength) of 35 Mcps and  $\sim 35\%$  [25], inferior to the InP SPADs.

Recently,  $\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.56}\text{Sb}_{0.44}$  (AlGaAsSb) has emerged as a superior avalanche material for Linear mode APDs, with experimental reports of highly dissimilar ionization coefficients [28] and low excess noise factor [29] (resulting in very low Noise-Equivalent-Power [30]). Reported values of  $C_{bd}$  for a GaAsSb/AlGaAsSb SAM APD are  $\sim 4$  mV/K [31], lower than equivalent InGaAs-based APDs with InP ( $\sim 100$  mV.K<sup>-1</sup>) [32] and InAlAs ( $\sim 20$ -50 mV.K<sup>-1</sup>) [23-26] avalanche layers. Having very low  $C_{bd}$  values is advantageous for achieving SPADs with higher operation stability. This can help counteract changes in required overbias if the SPAD is flooded with high optical power thermal attack, one of the possible attack methods on optical communication systems utilizing SPADs [33]. However, there is as yet no experimental report of SPADs using  $\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.56}\text{Sb}_{0.44}$  avalanche layer. In this work, we report Linear and Geiger mode experimental data of an InGaAs/AlGaAsSb APD operated at 200 K and  $\sim 1550$  nm wavelength.

## II. EXPERIMENTAL DETAILS

The InGaAs/AlGaAsSb APD wafer was grown on a semi-insulating InP substrate using molecular beam epitaxy. The wafers were designed and fabricated into mesa devices (using standard contact lithography and wet chemical etching) by Phlux Technology Ltd [30]. The InGaAs photon absorption layer and the AlGaAsSb avalanche layer are separated by a charge sheet to minimize the electric field and band-to-band tunneling in the InGaAs layer. The devices were characterized in both Linear mode and Geiger mode operation. The former included temperature-dependent Current-Voltage (in the dark and under 1550 nm illumination) and avalanche multiplication ( $M(V)$ ) on devices with 230  $\mu\text{m}$  diameter. The latter included temperature-dependent DCR and SPDE on devices with 50  $\mu\text{m}$  diameter, since SPADs typically have smaller active areas than APDs to reduce the absolute dark currents. All measurements were performed with the device-under-test (DUT) inside a low-temperature probe station.

The forward and reverse Current-Voltage characteristics of three devices were measured using a source-measure-unit with the DUTs at 200 – 300 K. To accurately measure  $M(V)$  against the dark current, a phase-sensitive detection technique was implemented using a modulated laser light (1550 nm wavelength, 50 % duty cycle, and 10 kHz repetition frequency) and a lock-in amplifier.  $M(V)$  data were obtained at 200 – 300 K (using three devices for each temperature) for extraction of  $C_{bd}$ .

The DCR and SPDE measurements were carried out using the setup described in [23]. The DUT was reverse-biased in a gated mode, i.e. voltage pulses (referred to as a.c.) were superimposed onto a d.c. bias, bringing the total bias on the DUT beyond the breakdown voltage only during the voltage pulse duration. The voltage gate pulses had an amplitude of  $\sim 8$  V, on-time ( $t_{on}$ ) of 20 ns (including 5 ns rising edge and 5 ns falling edge), and a repetition rate ( $f$ ) of 100 kHz unless otherwise stated. As the d.c. bias increased (always below  $V_{bd}$ ), the effective overbias on the DUT increased from 0 to 8 V. A combination of a variable capacitor and a differential amplifier were added to reduce capacitive transients within the DUT's avalanche current signals [34], before being detected by an edge-triggered discriminator. The signal from the discriminator was recorded using a counter, yielding a measured count rate ( $C_{rate}$ ).

When the DUT was kept in the dark, gated measurements yielded the raw dark count rate ( $C_{rate\_dark}$ ). The DCR was obtained from

$$\text{DCR} = - \left[ \ln \left( 1 - \frac{C_{rate\_dark}}{f} \right) \right] / t_{on}, \quad (1)$$

derived from  $C_{rate\_dark} = f [1 - \exp(-\text{DCR} \times t_{on})]$  [34]. Afterpulsing was assessed by repeating gated DCR measurements at different  $f$ .

The SPDE measurements used a 1566.5 nm wavelength pulsed laser (pulse width  $\sim 23$  ps) and a variable optical attenuator to adjust the average number of photons per pulse ( $\bar{N}$ ) falling on the DUT ( $\bar{N} = 0.1$ ). The pulsed laser and optical attenuator settings were confirmed through optical power measurement for each SPDE measurement session. SPDE was given by [35]

$$\text{SPDE} = \frac{P_t - P_d}{1 - \exp(-\bar{N})}, \quad (2)$$

where  $P_t$  and  $P_d$  are the probability of detecting an event with and without photon(s) injection, respectively, these were obtained experimentally using  $C_{rate\_off}$ . Eqn. (2) assumes only one event can be recorded in a single voltage pulse period, which is appropriate in our SPDE measurements [36]. For some of our DCR and SPDE measurements, additional data of the avalanche event's timing was recorded using a combination of a delay generator (Ortec 416A), a time-to-amplitude converter (Ortec 566), and a multichannel analyzer (Canberra Multiport II).

## III. RESULTS AND DISCUSSION

Temperature ( $T$ ) dependent Current-Voltage characteristics in the dark are shown in Fig. 1(a). For a given temperature, the data from the three devices are indistinguishable hence mean values are presented. At 300 K the dark current increases rapidly at  $\sim 65$  V, which was later confirmed as the  $V_{bd}$ . At 250 K and below, the dark current increases gradually between 50 V and  $V_{bd}$ , indicating onset of significant impact ionization (also observed in photocurrent increase with reverse bias). This is supported by the analysis of the activation energy of the dark counts presented later. As temperature decreases,  $V_{bd}$  reduces, as expected for AlGaAsSb APDs [31]. Current-Voltage characteristics of the same devices when illuminated with 1550

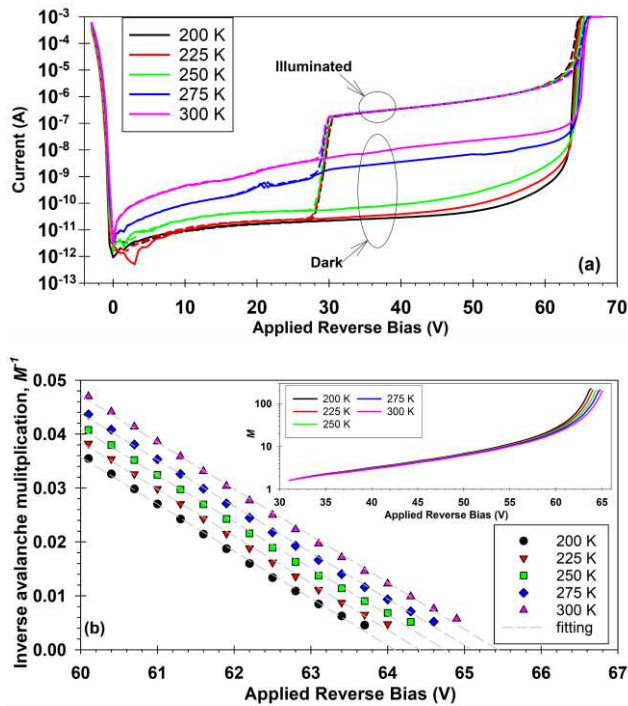


Fig. 1. (a) Reverse Current-Voltage data at 200 to 300 K, under dark (solid lines) and 1550 nm wavelength illumination (dashed lines). (b)  $M^{-1}(V)$  data and linear regression fittings.  $M(V)$  are shown in inset of (b). All data were mean values from three devices (230  $\mu\text{m}$  diameter).

nm wavelength light are also shown in Fig. 1(a). A sudden increase in photocurrent occurred  $\sim 31$  V, indicating punch-through (i.e. the depletion region has reached the InGaAs absorption layer).

The avalanche multiplication data (from three devices) for  $T = 200$  to 300 K are shown in Fig. 1(b)(inset). The data assumed that  $M$  at punch-through for our APD was  $\sim 1.62$ , given by the ratio of our APD's responsivity ( $1.45 \text{ A}\cdot\text{W}^{-1}$ ) to the responsivity of a reference InGaAs/AlGaAsSb APD with a unity gain at the punchthrough voltage ( $\sim 0.90 \text{ A}\cdot\text{W}^{-1}$ ). The largest value of  $M$  obtained reliably was  $\sim 200$ . The mean  $M^{-1}(V)$  data are plotted against  $T$  in Fig. 1(b). Values of breakdown voltage were extracted by extrapolating  $M^{-1}(V)$  to zero for each temperature point, yielding a  $C_{bd}$  value of  $13.5 \text{ mV}\cdot\text{K}^{-1}$ . This is much lower compared to  $\sim 100 \text{ mV}\cdot\text{K}^{-1}$  [32] and  $20\text{-}50 \text{ mV}\cdot\text{K}^{-1}$  [23-26] for InGaAs/InP and InGaAs/InAlAs APDs.

DCR versus overbias data at 200, 225, 250, 275, and 300 K are shown in Fig. 2(a). For a given overbias, the DCR decreases as temperature decreases, with  $\sim 2$  orders of magnitude reduction from 300 to 200 K. The DCR at 200 K is supported by an additional 200 K DCR data from free-running operation (using a 360 k $\Omega$  ballast resistor for passive quenching), also shown in Fig. 2(a). They are in agreement between 0 and 5 V overbias, after which ineffective quenching of the avalanche pulses in free-running operation limits the maximum DUT avalanche counts.

Plotting the DCR versus  $1000/T$  as a function of overbias, as shown in Fig. 2(b), facilitated the extraction of the activation energy ( $E_a$ ) of the DCR using  $\text{DCR} \propto \exp[-E_a/(kT)]$ , where  $k$  is the Boltzmann constant. The corresponding  $E_a$  values obtained are 0.23 to 0.13 eV for  $\Delta V$  of 0.5 to 6.0 V. The largest  $E_a$  value

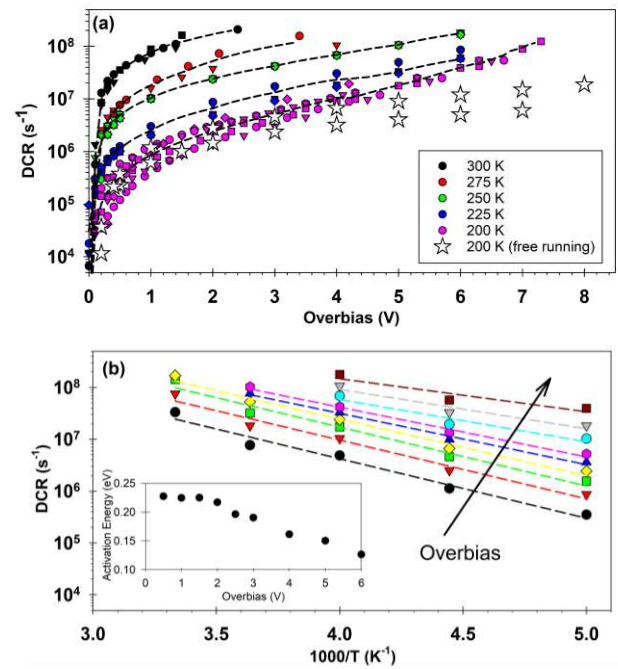


Fig. 2. (a) Normalized DCR data (symbols) and mean values (dashed lines) versus overbias at 200 to 300 K from gated and free running measurements. (b) Normalized DCR versus  $1000/T$  (symbols) and fittings (dashed lines) at 0.5, 1.0, 1.5, 2.0, 2.5, 3.0, 4.0, 5.0, and 6.0 V overbias (from bottom to top). (Inset) activation energy versus overbias. Data were obtained from 50  $\mu\text{m}$  diameter devices.

(0.23 eV) is less than 0.38 eV (half of  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$  bandgap) and  $E_a$  decreases with overbias. The likely cause of the dark counts observed is therefore generation-recombination and tunneling (which has a weak temperature dependence) in the APD or a combination of them [23,37]. Since avalanche breakdown dominates band-to-band tunneling current in AlGaAsSb avalanche regions [38], the source of tunneling currents is likely to be the narrow bandgap InGaAs absorber.

The number of dark counts versus time from a device (obtained using the delay generator, time-to-amplitude converter, and multichannel analyzer) at 200 K and overbias of 1 to 5 V are shown in Fig. 3(a). As the overbias increases, the recorded dark counts occur within a longer time window. This is because the portion of the a.c. voltage pulse above the  $V_{bd}$  increases with the d.c. bias (with a fixed a.c. voltage amplitude), as illustrated in Fig. 3(a)(inset). The data confirm that well-behaved a.c. voltage pulses appeared across the DUT as intended during the gated Geiger mode measurements. Also, the uniform distribution of dark counts versus time indicates that the dark counts originated primarily from bulk dark carriers rather than trapped carriers. The increase in counts at the rising edge for 5 V overbias is attributed to the increase in afterpulsing (as shown later in Fig. 5). We ruled out contributions from capacitive transients since the a.c. voltage amplitude remains constant in these measurements.

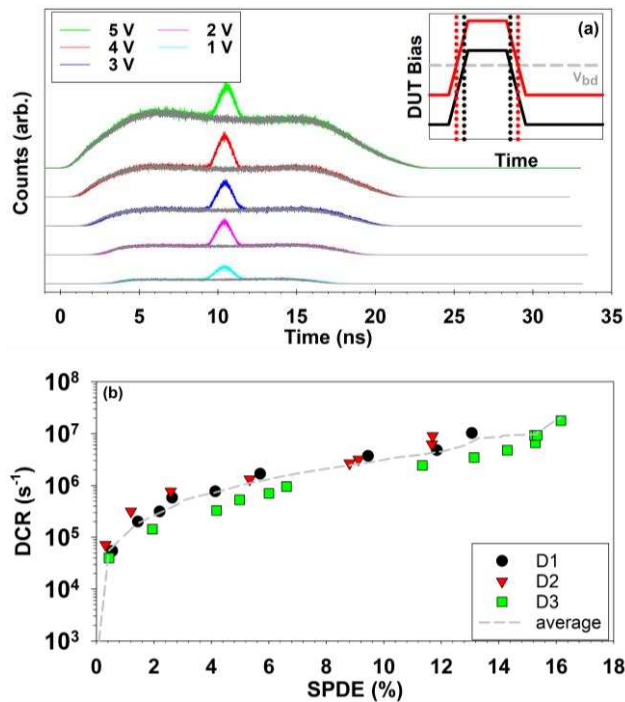


Fig. 3. (a) Counts of avalanche events versus time from DCR (grey) or SPDE measurements (color) at 200 K as a function of overbias. Data from different overbias voltages offset vertically for clarity. (b) DCR versus SPDE data for three 50  $\mu\text{m}$  diameter devices (symbols) and their mean values (line) at 200 K.

The measurements were repeated with an injection of 0.1 photon per pulse. The data (also shown in Fig. 3(a)) are similar to the dark count data, but there are now peaks generated by photon injection. Before and after the photon peak, the number of counts is indistinguishable from those of dark count. From these data, we extracted the values for  $P_i$  and  $P_d$  to calculate SPDE (for three devices) at 200 K. The SPDE values are plotted against DCR in Fig. 3(b). The highest SPDE of  $\sim 16\%$ , with a DCR  $\sim 20$  Mcps, occurred at 5 V overbias. At higher bias, the SPDE started to fall due to high dark counts preventing the detection of signal photons. The InGaAs/AlGaAsSb APDs exhibited increasing SPDE at the expense of increasing DCR. The photon peak duration is dominated by the timing uncertainty of the measurement equipment ( $\sim 1$  ns).

DCR (scaled to device area) versus SPDE data of our InGaAs/AlGaAsSb APDs are compared with reported values

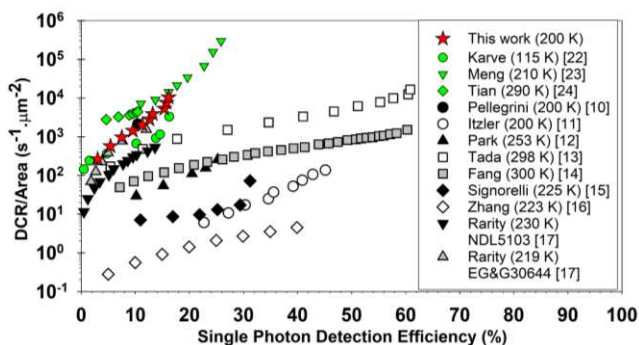


Fig. 4 DCR versus SPDE data from this work (star) and reports using InAlAs [22-24] (green) and InP [10-17] (black/white/grey) avalanche layers.

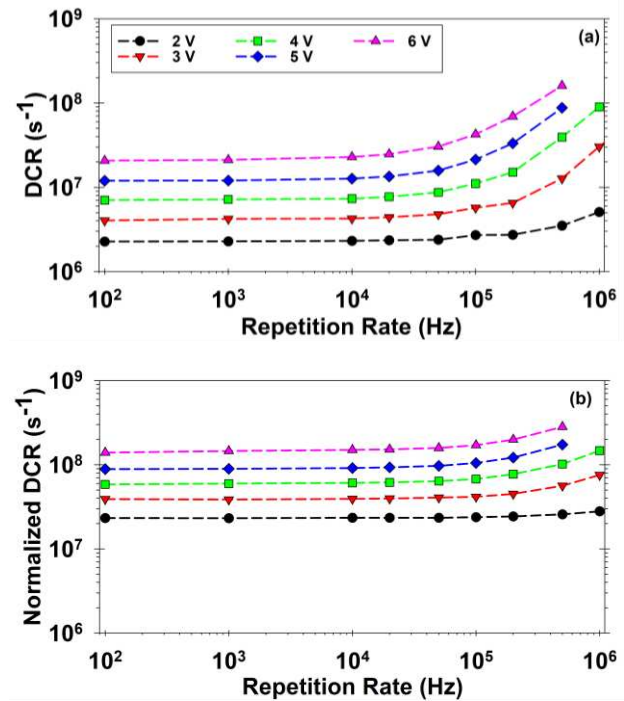


Fig. 5. Mean DCR versus repetition rate characteristics for three 50  $\mu\text{m}$  diameter devices at 200 K (a) and 250 K (b).

for InP [10-17] and InAlAs [22-24] SPADs in Fig. 4. Scaling to device area was necessary because of different device areas used in the various reports. Although this work does not yet match the best SPDE or lowest DCR of the state-of-the-art InGaAs/InP SPADs, the performance of our InGaAs/AlGaAsSb APDs is similar to those of InAlAs SPADs and older InGaAs/InP SPADs [10,17] for both DCR and SPDE. It is encouraging despite the AlGaAsSb APDs originally designed for linear mode operation and having relatively large device areas. Adapting the wafer design for operation beyond the breakdown voltage, optimization in wafer growth, fabrication, and reducing the device areas are expected to produce InGaAs/AlGaAsSb SPADs with lower DCRs than those of this work.

The extent of afterpulsing, an important aspect of 1550 nm wavelength SPADs, in the AlGaAsSb APDs, is evaluated in Fig. 5. The DCR were measured as functions of  $f$  and overbias at 200 K, with a fixed a.c. voltage pulse duration (20 ns). At 1 V overbias, the DCR does not vary with  $f$ . For all other overbias, as  $f$  increases (and hold-off time decreases), the DCR remains constant but starts to increase with  $f$  eventually. The inflection point occurs at a lower frequency for data obtained at higher overbias. At 6 V overbias and 200 K, the DCR increases with  $f$  when  $f \geq 20$  kHz, corresponds to a hold-off time of 50  $\mu\text{s}$ . Similar data obtained at 250 K exhibit the same trends, but the inflection points occur at higher frequencies (e.g. 200 kHz for 6 V overbias). These characteristics are in line with or better than those of InGaAs/InP SPADs and InGaAs/InAlAs SPADs [10, 11, 15].

#### IV. CONCLUSION

We have characterized an  $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}/\text{Al}_{0.85}\text{Ga}_{0.15}\text{As}_{0.56}\text{Sb}_{0.44}$  APD originally designed for Linear mode operation in both Linear and Geiger modes. In Linear mode, the APD achieved  $M \sim 200$  at room temperature when illuminated by a 1550 nm wavelength light. The thermal coefficient of the APD breakdown voltage was found to be  $13.5 \text{ mV}\cdot\text{K}^{-1}$ , much lower than the values of InAlAs and InP SPADs. In Geiger Mode, at 200 K and standard gated mode operation, the APD achieved a maximum SPDE of  $\sim 16\%$  with a DCR  $\sim 20$  Mcps, competitive with InAlAs SPADs. Analyses of the temperature dependence of DCR data indicated generation-recombination current and tunneling are possible contributors to dark count rates at all overbias. Hence, there is potential to improve the performance of InGaAs/AlGaAsSb SPADs through improved wafer design and reduced device area.

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