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# Potential Induced Degradation in Photovoltaic Modules: A Review of the Latest Research and Developments

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**Abstract:** Photovoltaic (PV) technology plays a crucial role in the transition towards a low-carbon energy system, but the potential-induced degradation (PID) phenomenon can significantly impact the performance and lifespan of PV modules. PID occurs when a high voltage potential difference exists between the module and ground, leading to ion migration and the formation of conductive paths. This results in reduced power output and poses a challenge for PV systems. Research and development efforts have focused on the use of new materials, designs, and mitigation strategies to prevent or mitigate PID. Materials such as conductive polymers, anti-reflective coatings, and specialized coatings have been developed, along with mitigation strategies such as bypass diodes and DC-DC converters. Understanding the various factors that contribute to PID, such as temperature and humidity, is critical for the development of effective approaches to prevent and mitigate this issue. This review aims to provide an overview of the latest research and developments in the field of PID in PV modules, highlighting the materials, designs, and strategies that have been developed to address this issue. We emphasize the importance of PID research and development in the context of the global effort to combat climate change. By improving the performance and reliability of PV systems, we can increase their contribution to the transition towards a low-carbon energy system.

**Keywords:** solar photovoltaic; potential induced degradation; PV reliability; PV performance



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## 1. Introduction

The photovoltaic (PV) industry faces a significant challenge in the form of potential-induced degradation (PID) [1–3], which can cause a reduction in the performance of PV modules over time. PID is caused by an electrical potential difference between the front and back electrodes of the PV module and can be triggered by various factors such as humidity, high temperatures, and certain chemicals. PID can have a major impact on the overall performance and efficiency of PV systems, with some studies estimating that it can reduce power output by 30% or more [4,5].

To address this challenge, researchers and manufacturers are continually developing strategies and technologies to prevent or mitigate the effects of PID. This includes the development of new materials and designs for PV modules that are more resistant to PID, as well as improved testing methods and PID prevention strategies.

In this review, we aim to provide a comprehensive overview of the latest research and developments in the field of PID in PV modules. Our focus is on highlighting the advantages and disadvantages of current PID mitigation strategies and exploring the importance of ongoing research efforts to overcome the challenge of PID in the PV industry. We will also examine the mechanisms behind PID and its impact on PV module performance.

Section 2 of this review will explore the mechanisms behind PID, while Section 3 will cover the latest research and developments in materials and designs specifically designed to prevent PID. Section 4 will introduce improved testing methods for detecting PID, and Section 5 will cover new PID-resistant materials and designs. Section 6 will detail the

various causes of PV module PID, and Section 7 will describe three mitigation strategies for PV module PID. Finally, in Sections 8 and 9, the review's discussions and conclusions are presented.

## 2. PID Mechanism

PV systems can experience PID, which leads to decreased performance of PV modules due to an electrical potential difference between their front and back electrodes [6–8]. PID can be caused by multiple factors, including moisture ingress, elevated temperatures, and the presence of certain chemicals. The presence of an electrical potential difference between the module's front and back electrodes is a primary mechanism responsible for PID [9]. Additionally, high temperatures can exacerbate PID by elevating the electrical resistance within the PV module.

The voltage required for PID to occur can vary depending on the contributing factors and the severity of the PID. Typically, PID can be triggered by a voltage of a few hundred volts [10] or higher between the front and back electrodes of the PV module. However, the effect of this voltage on the module's performance can differ based on the extent of PID. As explained in [11], a small voltage (<500 V) may have a negligible impact on the module's performance, while a higher voltage (e.g., 1000 V) can significantly decrease the power output of the module.

The specific voltage that is needed for PID to occur can vary depending on the specific factors that are contributing to PID and the severity of the PID. In general, PID can be caused by a voltage of a few hundred millivolts [10] or more between the front and back electrodes of the PV module. However, the impact of this voltage on the performance of the PV module can vary depending on the severity of the PID. In some cases, as described in [11], a small voltage may have minimal impact on the module's performance, while in other cases, a larger voltage may significantly reduce the module's power output.

There are several methods that can be used to conduct a photovoltaic potential-induced degradation (PID) test on a photovoltaic (PV) module. One common method is to use a PID tester [12], which is a specialized piece of equipment that is designed specifically for testing for PID in PV modules. To conduct a PID test using a PID tester, the PV module is typically connected to the tester in a reverse bias, with the front and back electrodes of the module connected to the tester in opposite polarity. The tester then applies a voltage and current to the PV module, typically at a level that is higher than the maximum voltage and current that the module is expected to encounter under normal operating conditions.

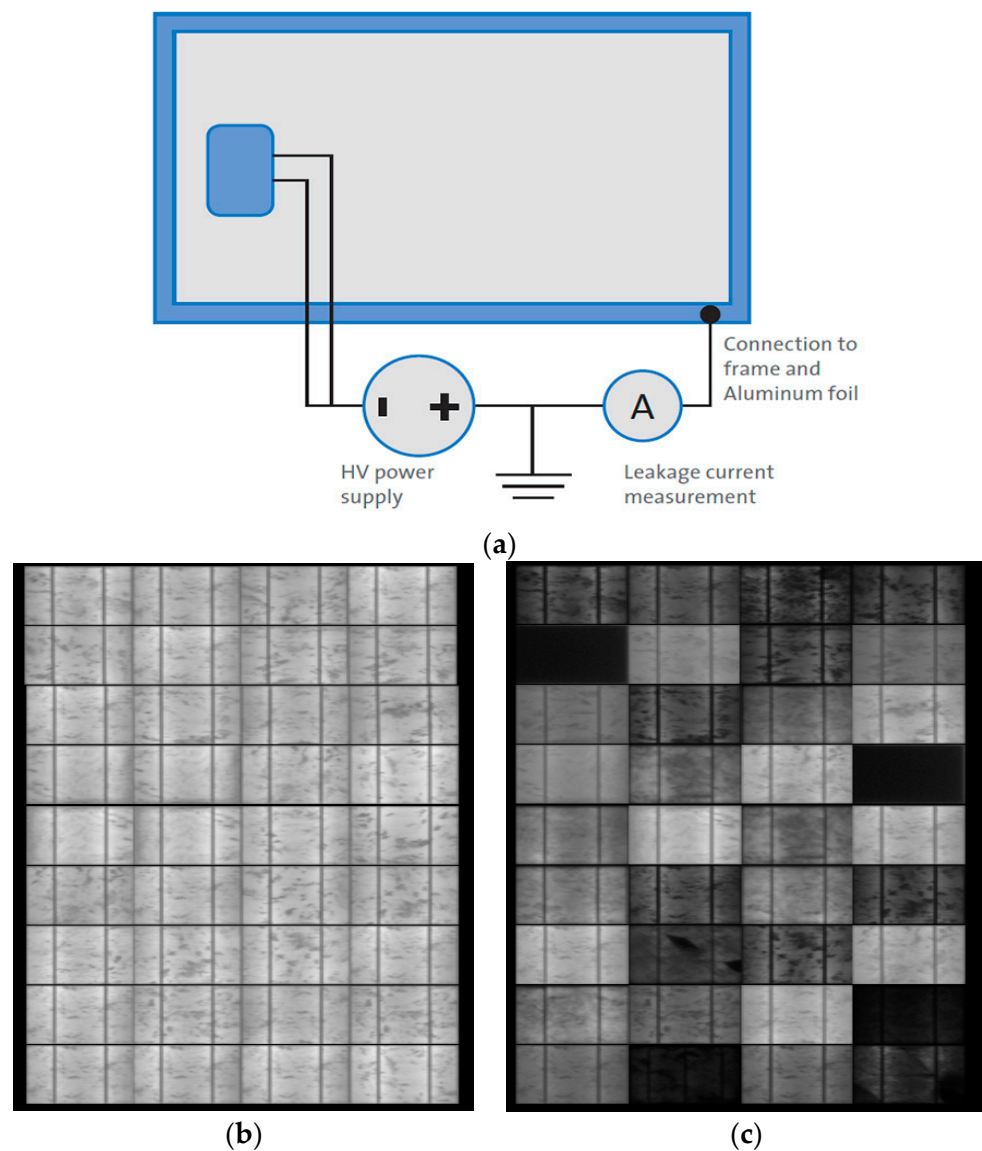
The specific voltage and current that are required to connect with a PV module in a reverse bias for a PID test will depend on the specific tester being used and the characteristics of the PV module. In general, the tester will apply a voltage and current that are high enough to stress the module and simulate the conditions that could lead to PID, but not so high as to damage the module [13,14]. Once the PID test is complete, the tester will typically provide data on the performance of the PV module during the test, including any changes in power output or other parameters that may be indicative of PID. This data can be used to assess the risk of PID in the PV module and identify any potential issues that may need to be addressed.

Electroluminescence (EL) imaging is a technique that is used to evaluate the performance of photovoltaic (PV) modules by measuring the light emission that occurs when an electrical current is applied to the module [15]. EL imaging is typically used to identify defects or issues in PV modules that may be affecting their performance, such as cracks, damaged electrodes, or other issues [16]. This information can be used to identify and address any issues that may be contributing to the durability and reliability issues of the PV modules.

One of the ways in which EL imaging can be used to detect photovoltaic PID in PV modules is by looking for changes in the light emission patterns of the module [17,18]. PID is a phenomenon that can reduce the performance of PV modules due to the presence of an electrical potential difference between the front and back electrodes of the module. This

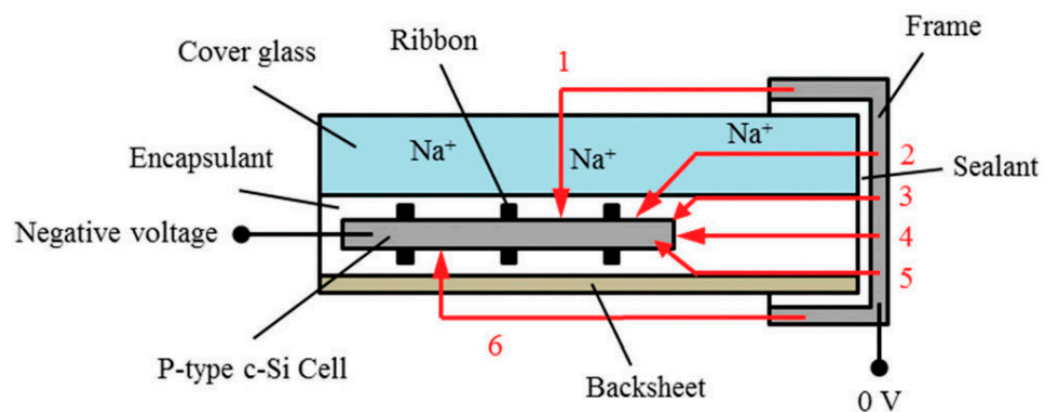
potential difference can cause an electrical current to flow between the electrodes, which can result in changes in the light emission patterns of the module. By analyzing the EL images of a PV module, it is possible to identify any changes in the light emission patterns that may be indicative of PID. This can be useful for identifying PID in PV modules and for determining the severity of the PID [19].

The PID test is done to ensure that manufactured modules will perform over a long period of time under different conditions. For PID testing of solar modules, as shown in Figure 1a, the module is subjected to a temperature of 60 °C with around 85% humidity and under 1000 V load for a period of 96 h. For example, in Figure 1b,c, a polycrystalline silicon PV module was connected in a reverse bias to 1000 V, which is significantly higher than the module's open circuit voltage of 24 V. The test was completed for a duration of 96 h. EL images of the module were taken before and after the PID test was completed. As can be seen in Figure 1b, in comparison with Figure 1c, the PID test significantly reduced the light emissions of the solar cells, indicating a severe case of PID occurring in the module. The reduction in light emissions is a clear indication that the PID has impacted the performance of the module and reduced its ability to generate electricity.



**Figure 1.** (a) Procedure for PID testing. A high-voltage power supply is used to power the PV module, and the leakage current of the module can also be measured. (b) Before the PID test. (c) After 96 h of the PID test.

Ionic migration is one of the mechanisms associated with PID in PV modules. In certain conditions, particularly when subjected to high voltage and high temperature, the migration of sodium ions ( $\text{Na}^+$ ) through the front glass and encapsulant of conventional crystalline silicon PV modules can contribute to PID, as shown in Figure 2. Sodium ions are naturally present in many materials used in PV module construction, such as glass and encapsulant materials. Under normal operating conditions, these ions remain relatively immobile [20]. However, when the module is exposed to elevated voltages,  $\text{Na}^+$  ions can drift through the module structure due to the presence of an electric field. This drift is accelerated by the high-temperature conditions often encountered in PV systems. Leakage currents can flow from the module frame to the solar cells along several different pathways: (1) along the surface of the front glass, and through the bulk of front glass and the encapsulant; (2) through the bulk of front glass (laterally) and through the bulk of the encapsulant; (3) along the interface between the front glass and the encapsulant, and through the bulk of the encapsulant; (4) through the bulk of the encapsulant; (5) along the interface between the encapsulant and the backsheet, and through the bulk of the encapsulant; and (6) along the surface of the backsheet, and through the bulk of the backsheet and encapsulant.



**Figure 2.** The diagram illustrates the internal structure of a typical c-Si PV module, which consists of a glass-encapsulant-cell-encapsulant-backsheet arrangement. It also presents a model showcasing the potential paths for leakage currents. In this setup, the solar cells are maintained at a negative bias, while the module frame is grounded. The arrow depicted in the diagram indicates the direction of these leakage currents. Specifically, during negative voltage potential, such as through path 1, positive ions like sodium ions ( $\text{Na}^+$ ) migrate towards the cells [20].

As  $\text{Na}^+$  ions migrate towards the negatively biased cell, they accumulate near the surface and create a localized electric field. This electric field can lead to a charge imbalance within the cell, affecting its electrical performance. The accumulation of positive charges near the surface can induce a reverse bias in the p-n junction, reducing the cell's output voltage and ultimately its power output. The migration of sodium ions is facilitated by the presence of moisture within the module. Water molecules can provide a pathway for the movement of ions and increase the ionic conductivity of the encapsulant material. As a result, humid environments can exacerbate the ionic migration and enhance the occurrence of PID.

To mitigate PID related to ionic migration, various strategies have been developed. One approach involves the selection of low-sodium-content materials for the encapsulant and front glass to minimize the initial concentration of  $\text{Na}^+$  ions. Additionally, the use of anti-reflective coatings on the front glass can help reduce the intensity of the electric field at the surface and minimize the migration of sodium ions.

Interface charge is another mechanism that can contribute to PID in PV modules. In certain situations, the formation of a charged interface between the silicon wafer and the encapsulant or backsheet material can occur [21], leading to performance degradation. The interface charge phenomenon arises from charge separation at the interface between differ-

ent materials within the PV module. This charge separation can be caused by either ionic conduction or electronic conduction processes. In some cases, impurities or contaminants present in the encapsulant or backsheet materials can facilitate the formation of charged species at the interface [22].

When a charged interface is formed, it creates an electric field within the module structure. This electric field can lead to various effects on the PV cell's performance. One significant consequence is the reduction in the cell's output voltage. The presence of the electric field can induce a reverse bias across the p-n junction, reducing the effective voltage available for the photovoltaic conversion process. As a result, the power output of the cell decreases, leading to a decline in the module's overall performance.

### 3. Areas of PID Research and Development

In recent years, there has been a significant amount of research focused on understanding and mitigating PID. Some of the key findings and developments in this area are presented in Figure 2, described as follows:

**Improved PID testing methods:** Researchers have also developed new methods for testing the susceptibility of PV modules to PID, which can help manufacturers and installers identify and mitigate potential issues before they occur. **New materials and designs for PV modules:** Researchers have been exploring the use of new materials and designs for PV modules that are more resistant to PID. For example, some studies have found that using materials with a higher resistance to moisture ingress, such as fluorine-doped tin oxide (FTO) or metal-plated glass, can help to reduce the risk of PID. **Finally, PID prevention strategies:** Researchers have identified several strategies that can be used to prevent PID, such as sealing the edges of PV modules to prevent moisture ingress, using appropriate ventilation to reduce temperatures, and avoiding the use of certain chemicals that can contribute to PID.

### 4. Improved PID Testing Methods

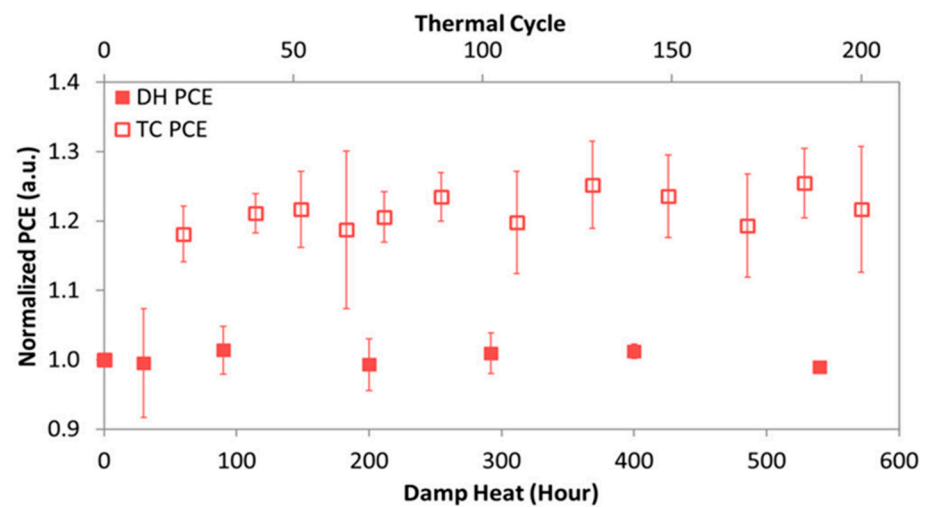
Improved PID testing methods refer to methods that have been developed specifically to test the susceptibility of PV modules to photovoltaic PID. There are several different testing methods that have been developed for this purpose. Some examples are discussed next.

#### 4.1. Temperature and Humidity Cycling

In this method, the PV module is subjected to temperature and humidity cycles, simulating field conditions. This test aims to replicate the conditions PV modules encounter in the field, including temperature and humidity changes, to identify potential PID issues. The module is placed in a controlled environment, typically a chamber [23], where temperature and humidity levels can be precisely controlled. Multiple cycles of varying temperature and humidity levels are applied to the module over a set duration, often several days. This ensures an adequate number of cycles. Following the test, the PV module is assessed for any performance changes or issues indicative of PID. Evaluation techniques include measuring power output, analyzing electrical characteristics, or examining EL images.

In a recent study [24], metal halide perovskite solar cells (PSCs) were subjected to rapid thermal and damp heating tests to assess their stability under different conditions. The results, shown in Figure 3, indicate that under all thermal cycles (ranging from 0 to 200) and under damp heat (ranging from 0 to 600 h), the normalized power conversion efficiency (PCE) remained stable at around 1 to 1.2, representing significant progress in the verification of PSC technology.

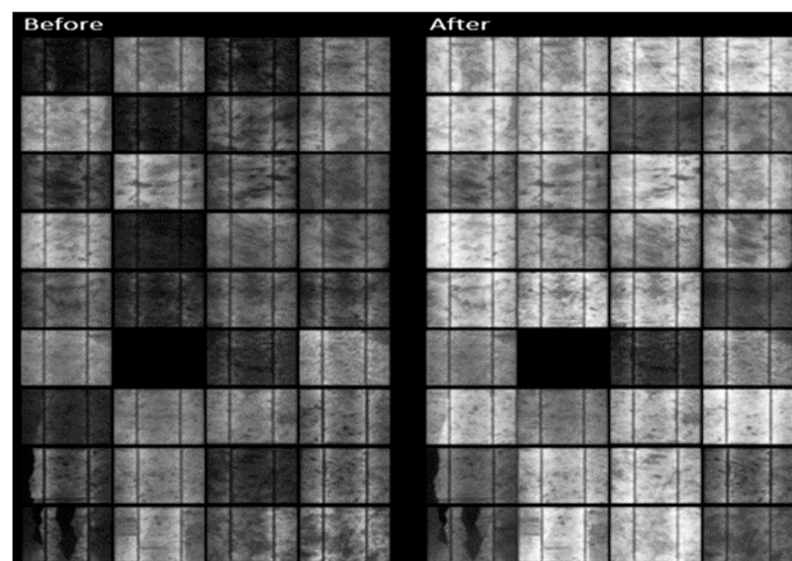




**Figure 3.** Damp heat and thermal cycling test results for method 3-encapsulated PSCs [24].

#### 4.2. Direct Current Bias Test

A direct current (DC) voltage is applied to the PV module to simulate conditions that could lead to PID by stressing the module. To conduct a DC bias test, the PV module is typically connected to a DC power source, with the front and back electrodes of the module being connected to the power source in opposite polarity [25]. A DC voltage is then applied to the module, typically at a level that is higher than the maximum voltage that the module is expected to encounter under normal operating conditions. The DC bias test is typically conducted for a set duration of time, often several days or more, to ensure that the module has been subjected to sufficient stress. A recent study [26] examined the impact of a direct current bias test on the performance of PID modules. As shown in Figure 4, the authors demonstrated that when a direct current bias was applied to a PID-affected module for 10 days, the EL showed improved light emissions, indicating an improvement in the performance of the solar cells. Overall, the DC bias test is a useful method for testing for PID in PV modules and for identifying any issues or defects that may be contributing to PID. It can be used in combination with other testing methods to provide a more complete understanding of the performance and reliability of PV modules.



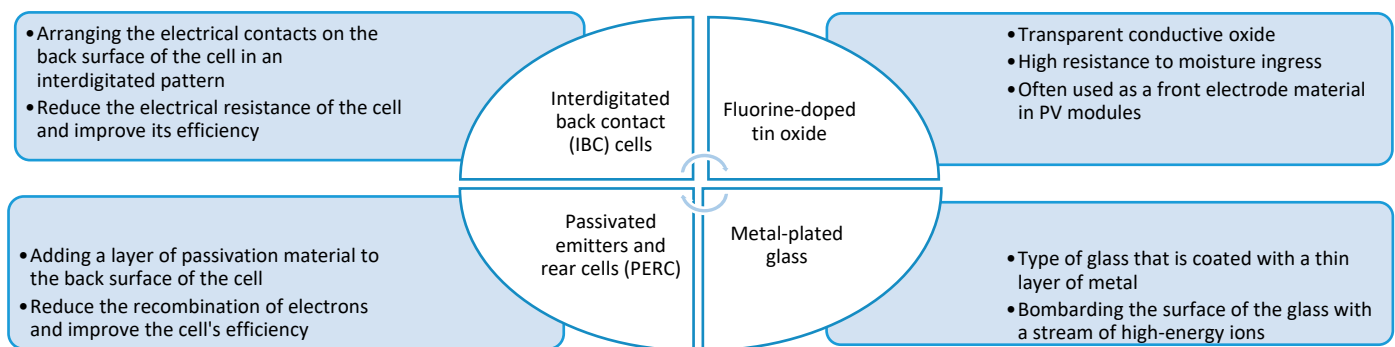
**Figure 4.** EL images before the PID recovery and after 10 days of the PID recovery, when a direct current bias test was applied [26].

### 4.3. Accelerated Aging Test

This method involves subjecting the PV module to extreme conditions, such as high temperatures, humidity, or ultraviolet (UV) radiation, in order to accelerate the aging process and simulate the conditions that the module may encounter over its lifetime [27,28]. To conduct an accelerated aging test, the PV module is typically placed in a chamber or other controlled environment, where the temperature, humidity, and UV radiation levels can be precisely controlled. The module is then subjected to a series of extreme conditions, with the temperature, humidity, and UV radiation levels being varied over a range of values. The module is typically tested for a set duration of time [29], often several days or more, to ensure that it has been subjected to a sufficient number of temperature and humidity cycles.

## 5. New Materials and Designs for PV Modules, Especially Designed to Prevent PID

There have been several new materials and designs for photovoltaic (PV) modules developed in recent years, with the goal of improving their performance and reducing the risk of issues such as photovoltaic PIV. Some examples of new materials and designs that have been developed include those summarized in Figure 5.



**Figure 5.** New materials and designs for PV modules, especially designed to prevent PID.

**Fluorine-doped tin oxide (FTO):** FTO is a type of transparent conductive oxide [30] that has a high resistance to moisture ingress and has been shown to be effective at preventing PID. It is often used as a front electrode material in PV modules. FTO is a doped version of tin oxide ( $\text{SnO}_2$ ) [31], in which a small amount of fluorine is introduced into the crystal structure of the tin oxide. This helps to improve the electrical conductivity of the material, making it more suitable for use as an electrode in PV modules. FTO has several properties that make it well-suited for use in PV modules. One of the main advantages of FTO is its transparency, which allows it to transmit light effectively and enables it to be used as an electrode in transparent PV modules. FTO also has a relatively low resistivity, which helps to reduce losses due to electrical resistance in the module.

**Metal-plated glass:** Metal-plated glass is another material that has been shown to be effective at preventing PID. It is typically used as the front sheet of the PV module, and the metal coating helps to prevent moisture ingress and reduce the risk of PID. Metal-plated glass is a type of glass that is coated with a thin layer of metal [32], typically using a process known as sputtering. This process involves bombarding the surface of the glass with a stream of high-energy ions, which causes the metal atoms to become dislodged from their source material and to be deposited onto the surface of the glass. The thickness of the metal layer can be precisely controlled by adjusting the sputtering process, and the metal layer can be made as thin as a few nanometers or less.

**Passivated emitters and rear cells (PERCs):** PERCs are a type of PV cell design that involves adding a layer of passivation material to the back surface of the cell [33,34], which helps to reduce the recombination of electrons and improve the cell's efficiency. It has been found to be effective at reducing the risk of PID. PERC technology is based on the



principle of passivation, which refers to the process of reducing the electrical activity at the surface of a material. By adding a layer of passivation material to the back surface of the PV cell, it is possible to reduce the recombination of electrons and holes at the surface of the cell, which helps to improve the efficiency of the cell. There are other benefits to using PERC technology in PV modules other than preventing PID. One of the main benefits is that it can significantly improve the efficiency of the PV cells [35], which can translate into higher power output and increased energy production from the PV system. In addition, as described by [36,37], PERC technology can help to improve the reliability and long-term performance of PV modules, as it helps to reduce the risk of defects or issues that may impact the performance of the cells.

Interdigitated back contact (IBC) cells: IBC cells are a type of PV cell design that involves arranging the electrical contacts on the back surface of the cell in an interdigitated pattern [38–40]. This can help to reduce the electrical resistance of the cell and improve its efficiency and has also been found to be effective at reducing the risk of PID. There are several steps involved in creating an interdigitated back contact (IBC) for a photovoltaic (PV) cell:

- Start by preparing the substrate material for the IBC. This typically involves cleaning and etching the surface of the substrate to create a rough texture that will help to promote adhesion between the substrate and the IBC layers.
- Next, apply a layer of metal to the substrate using a process such as sputtering or evaporation. This layer will form the bottom electrode of the IBC.
- Apply a layer of passivation material to the substrate, using a process such as plasma-enhanced chemical vapor deposition (PECVD) [41]. This layer helps to reduce the recombination of electrons and holes at the surface of the cell, which improves the efficiency of the cell.
- Repeat steps 2 and 3 to create additional layers of metal and passivation material, building up the IBC structure as desired. The thickness and number of layers can be varied to suit the specific requirements of the PV cell.
- Once the IBC has been built up to the desired thickness, the PV cell is typically passivated and sealed to protect it from the environment. This typically involves applying a layer of encapsulant material to the cell to seal it and prevent moisture or other contaminants from entering the cell.

## 6. PID Mitigating Strategies

To prevent and mitigate PID, it is important to identify and address the underlying causes of the phenomenon. This may involve designing PV modules to be more resistant to PID, using materials and construction techniques that are less prone to PID, and implementing proper installation and maintenance procedures to reduce the risk of PID.

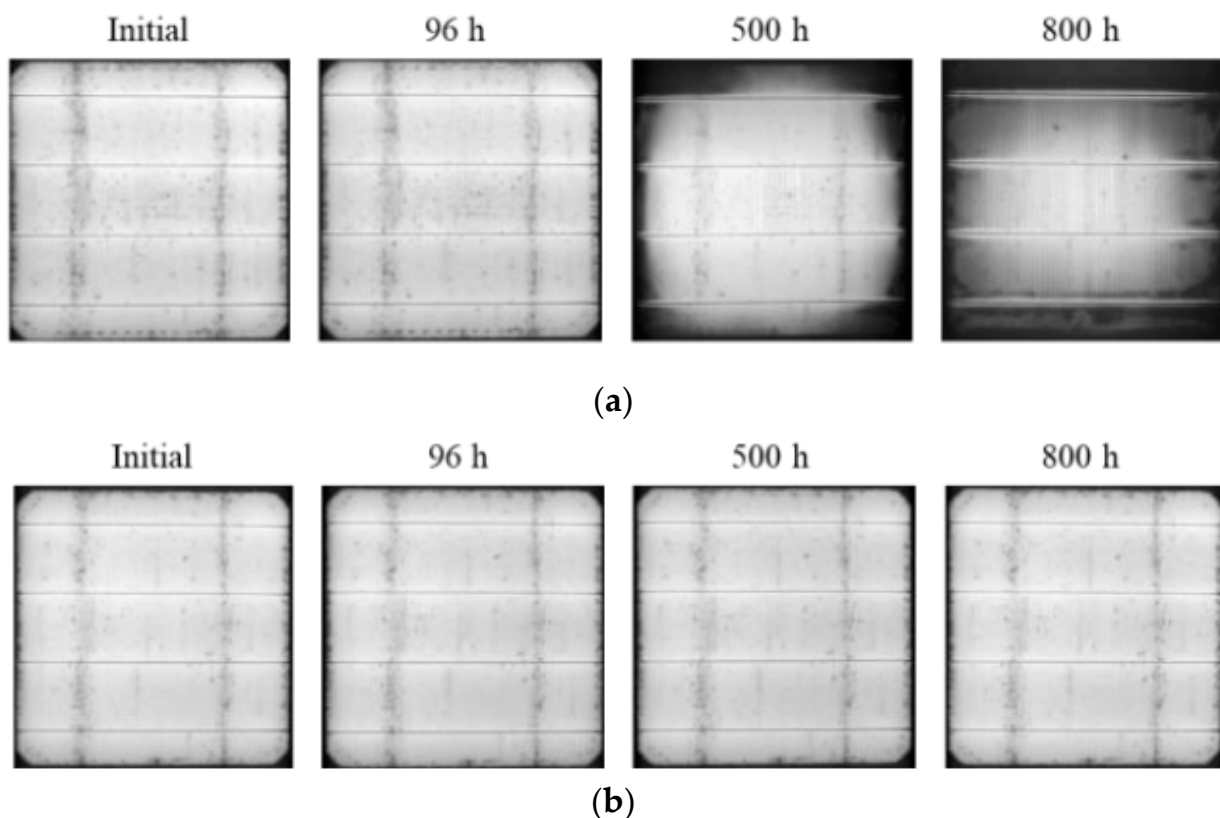
### 6.1. Sealing the Edges of PV Modules to Prevent Moisture Ingress

A main cause of PID is moisture ingress, which occurs when water enters the PV module through gaps or seams in the module. To prevent moisture ingress, it is important to seal the edges of the PV module to prevent water from entering the module [42]. To seal the edges of PV modules, the following steps can be taken:

- Clean the edges of the PV module to remove any dirt, debris, or other contaminants that may interfere with the sealing process.
- Inspect the edges to identify any gaps or seams that may allow moisture to enter the module.
- Apply a sealant material to the edges of the PV module, using a sealant gun or other suitable applicator. The sealant should be applied in a continuous bead around the edges of the module, taking care to fill in any gaps or seams that may allow moisture to enter the module.

- Allow the sealant to cure according to the manufacturer's instructions. This may involve waiting for a specified amount of time or exposing the sealant to heat or other conditions to accelerate the curing process.
- Inspect the sealed edges of the PV module to ensure that the sealant has been applied correctly and that there are no gaps or seams that may allow moisture to enter the module.

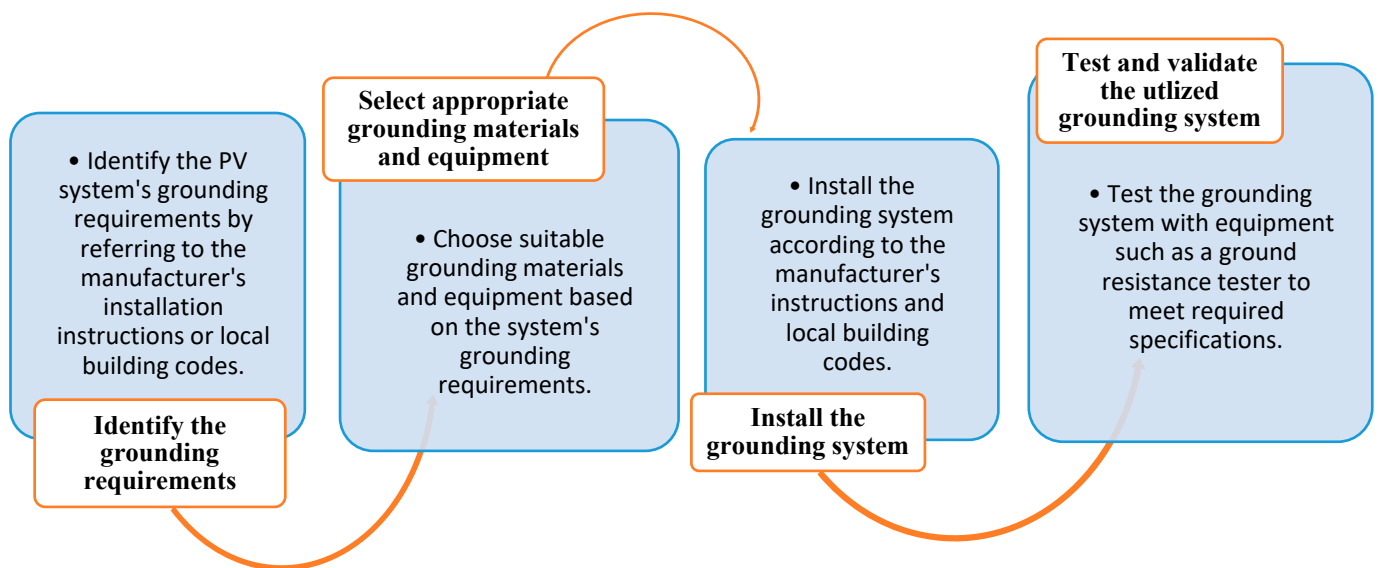
According to a recent paper [43], the degradation of glass/glass (G/G) mini modules begins at the edges after 500 h of testing (as shown in Figure 6a). This degradation gradually expands the inactive area. In contrast, the use of an edge sealant configuration in G/G modules prevents degradation due to PID, as illustrated in Figure 6b.



**Figure 6.** EL images of mini-PV modules before and after 1000 h of PID test at 85 °C/85% RH/−1000 V [43]. (a) G/G, and (b) G/G with edge sealant.

### 6.2. PV Installation Grounding Techniques

Another way to prevent and mitigate PID is to use proper grounding techniques that are important in the installation of PV modules to ensure the safety of the system and minimize the risk of PID. Proper grounding techniques involve properly grounding the PV module and other components of the PV system to ensure that they are connected to the earth and are at the same electrical potential as the earth. There are several steps involved in using proper grounding techniques when installing a PV system, and these are presented in Figure 7, including key steps: identify the grounding requirement for the PV system, select appropriate grounding materials and equipment, install the grounding system, and finally test and validate the utilized grounding system.



**Figure 7.** Grounding techniques/strategy when installing a PV system to prevent potential PID.

### 6.3. PV Maintenance and Fault Detection Strategy

It is possible to prevent and mitigate PID by implementing proper installation and maintenance procedures. Performing regular maintenance to identify and address any issues contributing to PID may involve the use of proper installation techniques and performing regular maintenance to ensure that the PV system is installed correctly.

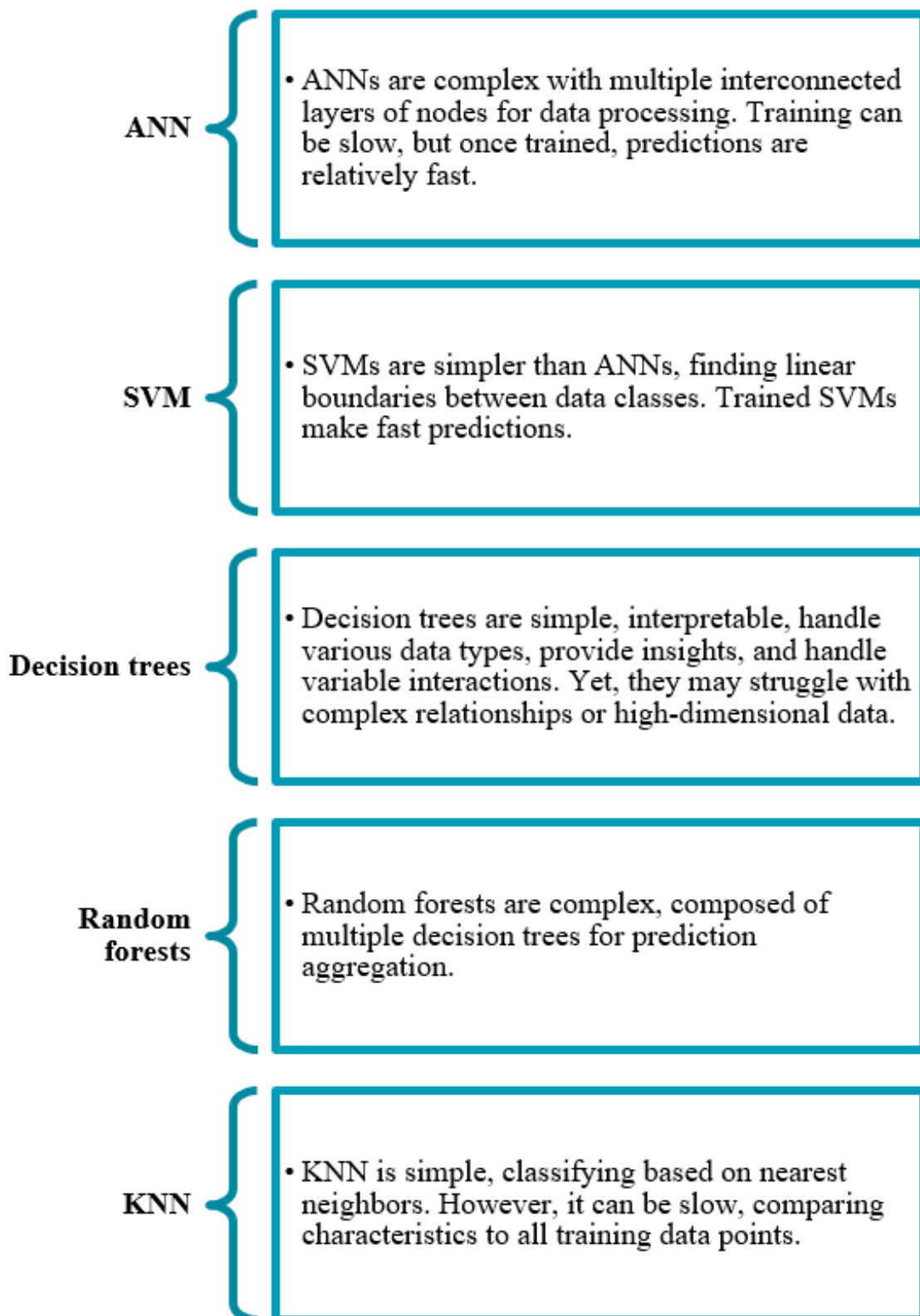
There are several algorithms that have been developed for detecting faults in PV systems. These algorithms can be trained to recognize specific types of faults based on historical data and analyze the electrical behavior of PV systems to identify deviations from normal operating conditions that may indicate the presence of a fault. Some examples of algorithms used for PV fault detection include the followings:

- Maximum power point tracking (MPPT) algorithms [44,45]: these algorithms optimize the power output of PV systems and can also be used to detect faults by analyzing the output of PV panels and identifying deviations from expected behavior.
- Wavelet transforms: these algorithms can be used to analyze the frequency spectrum of PV system data and identify anomalies that may be indicative of a fault [46–48].
- Current and voltage waveform analysis: techniques such as fast Fourier transforms (FFTs) [49] can be used to analyze the waveforms of current and voltage in PV inverters and identify deviations that may be caused by a fault.

In addition to the above-mentioned PV fault detection techniques, there are several statistical-based methods that can be used to detect PV faults. The first method is descriptive statistics, which involves calculating summary statistics such as mean, median, and standard deviation for a set of PV system data and using these statistics to identify patterns or anomalies that may indicate the presence of a fault [50]. The second method is data visualization, such as the work presented by [51]. This method involves using plots and charts to visually represent PV system data and using these visualizations to identify patterns or anomalies that may indicate the presence of a fault. The last method is called hypothesis testing. It involves formulating a hypothesis about the cause of a fault and using statistical tests to determine whether the data support or refute the hypothesis.

In comparison with mathematical-based models, statistical methods are generally less complex and easier to understand, but they may not be as accurate or efficient at detecting PV faults. Mathematical-based models, such as machine learning (ML) algorithms, can often analyze data more accurately and quickly, but they may be more complex and require more data to be effective. Ultimately, the choice between statistical and mathematical-based methods will depend on the specific needs and constraints of the PV system. The five most

common ML-based PV fault detection techniques are presented below, and summarized in Figure 8:



**Figure 8.** Comparison between the five most common ML-based algorithms used for PV fault detection.

Artificial Neural Networks (ANNs) are modeled after the human brain and are particularly good at analyzing patterns in data and making decisions based on this information. ANNs can analyze data from PV systems to identify deviations from expected behavior indicating the presence of a fault [52]. However, ANNs require a large amount of data to be effective and may not perform well if the data are noisy or of poor quality.

Support Vector Machines (SVMs) classify data points based on their characteristics [53]. They can handle high-dimensional data [54] and perform well even when the number of data points is relatively small.

Decision Trees build a tree-like model of decisions based on certain features of the data [55]. Random Forests, based on the idea of building multiple decision trees and aggregating their predictions, are powerful and widely used tools for PV system fault detection and diagnosis [56–58]. They can handle high-dimensional data and perform well even when the data are noisy or have a complex structure. However, they can be computationally intensive to train, so they may not be the best choice in situations where speed is a critical factor.

K-nearest neighbors (KNNs) classify data points based on the characteristics of their nearest neighbors. It is easy to implement and understand [59–61] and can handle high-dimensional data. However, it can be computationally intensive to find the K nearest neighbors for each data point [62].

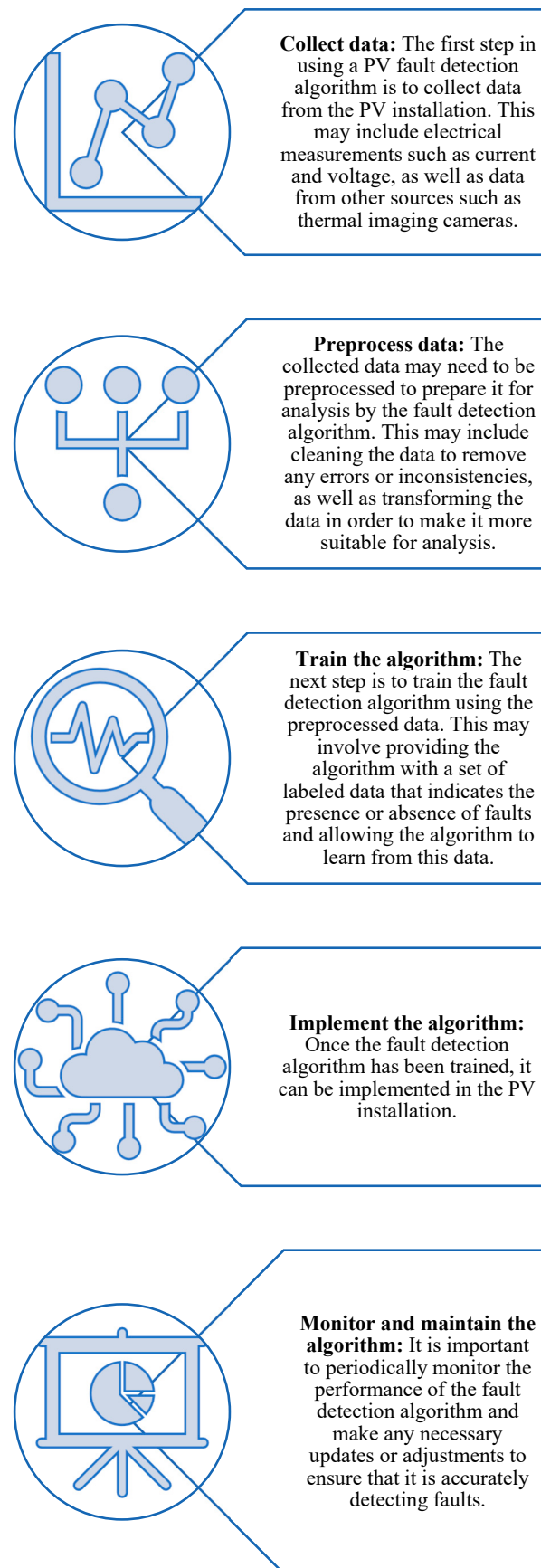
The complexity and speed of PV fault detection can vary among different ML algorithms. ANNs have been shown to be more effective for PV fault detection in some cases. One reason for this is that ANNs can process and analyze large amounts of data more efficiently than KNNs or SVMs. Another reason for the effectiveness of ANNs for PV fault detection is their ability to learn and adapt to new data.

One of the main concerns with ANNs is their susceptibility to overfitting. Techniques such as regularization and cross-validation can mitigate this, but it is important to monitor the performance of the model to ensure that it is not overfitting. ANNs can be computationally intensive, particularly for large and complex datasets, and they may require specialized hardware or cloud resources to train and deploy effectively. SVMs and KNNs are generally less computationally demanding and can be trained and deployed on more modest hardware.

Finally, the interpretability of the algorithm may be important for some applications. ANNs are often considered to be “black box” models, meaning that it can be difficult to understand how the model is making its predictions.

There are several steps that can be taken to practically utilize a PV fault detection algorithm in a PV installation, and the step-by-step instructions are described in Figure 9. To practically utilize a PV fault detection algorithm in a PV installation, there are several steps that need to be taken, including collecting data, preprocessing the data, training the algorithm, implementing the algorithm, monitoring the algorithm, and maintaining the algorithm. Collecting data involves gathering information from the PV system while preprocessing the data involves filtering and cleaning the collected data. Training the algorithm involves teaching the algorithm how to recognize normal and abnormal operating conditions of the PV system. Implementing the algorithm involves integrating it into the control system of the PV installation. Monitoring and maintaining the algorithm involve regularly checking the algorithm’s performance and updating it with new data to improve its accuracy over time.





**Figure 9.** Step-by-step guide on how to implement PV fault detection algorithm in a PV installation.

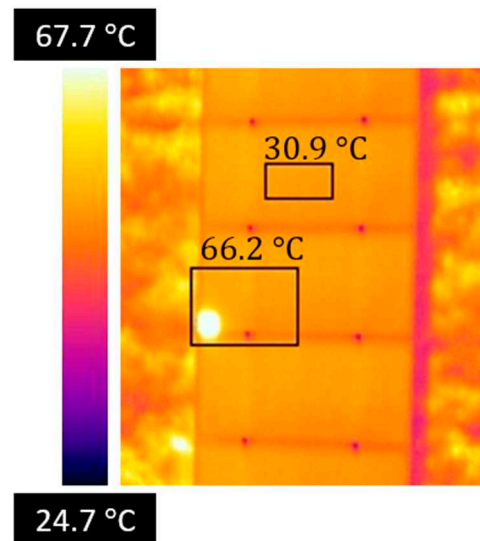
## 7. Factors That Contribute to PID in PV Modules

While PID can have a number of causes, two of the most common are PV hot spots and PV shading, and these factors will be described in this section.

### 7.1. PV Hotspots

A hot spot in a solar cell is an area on the cell where the temperature is significantly higher than the surrounding areas. Hot spots can be caused by a variety of factors, including high levels of sunlight, high electrical current, and poor ventilation. Hot spots can lead to several problems in solar cells, including reduced power output, shortened lifespan, and permanent damage [63–65]. In some cases, hot spots can even cause the cell to catch fire. Hot spots are a particular concern in PV modules because they can lead to potential-induced degradation (PID).

Hot spots in solar cells can lead to PID when the temperature of the hot spot becomes high enough to cause a voltage difference between the solar cell and the module's frame. This voltage difference can cause an electrical current to flow through the module [66], leading to the formation of hot spots on the cells. As an example, Figure 10 illustrates the impact of a single hotspot on a PV module, where the temperature of the hotspot reaches 66.2 °C, compared to the temperature of an adjacent free hotspot PV module, which is 30.9 °C. The hot spots can then cause further degradation, leading to a decrease in the power output of the module and potentially causing permanent damage to the cells. Hot spot PID is typically caused by a combination of factors, including high temperatures, high humidity, and high electrical current. It can be prevented or mitigated through the use of proper design techniques and materials that are resistant to PID.



**Figure 10.** PV module with hotspot. This image is taken using a thermal drone, EVO II Dual 640T. A thermal camera has a sensitivity of 3 °C or 3% of the reading (whichever is greater). The image was taken when the solar irradiance was 733 W/m<sup>2</sup>.

There are several approaches that can be taken to mitigate hot spots in PV modules, including (i) design for proper ventilation: ensuring that there is sufficient airflow around the PV module can help to dissipate heat and reduce the likelihood of hot spots [67], (ii) use materials with high thermal conductivity: materials with high thermal conductivity, such as aluminum [68], can help to dissipate heat more effectively and reduce the likelihood of hot spots, (iii) use solar cells with low thermal resistance: solar cells with low thermal resistance will be less prone to hot spots because they are better able to dissipate heat, (iv) install shading devices: installing shading devices, such as louvers or reflective panels, can help to reduce the amount of direct sunlight that reaches the PV module and reduce the

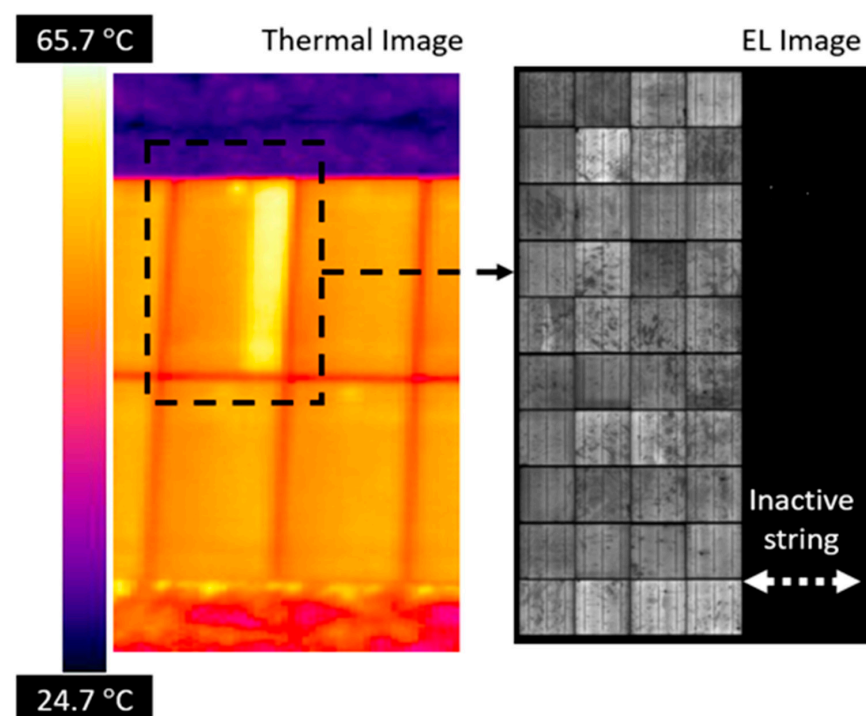
risk of hot spots [69,70], and (v) implement active cooling systems: in some cases, it may be necessary to use active cooling systems, such as fans or water cooling [71], to dissipate heat and prevent hot spots.

### 7.2. PV Shading

Shading is a common problem in PV systems, and it can have a significant impact on the performance of PV modules. When a PV module is partially or fully shaded, the cells in the shaded area are unable to generate electricity as efficiently as unshaded cells [72–75]. This can lead to a voltage difference between the shaded cells and the unshaded cells, which can cause PID in the module. PID caused by shading can be particularly problematic because it is often difficult to detect. Shading may not be consistently present, and it may be difficult to identify the specific cells that are affected. As a result, the effects of shading on PID may not be immediately apparent, and the degradation may not be detected until it has already caused a significant reduction in the power output of the module.

There have been several studies that have investigated the impact of shading on PID in PV modules [76–78]. One study found that shading can significantly increase the likelihood of PID [76], particularly when the shading is severe or occurs over a long period of time. Another study found that shading can cause both hot spot PID and light-induced degradation (LID) in PV modules [77,78], with LID being more likely to occur in cells that are shaded for extended periods.

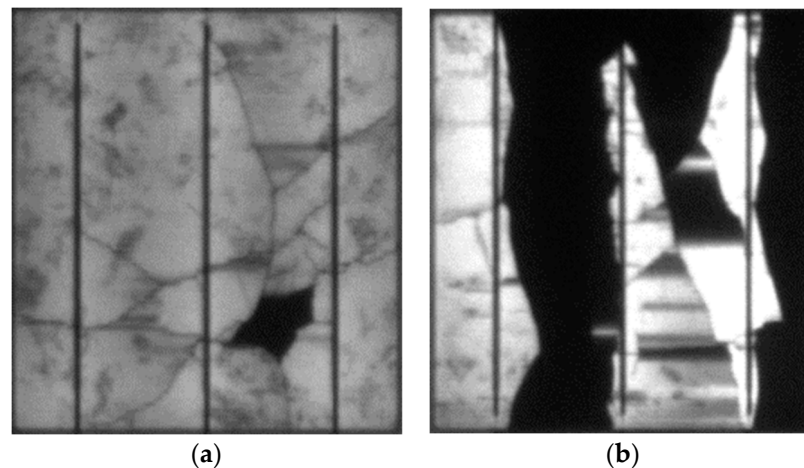
When a PV module is permanently shaded, meaning that it consistently receives less sunlight than it is designed for, it can lead to several problems. One problem is that the module may have an inactive bypass diode [79]. Bypass diodes are used to prevent a PV module from overheating when it is shaded. If a PV module is permanently shaded, the bypass diode may not activate, resulting in a short circuit in the sub-string. This can cause the sub-string to heat up, reducing the output power generation of the module by one third. As shown in Figure 11, a PV module has a heated sub-string due to its being affected by permanent shade on one of its sub-strings. During an EL test, the inactive bypass diode caused the module to lose one third of its power. The images shown in Figure 11 were taken at night at a PV test facility located at the University of York in the United Kingdom.



**Figure 11.** PV module with an inactive bypass diode due to permanent shade affecting the module.

### 7.3. PV Cracks

Solar cell cracks are cracks or fractures that can occur in PV modules, as shown in the example of a solar cell in Figure 12a. They can be caused by a variety of factors, including mechanical stress, thermal expansion and contraction, and environmental factors such as extreme temperature fluctuations or moisture [80–82]. PV cracks can lead to several problems, including reduced power output, decreased efficiency, and shortened lifespan. In some cases, PV cracks can even cause the module to fail completely (Figure 12b).



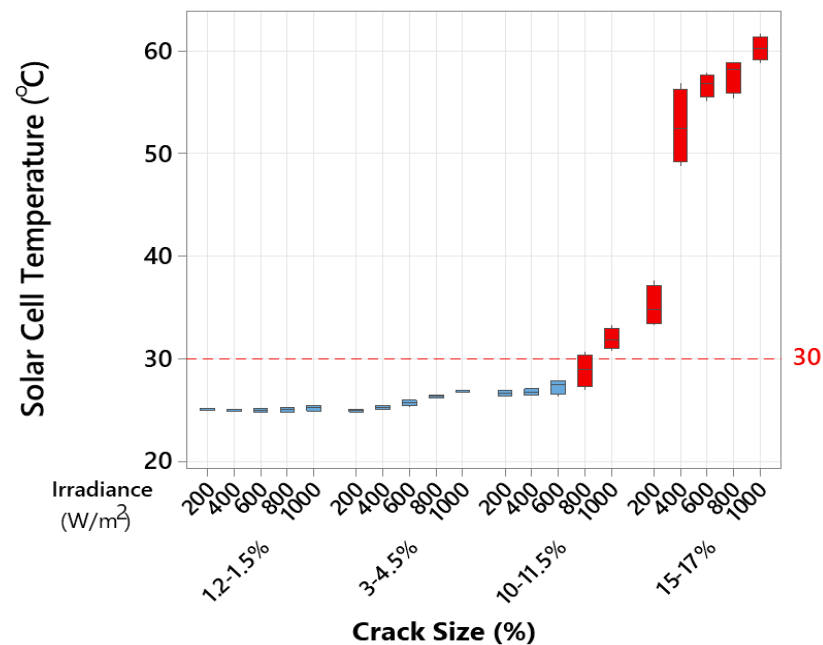
**Figure 12.** EL image of solar cells affected by cracks. (a) Minor crack in the middle of the solar cell, (b) major crack formation resulting in the solar cell failing to produce any output current.

PV cracks can develop in several different ways, depending on the specific cause. For example, mechanical stress can cause PV modules to crack when they are subjected to external forces, such as bending [83]. Thermal expansion and contraction can cause PV modules to crack when the temperature changes significantly, such as when the module is exposed to extreme heat or cold [84]. Environmental factors such as extreme temperature fluctuations or moisture can also cause PV modules to crack, particularly if the module is not properly sealed or protected from the elements [85].

The cracks in solar cells can lead to the development of PV hotspots for several reasons. First, PV cracks can allow moisture to enter the module, which can lead to the formation of hot spots when the moisture is trapped within the module. Second, PV cracks can create an electrical short circuit, which can cause an increase in electrical current and the formation of hot spots. Finally, PV cracks can cause a reduction in power output, which can lead to an increase in temperature and the formation of hot spots. According to a recent study [86], the larger the crack size, the more likely it is for a solar cell to develop a hotspot, as illustrated in Figure 13. In addition, cracks in solar cells can cause hotspots to develop because they can allow electrical current to bypass the normal resistance of the cell, resulting in a concentration of current in a small area. This concentration of current can cause the temperature of the cell to increase, leading to a hotspot. Hotspots can be damaging to solar cells because they can reduce the overall efficiency of the cell and may even cause the cell to fail.

There are several ways to prevent cracks from occurring in solar cells and to mitigate the potential damage caused by cracks that do occur. One important factor is to design the PV system with crack resistance in mind. This may involve using materials and design approaches that are less prone to cracking, such as using flexible solar cells or using reinforcement to increase the strength of the cell [87,88]. Proper sealing and protection can also help to prevent damage from environmental factors such as extreme temperatures, moisture, and UV radiation [89]. Regular inspection and maintenance are other important factors in preventing PV cracks. Inspecting the PV system on a regular basis can help to identify potential problems before they become serious, and addressing these problems

early can help to prevent cracks from occurring or from causing significant damage. In addition, proper handling and transportation techniques can also help to prevent PV cracks from occurring. This may involve using protective packaging to prevent damage during transportation and handling the modules carefully to avoid mechanical damage [90].



**Figure 13.** Solar cell temperature vs crack size in (%) [86]. Red squares indicate that the crack size has a significant impact on the temperature of the solar cell (>30 degrees), whereas blue circles indicate that the crack size has no significant impact.

## 8. Discussion

This section discusses several areas related to PID in PV modules that require further investigation. These areas include but are not limited to improved diagnosis and testing methods, an improved understanding of the causes of PID, the development of new materials and techniques for preventing PID, and long-term testing and performance evaluation.

### 8.1. More Accurate Diagnosis and Testing Methods

There is a need for more reliable and accurate methods for detecting and diagnosing PID in PV modules. Current methods are not always effective at identifying the extent of PID damage, which makes it difficult to determine the best approach for mitigating the damage. Currently, there are a few methods available for detecting and diagnosing PID in PV modules. These methods include electrical testing methods, such as IV curve measurement and power output testing, as well as visual inspection methods, such as the use of imaging technologies or other non-destructive testing methods. However, current methods for diagnosing PID are not always effective at accurately identifying the extent of PID damage, particularly in cases where the damage is subtle or not easily visible. This can make it difficult to determine the best approach for mitigating PID, and it can also lead to the use of inappropriate or ineffective remedies.

### 8.2. Improved Understanding of the Causes of PID

The basic mechanisms of PID are understood, but there is still much to learn about the specific factors that contribute to PID in PV modules. These factors may include the type of PV cells used, the materials and design of the PV module, the environment in which the module is installed, and other factors. Further research is needed to better understand the interactions between these factors and how they lead to PID. This could involve studying



the effects of different materials and design approaches on the likelihood of PID occurring, as well as the effects of different environmental conditions on PID.

### *8.3. Development of New Materials and Techniques for Preventing PID*

There is a need for new materials and techniques that are more resistant to PID. These could include new types of cells, new encapsulation material (i.e., Ethylene vinyl acetate (EVA), Polyvinyl butyral (PVB), and Polyethylene terephthalate (PET)), and new design approaches that reduce the likelihood of PID occurring. These approaches could involve changes to the materials or layout of the PV module, or the use of different techniques for mounting or installing the module.

### *8.4. Long-Term Testing and Performance Evaluation*

Long-term testing and performance evaluation: Many of the current studies on PID in PV modules have focused on short-term performance, but there is a need for more long-term data on the effects of PID on the lifespan and overall performance of PV modules. Understanding the long-term effects of PID on PV modules is important for a few reasons. First, PID can have a significant impact on the power output and efficiency of PV modules, and this impact may change over time. By studying the long-term effects of PID, it is possible to better understand how PID affects the performance of PV modules and to develop more accurate models for predicting and mitigating the effects of PID. Second, PID can have a significant impact on the lifespan of PV modules. By studying the long-term effects of PID on PV modules, it is possible to better understand how PID affects the lifespan of PV modules and to identify ways to extend the lifespan of PV modules by preventing or mitigating PID. Overall, long-term testing and performance evaluation are important areas of research in the field of PID in PV modules, and there is a need for more data on the long-term effects of PID on the performance and lifespan of PV modules.

### *8.5. Standardisation of Testing Methods and Performance Metrics*

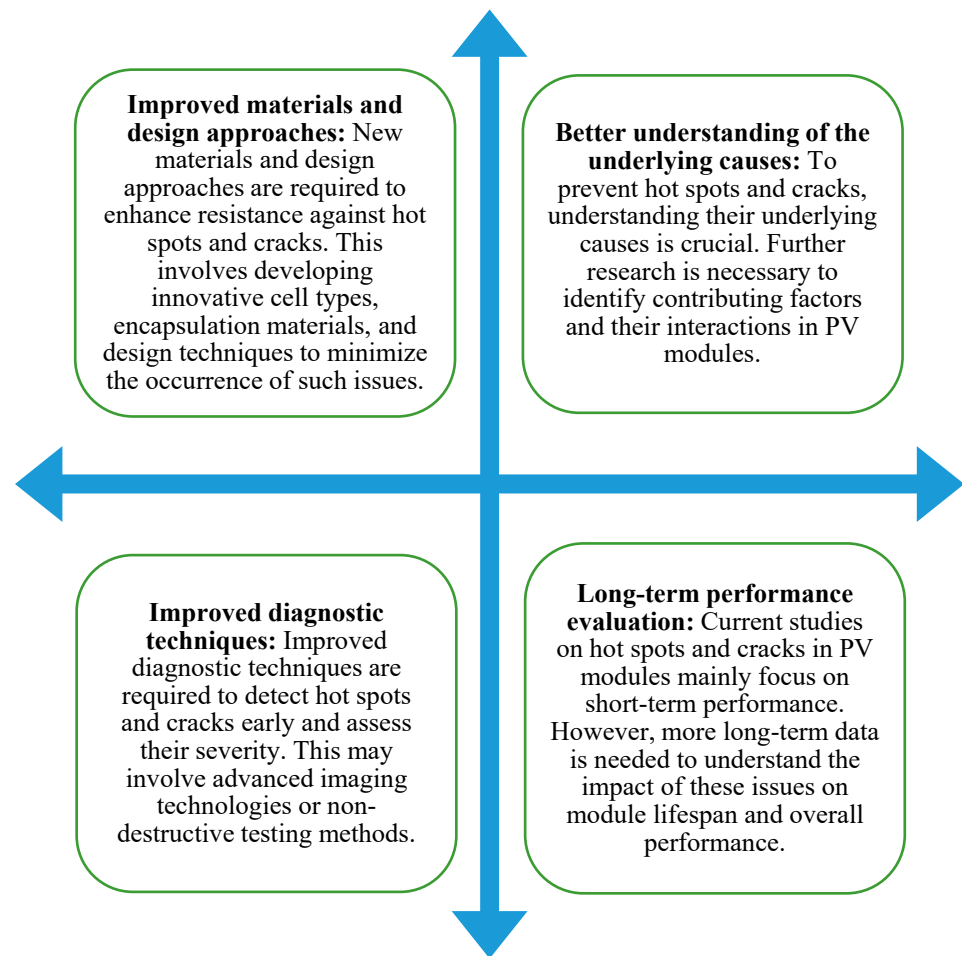
*Improved Understanding of the Causes of PID*

There is currently a lack of standardization in the methods used to test for and evaluate the performance of PV modules in the context of PID. Standardization would help to ensure that results from different studies are comparable and could facilitate the development of more accurate models for predicting PID in PV modules. For example, if all studies used the same testing methods and performance metrics, it would be easier to compare the results and to identify trends or patterns that may not be apparent when using different methods. Standardization would also help to ensure that the methods used to test for and evaluate PID are reliable and consistent, which is important for ensuring the accuracy of the results. There are several ways that standardization could be achieved in the field of PID in PV modules. One approach would be to develop and adopt a set of standardized testing methods and performance metrics that are widely accepted within the industry. Another approach would be to establish a certification or accreditation program for PID testing laboratories, which would ensure that the methods used by these laboratories meet certain standards of quality and reliability.

### *8.6. PV Hotspots and Cracks Preventions*

PV hotspots and cracks are two types of problems that can lead to potential-induced degradation (PID) in photovoltaic (PV) modules. Hot spots occur when the temperature of a PV module exceeds a certain threshold, and they can be caused by a variety of factors, including electrical shorts, moisture, or other problems. Cracks in PV modules can also lead to PID, and they can be caused by mechanical stress, thermal expansion and contraction, and other factors. There are currently key gaps in our knowledge about how to prevent hot spots and cracks in PV modules. To address these gaps, there are several key areas of research and development that could be pursued, as shown in Figure 14, including

(i) improved materials and design approaches, (ii) a better understanding of the underlying causes, (iii) improved diagnostic techniques, and (iv) long-term performance evaluation.



**Figure 14.** Future research area (gap of knowledge) to prevent PV hotspots and cracks.

#### 8.7. Overall Assessment of Power Loss Caused by PID

In Table 1, the reviewed papers mostly report on the power losses resulting from PID on PV modules and the conditions under which they were tested. The reported power losses after PID testing varies greatly, from 6.3% to 35%. The conditions that were used to test PID also vary from paper to paper; some papers use environmental chambers to simulate conditions that are similar to those experienced during actual field operations [91,92], such as high temperatures and humidity, and others investigate PID in operating PV plants. Some papers also reported different methods, such as EL, IR, and I-V curve measurements, which were used to assess the impact of PID on PV modules [93–95].

The conditions that have the greater impact on the PID test can be the humidity, temperature, and duration of exposure to the voltage bias. Moreover, as an example, Ref. [96] reported that the power losses are found to be higher when the PID test is done under light illumination, which is closer to the actual conditions that PV modules are subjected to when they're in the field. In general, this comparative assessment suggests that PID can have a significant impact on the performance of PV modules and that the losses can vary depending on factors such as the weather, the type of PV technology used, and the conditions the module is under.

**Table 1.** An overview of key research papers on PV system power and PID.

	Year of Publication	Reported PV Power Losses	Comments/Observations/PID-Test
[91]	2020	35% after 120 h of PID test (or described as PID stress time)	The PV modules were tested in an environmental chamber at 85 °C and 85% humidity with a –1000 V negative voltage bias. After 20 h, the modules were evaluated using a flash tester to record I-V curves and calculate electrical parameters.
[92]	2021	Rear-side PID in the dark and under rear illumination cause power losses of 6.3% and 8.8%, respectively	A PID testing setup, including a high voltage source, temperature-controlled sample holder, light source, and measurement electronics, was used to evaluate the rear side of a bifacial solar cell. The testing lasted two hours, replicating real field conditions and considering light absorption in passivating layers.
[93]	2022	In the three PV modules examined, PID accounts for nearly 10% of the power loss	Researchers assessed PID in a 22.2 kW PV plant with monocrystalline silicon technology located in a hot semi-arid climate. Inspection techniques such as electroluminescence (EL), thermography (IR), and I-V curves were used to evaluate the impact on affected modules.
[94]	2022	The average power loss is 25% for the 28 tested PV modules	A total of 28 PV modules were subjected to damp heat conditions in a test chamber at 85% humidity and 85 °C. A –1000 V negative voltage was applied, and aluminum foil was placed on the front surface. EL images and I-V curves were measured at 0, 48, and 96 h following the IEC62804 standard.
[95]	2022	PID-affected PV modules lose more than 35% of their power	A 1.2 MW installation in Barcelona, Spain, was tested to investigate the development and impact of PIDs on module performance. Data were monitored and logged using the ICONICA inverter at a sampling rate of 1 sample/10 min.
[96]	2023	Depending on the weather, it can range from 10% to 40% outside, and from 10% to 40% indoors	The study compared indoor and outdoor tests on n-type front emitter (n-FE) crystalline silicon PV modules for potential-induced degradation. The indoor temperature of 85 °C showed a higher acceleration factor for polarization-type PIDs (PID-p) compared to outdoor conditions. PID-p declined on rainy days and recovered on sunny days.

## 9. Conclusions

PID is a complex phenomenon that can significantly impact the performance and lifespan of PV modules. While progress has been made in understanding and mitigating PID, there are still several areas that require further investigation and action. Our recommendations to address PID in PV modules are as follows:

- **Standardized Testing Methods:** Develop standardized testing methods and performance metrics for PID to enable accurate diagnosis and evaluation of PID in PV modules.
- **Research and Development:** Increase research efforts to improve our understanding of the causes of PID and to develop new materials and techniques for preventing PID.
- **Long-term Testing:** Conduct long-term testing and performance evaluation of PV modules to better understand the impact of PID on their lifespan and reliability.
- **Industry Partnerships:** Promote partnerships and collaborations between researchers and industry to accelerate the development and deployment of PID-resistant PV technologies.
- **Funding:** Provide funding for research and development projects related to PID in PV modules to support the development of new solutions and innovations.

By focusing on these specific recommendations, we can make progress towards addressing PID in PV modules and improving the performance and reliability of PV systems. It is important for governments and industries to support these efforts through funding, partnerships, and collaborations, to ensure that PV technology can make the greatest possi-

ble contribution to the global effort to combat climate change. We encourage continued investment in research and development efforts to better understand and address PID in future PV deployment.

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