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# State-variable and cyclic-averaging analysis of bidirectional CLLC resonant converters

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**Abstract:** The study proposes the application of state variable and cyclic-averaging modelling techniques for analysis of bidirectional, dual active bridge, CLLC resonant converters. The techniques are applied for converters operating under single phase-shift modulation in forward and reverse modes and the equation description is obtained for both models. The design of the converter is presented and the simulation results obtained are compared to a Spice-based simulation to verify the accuracy of the proposed models. Results show the models are suitable to represent the behaviour of CLLC converters under single phase-shift modulation. In addition, when using the cyclic-averaging technique integration is not required to solve the model, which results in a considerably rapid analysis when compared to the execution times of state variable and Spice-based models.

# 1 Introduction

The interest in electric vehicles (EVs) has been increasing especially due to their reduced  $CO_2$  emission levels, government incentives and increased affordability. Most battery chargers for EVs are unidirectional, allowing power flow only from the grid to the vehicle; however, vehicle-to-grid (V2G) technology requires bidirectional power flow between grid and battery. Consequently, the battery may be available for grid support and to maintain stability at peak times. Typical bidirectional chargers for V2G consist of an AC–DC converter for power factor control and a DC–DC converter for output voltage and current regulation [1].

The dual active bridge (DAB) is a widely used topology of bidirectional DC–DC converter for V2G applications [2–4]. Due to the DAB limitations such as large reactive current, limited operating range and reduced efficiency, resonant variants were proposed to increase efficiency and improve operation for wide input/output voltage ranges [5].

Among various resonant topologies based on the DAB converter, the CLLC converter is a fourth-order resonant topology that provides reduced switching losses, high efficiency and operation under wide voltage range. A considerable advantage of this configuration is the easily achieved soft switching operation for forward and reverse modes when using frequency modulation [6].

To date, most literature focuses on frequency modulated CLLC converters [1, 6, 7]. However, for applications requiring fixed-frequency operation the frequency modulated control strategy cannot be applied. As an alternative, phase shift modulation can be implemented [8, 9], in this case the phase shift between the bridges or between each leg of the bridges is used to control power flow and direction.

This paper investigates a CLLC bidirectional converter operating under single phase-shift (SPS) modulation, Fig. 1, when only the phase shift between the output voltages of the two active bridges is used to control the output power.



Fig. 1 CLLC bidirectional resonant converter topology

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Modelling techniques are employed for converters analysis to facilitate the design process and predict performance. A linear state-variable model using dq transformation for CLLC converters is proposed in [9].

This study proposes two modelling techniques for time-domain analysis of the converter: state variable and cyclic averaging. Here, a simpler non-linear state-variable model is proposed and will serve as base for the implementation of the cyclic-averaging method.

The cyclic-averaging technique was proposed in [10] as an accurate method of time-domain analysis for periodically switched systems. The steady-state values of the state variables are obtained from analytical equations instead of integration. Therefore, steady-state prediction is obtained rapidly when compared with integration-based methods. Previously, cyclic averaging has been used to analyse LLC [11] and inductor-capacitor-capacitor (LCC) converter [12], here then, this is extended to apply cyclic averaging analysis to fourth-order CLLC converters, specifically regarding phase-shift modulated variants.

The paper is structured as follows. In Section 2, the statevariable model is presented. The cyclic-averaging model is described in Section 3. Section 4 contains the converter design and analysis of results.

# 2 State-variable analysis

Since the converter analysed in this paper is bidirectional, two modes of operation will be studied. In forward mode, the power flows from the DC link, represented in Fig. 1 as the voltage source,  $V_{\rm dc}$ , to the battery, represented by the source  $V_{\rm battery}$ . In reverse mode, the battery serves as a supply to the grid and the power flows from the battery to the DC bus.

#### 2.1 State-variable analysis for forward operation

For the state-variable analysis, the converter from Fig. 1 is divided into two sub-systems [12]. The resonant network is considered the fast sub-system and the output filter constitutes the slow subsystem, with these two sets of state-variable equations being connected by a coupling equation that represents the non-linear behaviour of the output bridge.

The equivalent circuit for state-variable analysis in forward mode is presented in Fig. 2.

The fast sub-system is referred to the primary while the slow is referred to the secondary. The square voltages generated by the two full bridges are represented as  $v_a$  and  $nv_b$ , where *n* is the

transformer's turns ratio. The resistances of switches and voltage sources are considered to have negligible effect on the voltage generation at the output of each bridge, therefore:

$$v_a = \pm V_{\rm dc} \tag{1}$$

$$nv_b = \pm nV_{\text{bat}} \tag{2}$$

Using basic circuit analysis, the state-variable description can be obtained.

The fast sub-system can be described by the following state equations:

$$\frac{di_{LS1}}{dt} = \frac{v_a - (r_1 + r'_2)i_{LS1} - v_{CS1} + r'_2i_{Lm} - v_{CS2} - nv_b}{L_{S1}}$$
(3)

$$\frac{\mathrm{d}v_{\mathrm{Cs1}}}{\mathrm{d}t} = \frac{i_{\mathrm{Ls1}}}{C_{\mathrm{s1}}} \tag{4}$$

$$\frac{di_{Lm}}{dt} = \frac{r'_{2}i_{Ls1} - (r_{Lm} + r'_{2})i_{Lm} + v_{Cs2'} + nv_{b}}{L_{m}}$$
(5)

$$\frac{dv_{Cs2'}}{dt} = \frac{i_{Ls1} - i_{Lm}}{C_{s2'}}$$
(6)

The slow sub-system is described as

$$\frac{\mathrm{d}v_{\mathrm{Cf}}}{\mathrm{d}t} = \frac{V_{\mathrm{bat}} + r_b i_{\mathrm{bridge}} - v_{\mathrm{Cf}}}{C_f (r_b + r_{\mathrm{Cf}})} \tag{7}$$

The coupling equation is obtained considering the operation of the active bridge on the output side:

$$i_{\text{bridge}} = \begin{cases} n(i_{\text{Ls1}} - i_{\text{Lm}}) & \text{when } v_b > 0\\ -n(i_{\text{Ls1}} - i_{\text{Lm}}) & \text{when } v_b < 0 \end{cases}$$
(8)

#### 2.2 State-variable analysis for reverse operation

Now considering the opposite power flow direction, the equivalent circuit for reverse mode analysis is shown in Fig. 3 and since the output is now on the primary side both fast and slow sub-systems are referred to the primary.

The fast sub-system can be described by the following equations:

$$\frac{di_{Ls1}}{dt} = \frac{nv_b - (r_1 + r'_2)i_{Ls1} - v_{Cs1} + r'_2i_{Lm} - v_{cs2} - v_a}{L_{s1}}$$
(9)

$$\frac{\mathrm{d}v_{\mathrm{Cs1}}}{\mathrm{d}t} = \frac{i_{\mathrm{Ls1}}}{C_{\mathrm{s1}}} \tag{10}$$

$$\frac{di_{Lm}}{dt} = \frac{nv_b - r'_2 i_{LS1} - (r_{Lm} + r'_2) i_{Lm} - v_{CS2'}}{L_m}$$
(11)

$$\frac{dv_{Cs2'}}{dt} = \frac{i_{Ls1} + i_{Lm}}{C_{s2'}}$$
(12)

The slow sub-system is described as

$$\frac{\mathrm{d}v_{Ci}}{\mathrm{d}t} = \frac{V_{\mathrm{dc}} + r_{\mathrm{dc}}i_{\mathrm{bridge}} - v_{Ci}}{C_i(r_{\mathrm{dc}} + r_{Ci})} \tag{13}$$

The coupling equation is given by

$$i_{\text{bridge}} = \begin{cases} i_{\text{Ls1}} & \text{when } v_a > 0\\ -i_{\text{Ls1}} & \text{when } v_a < 0 \end{cases}$$
(14)

## 3 Cyclic-averaging analysis

Cyclic averaging is a technique used to model periodically switching systems. Due to the switching characteristic of these systems, the state vector does not converge to a fixed value in steady state. Therefore, the steady-state behaviour is obtained by analysing a cycle, and averaging the values of the state variables using the proposed method [10].



Fig. 2 Equivalent circuit for forward mode



Fig. 3 Equivalent circuit for reverse mode

The CLLC system presented in this paper is considered cyclic because the state vector at the beginning and at the end of the switching period is equal, therefore:

$$x(t) = x(t + nT) \tag{15}$$

where T is the period of the input voltage and n is the number of cycles.

Based on the state of the input voltages  $v_a$  and  $nv_b$ , each cycle can be divided into operating modes and a state-variable description is obtained for each mode resulting in a set of piecewise linear equations. Each i mode has the following statevariable representation:

$$\dot{x}(t) = A_i x(t) + B_i \tag{16}$$

where  $\dot{x}(t)$  is the state vector,  $A_i$  is the dynamic matrix and  $B_i$  is the excitation term.

Here, the circuit operates in each mode for a fixed period. The time interval for each mode is given by a duty cycle  $d_i$ . If T is the period of a cycle, the circuit operates in mode *i*, during  $d_i$  T seconds. For each operation mode, the state vector  $\dot{x}(t_i)$  may be calculated analytically using the following equation:

$$\dot{x}(t) = e^{A_{i}(t-t_{i-1})}x(t_{i-1}) + \int_{t_{i-1}}^{t} e^{A_{i}(t-\tau)}B_{i}d\tau$$

$$\dot{x}(t_{i}) = e^{A_{i}d_{i}T}x(t_{i-1}) + \int_{t_{i-1}}^{t_{i}} e^{A_{i}(t_{i}-\tau)}B_{i}d\tau$$

$$x(t_{i}) = \phi_{i}x(t_{i-1}) + \Gamma_{i}$$
(17)

where:

i

$$\phi_{i} = \phi(t_{i}, t_{i-1}) = e^{A_{i}d_{i}T}, \Gamma_{i} = \int_{t_{i-1}}^{t_{i}} e^{A_{i}(t_{i}-\tau)} B_{i} d\tau = (e^{A_{i}d_{i}T} - I)A_{i}^{-1}B_{i} \text{ if } A_{i}$$
 is invertible.

The calculation of these terms is possible but complex, especially the integral term for cases where  $A_i$  is singular. The augment state vector is used to obtain a simplified and rapid solution without the integration. Therefore, the dynamic and input matrices are combined resulting in the following:

$$\frac{\mathrm{d}}{\mathrm{d}t} \left[ \frac{x(t)}{1} \right] = \begin{bmatrix} A_i & B_i \\ 0 & 0 \end{bmatrix} \left[ \frac{x(t)}{1} \right] \tag{18}$$

or

$$\frac{\mathrm{d}}{\mathrm{d}t}\hat{x}(t) = \hat{A}_i\hat{x}(t) \tag{19}$$

The solution for the *i*th mode is given by

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$$\hat{x}(t_i) = e^{\hat{A}_i d_i T} \hat{x}(t_{i-1}) = \hat{\phi}_i \hat{x}(t_{i-1})$$
(20)

Considering a system with m modes, the state vector for mode m, after the whole period is given by

$$\hat{x}(t_m) = \hat{\phi}_m \hat{\phi}_{m-1} \cdots \hat{\phi}_1 \hat{x}(t_0) = \hat{\phi}_{\text{tot}} \hat{x}(t_0)$$
(21)

where  $\hat{x}(t_0)$  is the initial condition.

The matrix  $\hat{\phi}_i$  can also be obtained from the combination of matrices  $\phi_i$  and  $\Gamma_i$ .

$$\hat{\phi}_i = \begin{bmatrix} \phi_i & \Gamma_i \\ 0 & 1 \end{bmatrix}$$
(22)

The periodic solution can be found assuming that after a whole cycle the state vector is equal to the initial state, as in (23)

$$\hat{x}_{per}(t_0 + T) = \hat{\phi}_{tot} \hat{x}_{per}(t_0) = \hat{x}_{per}(t_0)$$
 (23)

where

$$\hat{x}_{\text{per}}(t_0) = \left[\frac{x_{\text{per}}(t_0)}{1}\right] \tag{24}$$

Solving (23) gives

$$x_{\text{per}}(t_0) = (I^n - \phi_{\text{tot}})^{-1} \Gamma_{\text{tot}}$$
(25)

where

 $\phi_{\text{tot}} = \phi_m \phi_{m-1} \cdots \phi_1 \text{ and } \Gamma_{\text{tot}} = (\phi_m \phi_{m-1} \cdots \phi_2) \Gamma_1 + (\phi_m \phi_{m-1} \cdots \phi_3) \Gamma_2 + \cdots + \phi_m \Gamma_{m-1} + \Gamma_m$ 

Therefore, using (17), (20) and (25) the steady-state value for all state variables can be calculated for any t.

From the cyclic description obtained the average steady-state values of the state variables for a whole cycle can also be calculated. The average value definition is given by

$$x_{\rm avg} = \frac{1}{T} \int_{t_0}^{t_0 + T} x_{\rm per}(t) \mathrm{d}t$$
 (26)

Considering the system

$$\begin{cases} \dot{x}(t) = A_i x(t) + B_i \\ \dot{y}(t) = \dot{x}_{avg} = \frac{1}{T} x(t) \end{cases}$$
(27)

The augmented state vector method is used again to obtain a simplified solution without integration resulting in

$$\frac{d}{dt} \begin{bmatrix} x(t) \\ 1 \\ x_{avg}(t) \end{bmatrix} = \begin{bmatrix} A_i & B_i & 0 \\ 0 & 0 & 0 \\ I/T & 0 & 0 \end{bmatrix} \begin{bmatrix} x(t) \\ 1 \\ x_{avg}(t) \end{bmatrix}$$
(28)

or

$$\dot{z}(t) = \tilde{A}_i z(t) \tag{29}$$

The averaged state vector can then be obtained calculating  $z(t_0 + T)$  based on the initial state  $z(t_0)$ . Given the initial state

$$z(t_0) = \begin{bmatrix} x_{\text{per}}(t_0) \\ 1 \\ 0 \end{bmatrix}$$
(30)

The averaged state vector is then calculated

$$z(t_0 + T) = \tilde{\phi}_m \tilde{\phi}_{m-1} \cdots \tilde{\phi}_1 z(t_0) = \begin{bmatrix} x_{\text{per}}(t_0) \\ 1 \\ x_{\text{av}} \end{bmatrix}$$
(31)

where  $\tilde{\phi}_i = e^{\tilde{A}_i d_i T}$ 

#### 3.1 Cyclic-averaging analysis for forward operation

Here the cyclic analysis will be shown for forward operation, the same methodology is also applied to the reverse operation.

The state-space description for forward mode previously obtained from (3) to (7) is used as a base for application of the cyclic method. The set of equations can be represented in the matrix form as

$$\frac{\mathrm{d}}{\mathrm{d}t} \begin{bmatrix} i_{\mathrm{LS1}} \\ v_{\mathrm{CS1}} \\ i_{\mathrm{Lm}} \\ v_{\mathrm{Cs2'}} \\ v_{\mathrm{Cf}} \end{bmatrix} = \mathbf{A} \begin{bmatrix} i_{\mathrm{LS1}} \\ v_{\mathrm{CS1}} \\ i_{\mathrm{Lm}} \\ v_{\mathrm{Cs2'}} \\ v_{\mathrm{Cf}} \end{bmatrix} + \mathbf{B}$$
(32)

where (see (33)), and

$$\boldsymbol{B} = \begin{bmatrix} \frac{(v_a - nv_b)}{L_{s_1}} \\ 0 \\ \frac{nv_b}{L_m} \\ 0 \\ \frac{V_{\text{bat}} + r_b i_{\text{bridge}}}{C_f (r_b + r_{\text{Cf}})} \end{bmatrix}$$
(34)

The state-variable system depends on the state of voltages  $v_a$  and  $v_b$ . For a converter operating under SPS modulation there are two possible states for each voltage, leading to four operating modes. The periodic behaviour of the bridge voltages  $v_a$  and  $v_b$  for forward operation is shown in Fig. 4 and the four modes can be identified for a cycle. When operating in forward mode the right bridge  $(v_b)$  leads and the power flows from the DC bus to the battery.

At the beginning of the cycle, when the right-bridge voltage  $v_b$  becomes positive, the four modes are:

$$A = \begin{bmatrix} -\frac{(r_1 + r'_2)}{L_{s_1}} & -\frac{1}{L_{s_1}} & \frac{r'_{c_2}}{L_{s_1}} & -\frac{1}{L_{s_1}} & 0 \\ \frac{1}{C_{s_1}} & 0 & 0 & 0 & 0 \\ \frac{r'_{c_2}}{L_m} & 0 & -\frac{(r_{Lm} + r'_{c_2})}{L_m} & \frac{1}{L_m} & 0 \\ \frac{1}{C'_{s_2}} & 0 & -\frac{1}{C'_{s_2}} & 0 & 0 \\ 0 & 0 & 0 & 0 & -\frac{1}{C_f(r_b + r_{Cf})} \end{bmatrix}$$
(33)

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**Fig. 4** *Voltages at the output of right*  $(v_b)$  *and left bridge*  $(v_a)$  *for forward operation* 

Table 1 Parameter values for CLLC converter

Parameter	Value
V <sub>dc</sub>	48 V
V <sub>bat</sub>	12 V
C <sub>f</sub> /C <sub>i</sub>	300 µF
L <sub>s1</sub>	54.04 µH
C <sub>s1</sub>	31.24 nF
L <sub>m</sub>	27.02 µH
C <sub>s2</sub>	1.5 µF



Fig. 5 Average output voltage vs. phase-shift angle – forward mode

Mode 1:  $v_a = -V_{dc}$ ,  $v_b = V_{bat}$  and  $i_{bridge} = n(i_{Ls1} - i_{Lm})$ ; Mode 2:  $v_a = V_{dc}$ ,  $v_b = V_{bat}$  and  $i_{bridge} = n(i_{Ls1} - i_{Lm})$ ; Mode 3:  $v_a = V_{dc}$ ,  $v_b = -V_{bat}$  and  $i_{bridge} = -n(i_{Ls1} - i_{Lm})$ ;

*Mode 4:*  $v_a = -V_{dc}$ ,  $v_b = -V_{bat}$  and  $i_{bridge} = -n(i_{Ls1} - i_{Lm})$ ;

Based on each mode description, the substitution of the values on (32) will determine the dynamic matrices  $A_i$  and input matrices  $B_i$ , where i = 1, 2, 3, 4.

In SPS modulation, the frequency is fixed and the phase shift between the output voltage of the two bridges is a known angle between  $0^{\circ}$  and  $90^{\circ}$ . Therefore, the duration of each mode is fixed and can be calculated based on the period value and phase-shift angle. The duty cycle for the first mode is then determined as follows:

$$d_1 = \frac{PS}{360} \tag{35}$$

The remaining duty cycles are calculated based on the waveforms symmetry:  $d_2 = 0.5 - d_1$ ,  $d_3 = d_1$  and  $d_4 = d_2$ .

Once the state-space description and mode durations are obtained, the cyclic technique is used to model the converter.

From (25), (30) and (31), the steady-state average values of the state variables are calculated and to verify the model over a cycle

(20) and (25) are used to calculate the values of the state variables at the beginning of each mode (at times  $x_0, x_1, x_2$ , and  $x_3$  in Fig. 4).

#### 4 Results

To verify the models proposed, a converter was designed based on a methodology proposed in [13]. The state variable and cyclic models described in Sections 2 and 3 were simulated in Simulink and MATLAB, validation of the results being done against a component-based Spice simulation.

#### 4.1 Converter design

A 110 W, 48–12 V, CLLC converter is designed to operate at 100 kHz. Considering the DC voltage conversion ratio definition from (36), a conversion ratio close to 1 results in higher efficiency and smaller bridge currents [13]. Therefore, the chosen turns ratio is 4

$$DC_{ratio} = n \frac{V_{sec}}{V_{pri}} = n \frac{V_{bat}}{V_{dc}}$$
(36)

where *n* is the transformer turns ratio.

The resonant network is tuned to the switching frequency as in (37) [13]

$$\omega_r^2 = \frac{1}{(L_{s1} + L_m)C_{s1}} = \frac{n^2}{L_m C_{s2}} = 2\pi f_s^2$$
(37)

As a result, a base reactance value  $X_n$  can be defined

$$X_n = X_{Cs1} - X_{Ls1} = X_{Lm} = n^2 X_{Cs2}$$
(38)

The output power can be calculated based on the modulation angle as shown in (39), considering the most significant part of the power is transferred at fundamental frequency.

$$P_{\rm out} = \frac{8nV_{\rm dc}V_{\rm bat}}{n^2 X_n} \sin(\phi) \tag{39}$$

where  $\phi$  is the modulation angle and  $\phi_{\text{max}} = 90^{\circ}$  for SPS modulation.

The parameters obtained from this design procedure are listed on Table 1.

#### 4.2 Simulation results and model validation

The converter was simulated based on the design developed in the previous section considering forward and reverse operations for three cases of phase-shift angle:  $90^{\circ}$ ,  $45^{\circ}$  and  $22.5^{\circ}$ .

The resistance associated to the filter capacitor is neglected and consequently the output voltage is given by the output filter capacitor voltage,  $v_{Cf}$  for forward mode and  $v_{Ci}$  for reverse mode. The average value of the output voltage was measured for both models and compared to results from a LTspice simulation. Results for forward operation are shown in Fig. 5 and reverse operation in Fig. 6.

From Figs. 5 and 6, it can be noticed that both state-variable and cyclic-averaging models are accurate compared to Spice. For some points the LTspice result is slightly different, mainly due to the difference in precision used for the average voltage calculation in MATLAB/Simulink simulations (four decimal places) and LTspice simulations (three decimal places).

To verify the accuracy of the models during a whole cycle, the state-variables values on steady state were checked at the beginning of each mode, points  $x_0$ ,  $x_1$ ,  $x_2$ , and  $x_3$  from Figs. 4 and 6. In Tables 2 and 3, the results obtained at point  $x_0$  are shown considering forward and reverse operation.

The results obtained for the two proposed models are similar to the Spice results. Part of the error is a result of the difficulty to measure the current and voltage values for the LTspice and statevariable models at the exact point in time when Mode 1 starts.

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Fig. 6 Average output voltage vs. phase-shift angle – reverse mode

Table 2	Variables at: X for 90° phase shift – forward mode		
	Cyclic averaging	State variable	LTspice
i <sub>Ls1</sub> , A	-3.094	-3.097	-3.136
v <sub>Cs1</sub> , V	-3.782	-3.967	-3.880
<i>i</i> <sub>Lm</sub> , A	-4.566	-4.567	-4.597
v <sub>Cs2</sub> , V	-15.543	-15.510	-15.536

 Table 3
 Variables at: X for 90° phase shift – reverse mode

	Cyclic averaging	State variable	LTspice
i <sub>Ls1</sub> , A	0.490	0.506	0.490
v <sub>Cs1</sub> , V	-186.622	-186.500	-184.696
<i>i</i> <sub>Lm</sub> , A	-3.582	-3.573	-3.591
v <sub>Cs2</sub> , V	-0.935	-0.987	-0.916

 Table 4
 Comparison of execution time of proposed models

 Cyclic averaging
 State variable
 LTspice

execution time, s	0.005	44.79	25.79

In Table 4, the execution time for the three models is compared. The results obtained by cyclic-averaging method are directly steady state while state variable and Spice models need to be simulated until steady state is reached, around 7 ms for the state-variable simulation and 4 ms for Ltspice. Therefore, for comparison both state variable and spice models have a simulation time of 8 ms with 10 ns step size. The execution time when using cyclic-averaging technique is reduced compared to state variable and Spice models.

## 5 Conclusions

In this paper, state-variable and cyclic-averaging models were proposed to describe the operation of a bidirectional resonant CLLC converter. The models were developed and simulation results were verified against a Spice simulation. It was shown that both models can be used to accurately predict the behaviour of the CLLC converter and voltage and current stresses on circuit components under phase-shift modulation. Furthermore, it was confirmed that the use of cyclic averaging techniques results in a rapid analysis, with the lowest execution time between the models tested.

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